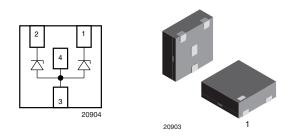
**Vishay Semiconductors** 

## Low Capacitance, 2-Line ESD-Protection Diode



**MARKING** (example only)



Dot = pin 1 marking

YY = type code (see table below)

XX = date code

#### FEATURES

- Compact LLP75-4L package
- Low package height < 0.6 mm</li>
- 2-line ESD-protection
- Low leakage current < 0.1 μA</li>
- Low load capacitance  $C_D = 1.5 \text{ pF}$
- ESD-protection acc. IEC 61000-4-2 ± 15 kV contact discharge ± 15 kV air discharge
- High surge current acc. IEC 61000-4-5 I<sub>PP</sub> > 3 A
- Soldering can be checked by standard vision inspection. No X-ray necessary
- e4 precious metal (e.g. Ag, Au, NiPd, NiPdAu) (no Sn)
- Compliant to RoHS directive 2002/95/EC and in accordance to WEEE 2002/96/EC

ORDERING INFORMATION					
DEVICE NAME	ORDERING CODE	TAPED UNITS PER REEL (8 mm TAPE ON 7" REEL)	MINIMUM ORDER QUANTITY		
VBUS052DB-HTF	VBUS052DB-HTF-GS08	3000	15 000		

PACKAGE DATA						
DEVICE NAME	PACKAGE NAME	TYPE CODE	WEIGHT	MOLDING COMPOUND FLAMMABILITY RATING	MOISTURE SENSITIVITY LEVEL	SOLDERING CONDITIONS
VBUS052DB-HTF	LLP75-4L	U7	4.2 mg	UL 94 V-0	MSL level 1 (according J-STD-020)	260 °C/10 s at terminals

ABSOLUTE MAXIMUM RATINGS						
RATING	TEST CONDITIONS SYMBOL		VALUE	UNIT		
Peak pulse current	Acc. IEC 61000-4-5, $t_p = 8/20 \ \mu s/single shot$	I <sub>PPM</sub>	3	А		
Peak pulse power	Acc. IEC 61000-4-5, $t_P = 8/20 \ \mu s/single shot$	P <sub>PP</sub>	45	W		
ESD immunity	Contact discharge acc. IEC 61000-4-2; 10 pulses		± 15	kV		
	Air discharge acc. IEC 61000-4-2; 10 pulses	V <sub>ESD</sub>	± 15	kV		
Operating temperature	Junction temperature	TJ	- 40 to + 125	°C		
Storage temperature		T <sub>STG</sub>	- 40 to + 150	°C		



<sup>\*\*</sup> Please see document "Vishay Material Category Policy": <u>www.vishay.com/doc?99902</u>

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#### **APPLICATION NOTE**

The VBUS052BD-HTF is a two-line ESD-protection device with the characteristic of a Z-diode with a high ESD-immunity and a very low capacitance which makes it usable for high frequency applications like USB2.0 or HDMI.

With the VBUS052BD-HTF two high speed data lines can be protected against transient voltage signals like ESD (electro static discharge). Connected to the data line (pin 1 and 2) and to ground (pin 3) negative transients will be clamped close below the ground level while positive transients will be clamped close above the 5 V working range. The clamping behaviour of the VBUS052BD-HTF is bidirectional but asymmetrical (BiAs) and so it offers the best protection for applications running up to 5 V.

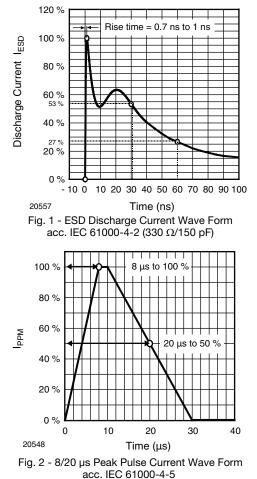
ELECTRICAL CHARACTERISTICS							
PARAMETER	TEST CONDITIONS/REMARKS	SYMBOL	MIN.	TYP.	MAX.	UNIT	
Protection paths	Number of lines which can be protected	N <sub>channel</sub>	-	-	2	lines	
Reverse working voltage	at $I_R = 0.1 \ \mu A$ ; pin 1 or pin 2 to pin 3	V <sub>RWM</sub>	5	-	-	V	
Reverse current	at $V_R = V_{RWM} = 5 V$ ; pin 1 or pin 2 to pin 3	I <sub>R</sub>	-	< 0.01	0.1	μA	
Reverse breakdown voltage	at $I_R = 1$ mA; pin 1 or pin 2 to pin 3	V <sub>BR</sub>	6.9	7.9	8.7	V	
Reverse clamping voltage	at I <sub>PP</sub> = 3 A, acc. IEC 61000-4-5; pin 1 or pin 2 to pin 3	V <sub>C</sub>	-	-	16	V	
Forward clamping voltage	at I <sub>F</sub> = 3 A, acc. IEC 61000-4-5; pin 3 to pin 1 or pin 2	V <sub>F</sub>	-	4.8	6	V	
Capacitance	at $V_R = 0$ V; f = 1 MHz; pin 1 or pin 2 to pin 3	CD	-	1.5	2.5	pF	

Note

• Ratings at 25 °C, ambient temperature unless otherwise specified.

#### **TYPICAL CHARACTERISTICS**

 $T_{amb} = 25 \text{ °C}$ , unless otherwise specified



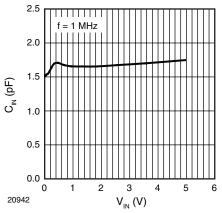


Fig. 3 - Typical Capacitance  $C_{D}\, vs.$  Reverse Voltage  $V_{R}$ 

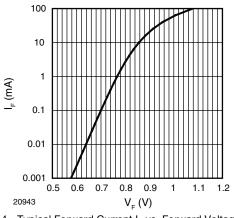


Fig. 4 - Typical Forward Current I<sub>F</sub> vs. Forward Voltage V<sub>F</sub>



## VBUS052BD-HTF

Low Capacitance, 2-Line ESD-Protection Diode **Vishay Semiconductors** 

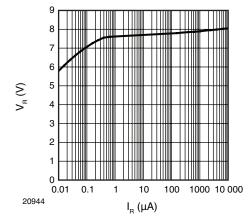
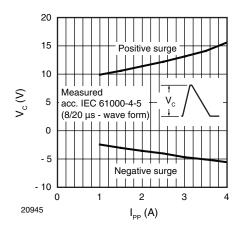
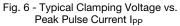


Fig. 5 - Typical Reverse Voltage  $V_R$  vs. Reverse Current  $I_R$ 





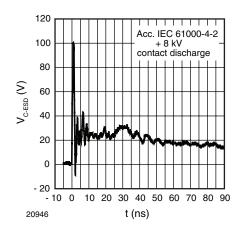


Fig. 7 - Typical Clamping Performance at + 8 kV Contact Discharge (acc. IEC 61000-4-2)

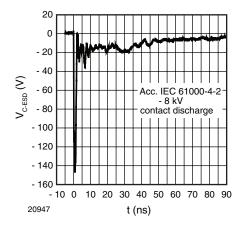


Fig. 8 - Typical Clamping Performance at - 8 kV Contact Discharge (acc. IEC 61000-4-2)

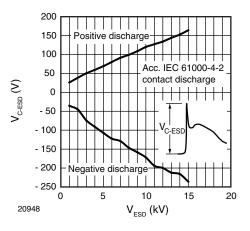


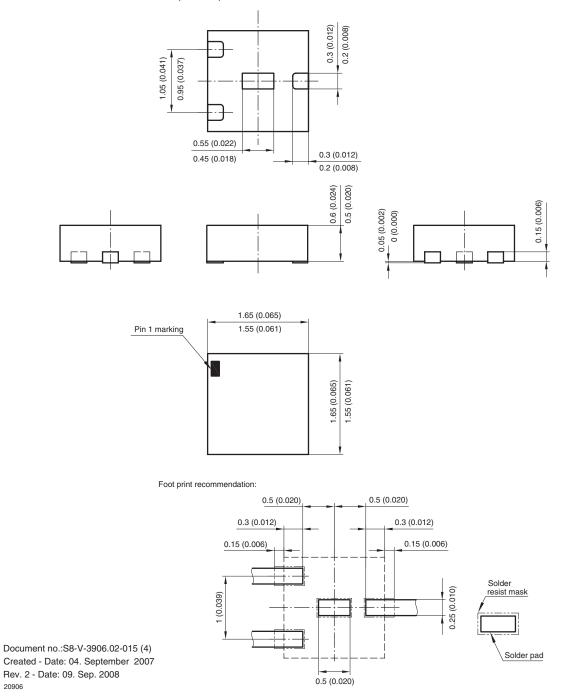
Fig. 9 - Typical Peak Clamping Voltage at ± ESD Contact Discharge (acc. IEC 61000-4-2)



### Vishay Semiconductors

Low Capacitance, 2-Line ESD-Protection Diode

#### PACKAGE DIMENSIONS in millimeters (inches): LLP75-4L





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