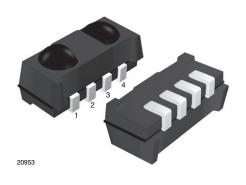
IR Receiver Modules for Remote Control Systems



www.vishay.com

MECHANICAL DATA

Pinning:

1, 4 = GND, 2 = V_S , 3 = OUT

FEATURES

- Very low supply current
- · Photo detector and preamplifier in one package
- Internal filter for PCM frequency
- Supply voltage: 2.5 V to 5.5 V
- Improved immunity against ambient light
- Capable of side or top view
- COMPLIANT Two lenses for high sensitivity and wide HALOGEN receiving angle
- Insensitive to supply voltage ripple and noise
- (5-2008) Narrow optical filter to reduce interference from plasma TV emissions
- Material categorization: For definitions of compliance please see www.vishay.com/doc?99912

DESCRIPTION

The TSOP752..., TSOP754.. series are two lens miniaturized receiver modules for infrared remote control systems. One PIN diode per lens and a preamplifier are assembled on a leadframe, the epoxy lens cap is designed as an IR filter.

The demodulated output signal can be directly connected to a digital input. The TSOP752.. is a legacy product for all common IR remote control data formats. The TSOP754.. is optimized to suppress almost all spurious pulses from energy saving fluorescent lamps. They may suppress some data signals.

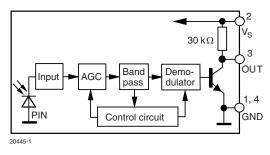
This component has not been qualified according to automotive specifications.

PARTS TABLE				
AGC		LEGACY, FOR LONG BURST REMOTE CONTROLS (AGC2)	RECOMMENDED FOR LONG BURST CODES (AGC4) ⁽¹⁾	
Carrier frequency	30 kHz	TSOP75230	TSOP75430	
	33 kHz	TSOP75233	TSOP75433	
	36 kHz	TSOP75236	TSOP75436 ⁽²⁾⁽³⁾⁽⁴⁾	
	38 kHz	TSOP75238	TSOP75438 ⁽⁵⁾⁽⁶⁾⁽⁹⁾	
	40 kHz	TSOP75240	TSOP75440	
	56 kHz	TSOP75256	TSOP75456 ⁽⁷⁾⁽⁸⁾	
Deekere	Pinning	1, 4 = GND, 2 = V _S , 3 = OUT		
Package Dimensions (mm		3.2 H x 3.0 W x 6.8 L		
Mounting		SMD		
Application		Remote control		
Best remote control code		⁽²⁾ RC-5 ⁽³⁾ RC-6 ⁽⁴⁾ Panasonic ⁽⁵⁾ NEC ⁽⁶⁾ Sharp ⁽⁷⁾ r-step ⁽⁸⁾ Thomson RCA ⁽⁹⁾ r-map		

Note

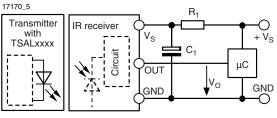
⁽¹⁾ We advise try AGC4 first if the burst length is unknown

BLOCK DIAGRAM



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APPLICATION CIRCUIT



R, and C, are recommended for protection against EOS. Components should be in the range of 33 Ω < R₁ < 1 k Ω , $C_1 > 0.1 \ \mu F$

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FREE

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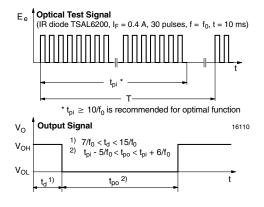
ABSOLUTE MAXIMUM RATINGS				
PARAMETER	TEST CONDITION	SYMBOL	VALUE	UNIT
Supply voltage		VS	-0.3 to +6.0	V
Supply current		I _S	3	mA
Output voltage		Vo	-0.3 to (V _S + 0.3)	V
Output current		Ι _Ο	5	mA
Junction temperature		Tj	100	°C
Storage temperature range		T _{stg}	-25 to +85	°C
Operating temperature range		T _{amb}	-25 to +85	°C
Power consumption	$T_{amb} \le 85 \ ^{\circ}C$	P _{tot}	10	mW

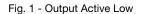
Note

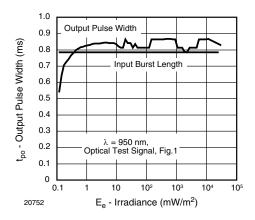
Stresses beyond those listed under "Absolute Maximum Ratings" may cause permanent damage to the device. This is a stress rating only
and functional operation of the device at these or any other conditions beyond those indicated in the operational sections of this specification
is not implied. Exposure to absolute maximum rating conditions for extended periods may affect the device reliability.

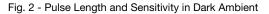
ELECTRICAL AND OPTICAL CHARACTERISTICS (T _{amb} = 25 °C, unless otherwise specified)						
PARAMETER	TEST CONDITION	SYMBOL	MIN.	TYP.	MAX.	UNIT
Supply voltage		Vs	2.5		5.5	V
Supply ourrent	$E_v = 0, V_S = 3.3 V$	I _{SD}	0.27	0.35	0.45	mA
Supply current	$E_v = 40$ klx, sunlight	I _{SH}		0.45		mA
Transmission distance	E _v = 0, test signal see fig. 1, IR diode TSAL6200, I _F = 200 mA	d		45		m
Output voltage low	$I_{OSL} = 0.5 \text{ mA}, E_e = 0.7 \text{ mW/m}^2,$ test signal see fig. 1	V _{OSL}			100	mV
Minimum irradiance	Pulse width tolerance: t _{pi} - 5/f _o < t _{po} < t _{pi} + 6/f _o , test signal see fig. 1	E _{e min.}		0.12	0.25	mW/m ²
Maximum irradiance	t _{pi} - 5/f _o < t _{po} < t _{pi} + 6/f _o , test signal see fig. 1	E _{e max.}	30			W/m ²
Directivity	Angle of half transmission distance	φ1/2		± 50		deg

TYPICAL CHARACTERISTICS (T_{amb} = 25 °C, unless otherwise specified)











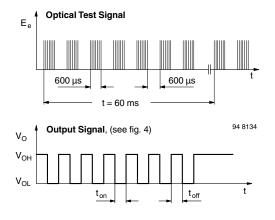


Fig. 3 - Output Function

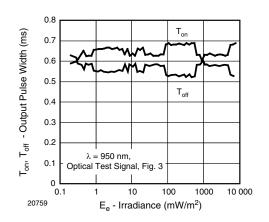


Fig. 4 - Output Pulse Diagram

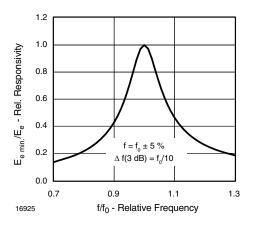


Fig. 5 - Frequency Dependence of Responsivity

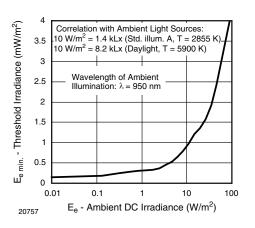


Fig. 6 - Sensitivity in Bright Ambient

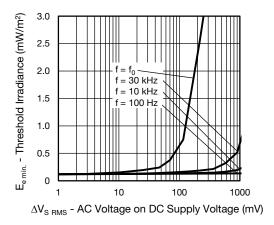


Fig. 7 - Sensitivity vs. Supply Voltage Disturbances

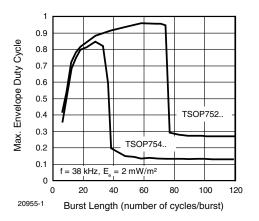


Fig. 8 - Max. Envelope Duty Cycle vs. Burst Length

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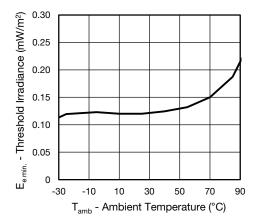


Fig. 9 - Sensitivity vs. Ambient Temperature

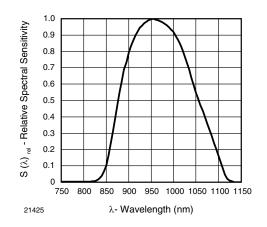


Fig. 10 - Relative Spectral Sensitivity vs. Wavelength

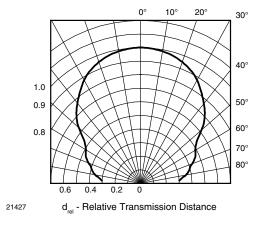


Fig. 11 - Horizontal Directivity

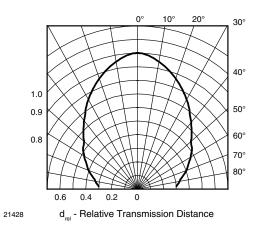


Fig. 12 - Vertical Directivity

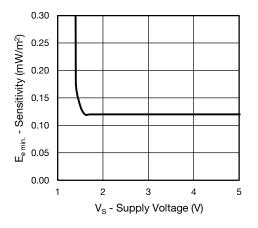


Fig. 13 - Sensitivity vs. Supply Voltage

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4



SUITABLE DATA FORMAT

This series is designed to suppress spurious output pulses due to noise or disturbance signals. The devices can distinguish data signals from noise due to differences in frequency, burst length, and envelope duty cycle. The data signal should be close to the device's band-pass center frequency (e.g. 38 kHz) and fulfill the conditions in the table below.

When a data signal is applied to the product in the presence of a disturbance, the sensitivity of the receiver is automatically reduced by the AGC to insure that no spurious pulses are present at the receiver's output.

Some examples which are suppressed are:

- DC light (e.g. from tungsten bulbs sunlight)
- Continuous signals at any frequency
- Strongly or weakly modulated patterns from fluorescent lamps with electronic ballasts (see fig. 14 or fig. 15)

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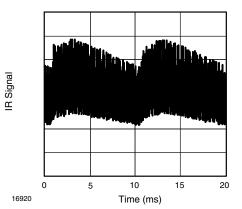


Fig. 14 - IR Disturbance from Fluorescent Lamp with Low Modulation

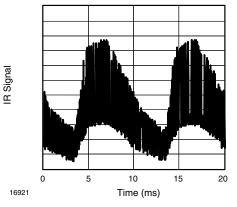


Fig. 15 - IR Disturbance from Fluorescent Lamp with High Modulation

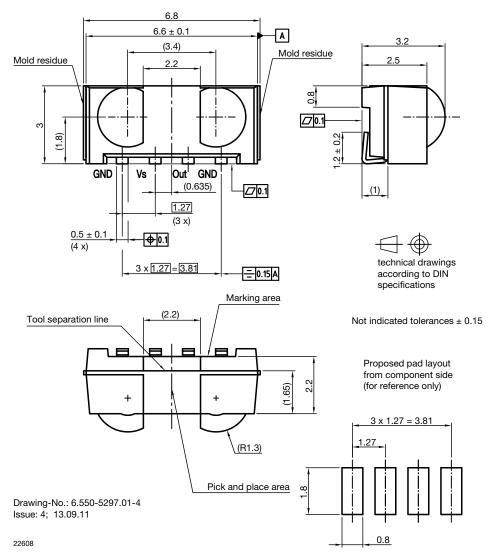
	TSOP752	TSOP754
Minimum burst length	10 cycles/burst	10 cycles/burst
After each burst of length a minimum gap time is required of	10 to 70 cycles ≥ 10 cycles	10 to 35 cycles ≥ 10 cycles
For bursts greater than a minimum gap time in the data stream is needed of	70 cycles > 4 x burst length	35 cycles > 10 x burst length
Maximum number of continuous short bursts/second	1800	1500
Recommended for NEC code	yes	yes
Recommended for RC5/RC6 code	yes	yes
Recommended for Thomson 56 kHz code	yes	yes
Recommended for Mitsubishi code (38 kHz, preburst 8 ms, 16 bit)	yes	no
Recommended for Sharp code	yes	yes
Suppression of interference from fluorescent lamps	Most common disturbance patterns are suppressed	Even extreme disturbance patterns are suppressed

Notes

- For data formats with short bursts please see the datasheet for TSOP753.., TSOP755..
- Example of compatible products for IR-codes:
- TSOP75436: RC-5. RC-6. Panasonic
- TSOP75438: NEC, Sharp, r-map
- TSOP75456: r-step, Thomson RCA
- For SIRCS 15 and 20 bit, Sony 12 bit IR-codes, please see the datasheet for TSOP75S40F



PACKAGE DIMENSIONS in millimeters



ASSEMBLY INSTRUCTIONS

Reflow Soldering

- Reflow soldering must be done within 72 h while stored under a max. temperature of 30 °C, 60 % RH after opening the dry pack envelope
- Set the furnace temperatures for pre-heating and heating in accordance with the reflow temperature profile as shown in the diagram. Excercise extreme care to keep the maximum temperature below 260 °C. The temperature shown in the profile means the temperature at the device surface. Since there is a temperature difference between the component and the circuit board, it should be verified that the temperature of the device is accurately being measured
- Handling after reflow should be done only after the work surface has been cooled off

Manual Soldering

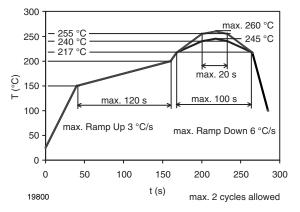
- Use a soldering iron of 25 W or less. Adjust the temperature of the soldering iron below 300 °C
- Finish soldering within 3 s
- · Handle products only after the temperature has cooled off



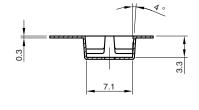
TSOP752.., TSOP754..

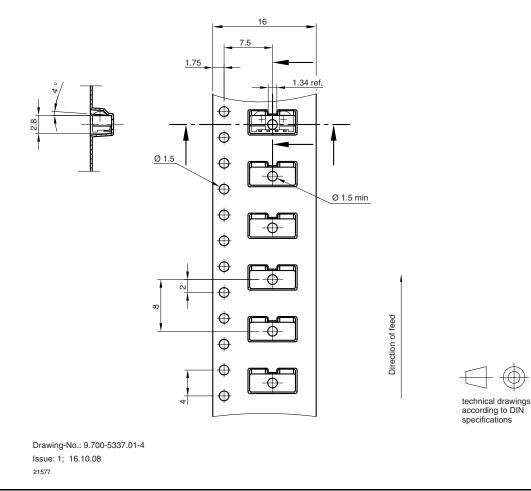
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VISHAY LEAD (Pb)-FREE REFLOW SOLDER PROFILE



TAPING VERSION TSOP..TR DIMENSIONS in millimeters



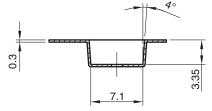


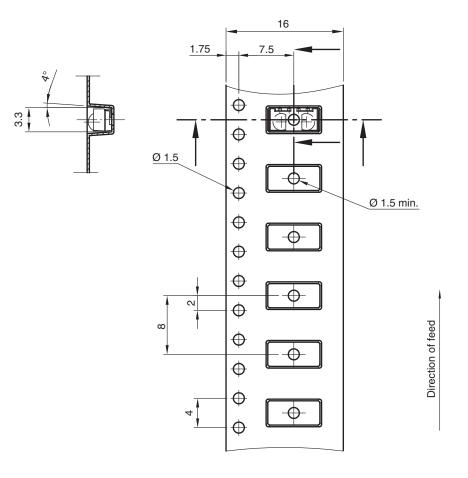
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Document Number: 82494



TAPING VERSION TSOP..TT DIMENSIONS in millimeters





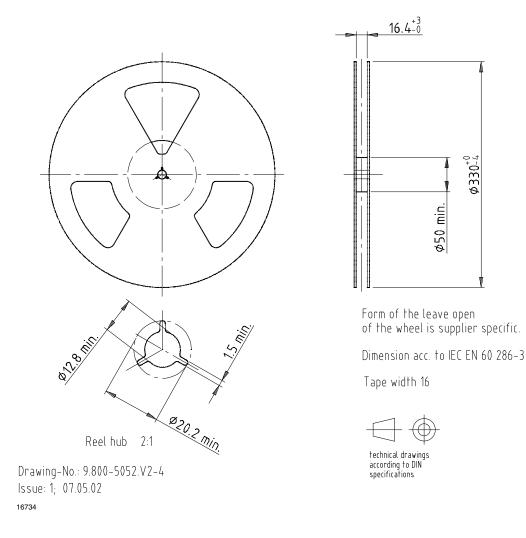


technical drawings according to DIN specifications

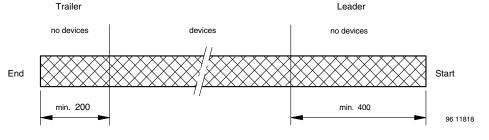
Drawing-No.: 9.700-5338.01-4 Issue: 3; 09.06.09 21578



REEL DIMENSIONS in millimeters



LEADER AND TRAILER DIMENSIONS in millimeters



COVER TAPE PEEL STRENGTH

According to DIN EN 60286-3 0.1 N to 1.3 N 300 ± 10 mm/min. 165° to 180° peel angle

LABEL

Standard bar code labels for finished goods

The standard bar code labels are product labels and used for identification of goods. The finished goods are packed in final packing area. The standard packing units are labeled with standard bar code labels before transported as finished goods to warehouses. The labels are on each packing unit and contain Vishay Semiconductor GmbH specific data.

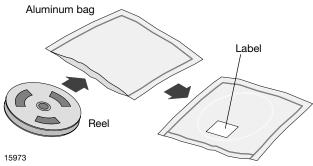
9



VISHAY SEMICONDUCTOR GmbH STANDARD BAR CODE PRODUCT LABEL (finished goods)			
PLAIN WRITING	ABBREVIATION	LENGTH	
Item-description	-	18	
Item-number	INO	8	
Selection-code	SEL	3	
LOT-/serial-number	BATCH	10	
Data-code	COD	3 (YWW)	
Plant-code	PTC	2	
Quantity	QTY	8	
Accepted by	ACC	-	
Packed by	РСК	-	
Mixed code indicator	MIXED CODE	-	
Origin	XXXXXX+	Company logo	
Long bar code top	Туре	Length	
Item-number	Ν	8	
Plant-code	Ν	2	
Sequence-number	Х	3	
Quantity	Ν	8	
Total length	-	21	
Short bar code bottom	Туре	Length	
Selection-code	Х	3	
Data-code	Ν	3	
Batch-number	Х	10	
Filter	-	1	
Total length	-	17	

DRY PACKING

The reel is packed in an anti-humidity bag to protect the devices from absorbing moisture during transportation and storage.



FINAL PACKING

The sealed reel is packed into a cardboard box. A secondary cardboard box is used for shipping purposes.

RECOMMENDED METHOD OF STORAGE

Dry box storage is recommended as soon as the aluminum bag has been opened to prevent moisture absorption. The following conditions should be observed, if dry boxes are not available:

- Storage temperature 10 °C to 30 °C
- Storage humidity ≤ 60 % RH max.

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After more than 72 h under these conditions moisture content will be too high for reflow soldering.

In case of moisture absorption, the devices will recover to the former condition by drying under the following condition: 192 h at 40 °C + 5 °C / - 0 °C and < 5 % RH (dry air/nitrogen) or

96 h at 60 °C + 5 °C and < 5 % RH for all device containers or

24 h at 125 °C + 5 °C not suitable for reel or tubes.

An EIA JEDEC® standard J-STD-020 level 4 label is included on all dry bags.



EIA JEDEC standard J-STD-020 level 4 label is included on all dry bags



ESD PRECAUTION

Proper storage and handling procedures should be followed to prevent ESD damage to the devices especially when they are removed from the antistatic shielding bag. Electro-static sensitive devices warning labels are on the packaging.

VISHAY SEMICONDUCTORS STANDARD BAR CODE LABELS

The Vishay Semiconductors standard bar code labels are printed at final packing areas. The labels are on each packing unit and contain Vishay Semiconductors specific data.

PartNo: TSOP75236TT	Lot: F0033958A)
01Y: 2200	Lot: 1910/3301 AC VISHAY
Selfcode:000	Region: 2110 SL: 0010
Orgin PHIL PPINES Machine: 12	Senate: WM08050160
>TC: 19	Operator 327 C & 0 F8 Rel Pt-(Rt)

22178



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Please note that some Vishay documentation may still make reference to RoHS Directive 2002/95/EC. We confirm that all the products identified as being compliant to Directive 2002/95/EC conform to Directive 2011/65/EU.

Vishay Intertechnology, Inc. hereby certifies that all its products that are identified as Halogen-Free follow Halogen-Free requirements as per JEDEC JS709A standards. Please note that some Vishay documentation may still make reference to the IEC 61249-2-21 definition. We confirm that all the products identified as being compliant to IEC 61249-2-21 conform to JEDEC JS709A standards.