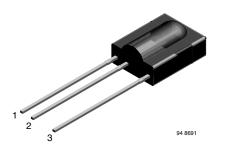


Vishay Semiconductors

## **IR Receiver Modules for Remote Control Systems**



## **MECHANICAL DATA**

**Pinning:** 1 = GND, 2 = V<sub>S</sub>, 3 = OUT

### **FEATURES**

- Very low supply current
- · Photo detector and preamplifier in one package
- Internal filter for PCM frequency
- Improved shielding against EMI
- Supply voltage: 2.5 V to 5.5 V
- · Improved immunity against ambient light
- Insensitive to supply voltage ripple and noise
- Material categorization: For definitions of compliance please see <u>www.vishay.com/doc?99912</u>

### DESCRIPTION

The TSOP311.., TSOP313.., and TSOP315.. series are miniaturized receivers for infrared remote control systems. A PIN diode and a preamplifier are assembled on a lead frame, the epoxy package acts as an IR filter.

The demodulated output signal can be directly decoded by a microprocessor. The TSOP311.. is a legacy product compatible with all common IR remote control data formats. The TSOP313.. is optimized to better suppress spurious pulses from energy saving fluorescent lamps. The TSOP315.. has an excellent noise suppression. It is immune to dimmed LCD backlighting and any fluorescent lamps. AGC3 and AGC5 may also suppress some data signals in case of continuous transmission.

This component has not been qualified according to automotive specifications.

PARTS TABLE					
AGC		LEGACY, FOR SHORT BURST REMOTE CONTROLS (AGC1)	NOISY ENVIRONMENTS AND SHORT BURSTS (AGC3)	VERY NOISY ENVIRONMENTS AND SHORT BURSTS (AGC5)	
Carrier frequency	30 kHz	TSOP31130	TSOP31330	TSOP31530	
	33 kHz	TSOP31133	TSOP31333	TSOP31533	
	36 kHz	TSOP31136	TSOP31336	TSOP31536	
	38 kHz	TSOP31138	TSOP31338	TSOP31538	
	40 kHz	TSOP31140	TSOP31340	TSOP31540	
	56 kHz	TSOP31156	TSOP31356	TSOP31556	
Package	Pinning	1 = GND, 2 = V <sub>S</sub> , 3 = OUT	1 = GND, 2 = V <sub>S</sub> , 3 = OUT	1 = GND, 2 = V <sub>S</sub> , 3 = OUT	
	Dimensions (mm)	12.5 H x 5.8 W x 10.0 L			
Mounting		Leaded			
Application		Remote control			

APPLICATION CIRCUIT

IR receiver

Circuit

R, and C, are recommended for protection against EOS.

Components should be in the range of 33  $\Omega$  < R<sub>1</sub> < 1 k $\Omega$ ,

٧٩

ΌΠΙ

GND

C.

Vo

17170 5

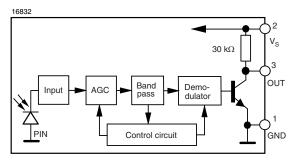
Transmitter

with

TSALxxxx

 $C_1 > 0.1 \, \mu F_2$ 

### **BLOCK DIAGRAM**



### Rev. 1.1, 18-Sep-13

1

μC

. V<sub>S</sub>

GND

THIS DOCUMENT IS SUBJECT TO CHANGE WITHOUT NOTICE. THE PRODUCTS DESCRIBED HEREIN AND THIS DOCUMENT ARE SUBJECT TO SPECIFIC DISCLAIMERS, SET FORTH AT www.vishay.com/doc?91000



COMPLIANT

HALOGEN

GREEN

(5-2008)



**Vishay Semiconductors** 

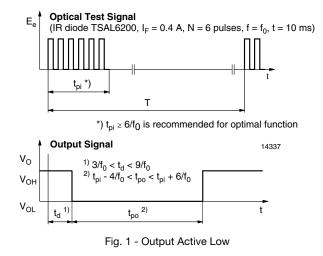
ABSOLUTE MAXIMUM RATINGS					
PARAMETER	TEST CONDITION	SYMBOL	VALUE	UNIT	
Supply voltage (pin 2)		Vs	-0.3 to +6	V	
Supply current (pin 2)		I <sub>S</sub>	3	mA	
Output voltage (pin 3)		Vo	-0.3 to (V <sub>S</sub> + 0.3)	V	
Output current (pin 3)		Ιο	5	mA	
Junction temperature		Тj	100	°C	
Storage temperature range		T <sub>stg</sub>	-25 to +85	°C	
Operating temperature range		T <sub>amb</sub>	-25 to +85	°C	
Power consumption	T <sub>amb</sub> ≤ 85 °C	P <sub>tot</sub>	10	mW	
Soldering temperature	$t \le 10$ s, 1 mm from case	T <sub>sd</sub>	260	°C	

#### Note

• Stresses beyond those listed under "Absolute Maximum Ratings" may cause permanent damage to the device. This is a stress rating only and functional operation of the device at these or any other conditions beyond those indicated in the operational sections of this specification is not implied. Exposure to absolute maximum rating conditions for extended periods may affect the device reliability.

ELECTRICAL AND OPTICAL CHARACTERISTICS (T <sub>amb</sub> = 25 °C, unless otherwise specified)						
PARAMETER	TEST CONDITION	SYMBOL M	MIN.	MIN. TYP.	MAX.	UNIT
Supply current (pin 2)	$E_v = 0, V_S = 3.3 V$	I <sub>SD</sub>	0.27	0.35	0.45	mA
Supply current (pin 2)	$E_v = 40$ klx, sunlight	I <sub>SH</sub>		0.45		mA
Supply voltage		VS	2.5		5.5	V
Transmission distance	$E_v = 0$ , test signal see fig. 1, IR diode TSAL6200, $I_F = 200 \text{ mA}$	d		45		m
Output voltage low (pin 3)	$I_{OSL} = 0.5 \text{ mA}, E_e = 0.7 \text{ mW/m}^2,$ test signal see fig. 1	V <sub>OSL</sub>			100	mV
Minimum irradiance	Pulse width tolerance: t <sub>pi</sub> - 5/f <sub>o</sub> < t <sub>po</sub> < t <sub>pi</sub> + 6/f <sub>o</sub> , test signal see fig. 1	E <sub>e min.</sub>		0.12	0.25	mW/m <sup>2</sup>
Maximum irradiance	$\begin{array}{c} t_{pi} \text{ - } 5/f_{o} < t_{po} < t_{pi} + 6/f_{o}, \\ \text{test signal see fig. 1} \end{array}$	E <sub>e max.</sub>	30			W/m <sup>2</sup>
Directivity	Angle of half transmission distance	φ1/2		± 45		deg

### TYPICAL CHARACTERISTICS (Tamb = 25 °C, unless otherwise specified)



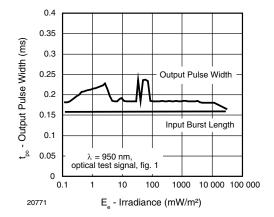
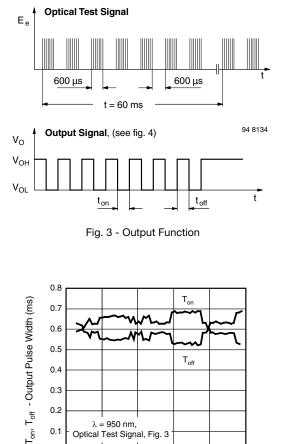


Fig. 2 - Pulse Length and Sensitivity in Dark Ambient

Rev. 1.1, 18-Sep-13

2





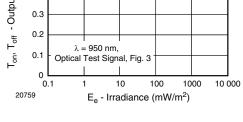


Fig. 4 - Output Pulse Diagram

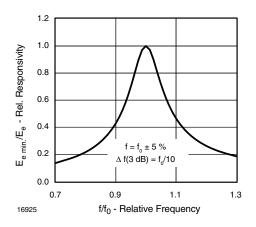


Fig. 5 - Frequency Dependence of Responsivity

## **Vishay Semiconductors**

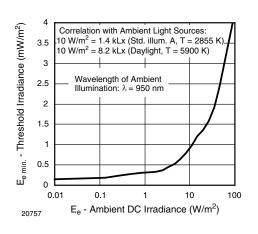


Fig. 6 - Sensitivity in Bright Ambient

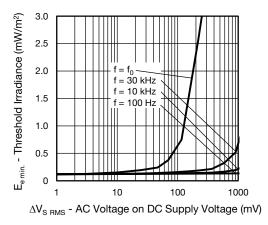


Fig. 7 - Sensitivity vs. Supply Voltage Disturbances

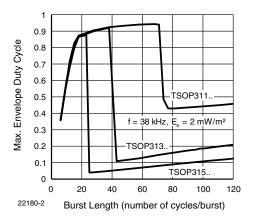


Fig. 8 - Max. Envelope Duty Cycle vs. Burst Length

Rev. 1.1, 18-Sep-13



### 0.30 $E_{e\,min.}$ - Threshold Irradiance (mW/m<sup>2</sup>) 0.25 0.20 0.15 0.10 0.05 0 -30 90 -10 10 30 50 70 T<sub>amb</sub> - Ambient Temperature (°C)

Fig. 9 - Sensitivity vs. Ambient Temperature

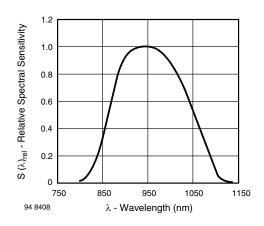


Fig. 10 - Relative Spectral Sensitivity vs. Wavelength

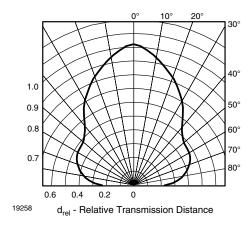


Fig. 11 - Horizontal Directivity

# TSOP311.., TSOP313.., TSOP315..

Vishay Semiconductors

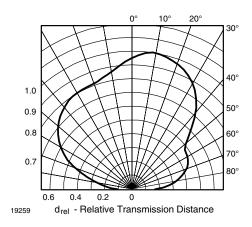


Fig. 12 - Vertical Directivity

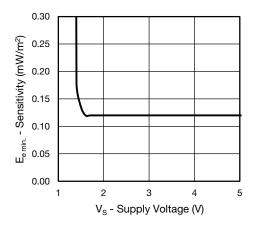


Fig. 13 - Sensitivity vs. Supply Voltage

4

**Vishay Semiconductors** 



### SUITABLE DATA FORMAT

This series is designed to suppress spurious output pulses due to noise or disturbance signals. The devices can distinguish data signals from noise due to differences in frequency, burst length, and envelope duty cycle. The data signal should be close to the device's band-pass center frequency (e.g. 38 kHz) and fulfill the conditions in the table below.

When a data signal is applied to the product in the presence of a disturbance, the sensitivity of the receiver is automatically reduced by the AGC to insure that no spurious pulses are present at the receiver's output. Some examples which are suppressed are:

- DC light (e.g. from tungsten bulbs sunlight)
- · Continuous signals at any frequency
- Strongly or weakly modulated patterns from fluorescent lamps with electronic ballasts (see figure 14 or figure 15).

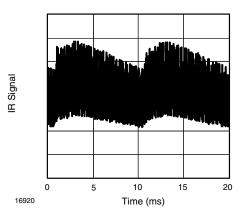


Fig. 14 - IR Disturbance from Fluorescent Lamp with Low Modulation

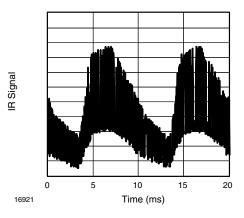


Fig. 15 - IR Disturbance from Fluorescent Lamp with High Modulation

	TSOP311	TSOP313	TSOP315	
Minimum burst length	6 cycles/burst	6 cycles/burst	6 cycles/burst	
After each burst of length A gap time is required of	6 to 70 cycles ≥ 10 cycles	6 to 35 cycles ≥ 10 cycles	6 to 24 cycles ≥ 10 cycles	
For bursts greater than a minimum gap time in the data stream is needed of	70 cycles > 1.2 x burst length	35 cycles > 6 x burst length	24 cycles > 25 ms	
Maximum number of continuous short bursts/second	2000	2000	2000	
Recommended for NEC code	yes	yes	yes	
Recommended for RC5/RC6 code	yes	yes	yes	
Recommended for RCMM code	yes	yes	yes	
Recommended for r-step code	yes	yes	yes	
Recommended for XMP code	yes	yes	yes	
Suppression of interference from fluorescent lamps	Common disturbance patterns are supressed (example: signal pattern of fig. 14)	Even critical disturbance patterns are suppressed (examples: signal pattern of fig. 14 and fig. 15)	Even critical disturbance patterns are suppressed (examples: signal pattern of fig. 14 and fig. 15)	

### Notes

• For data formats with long bursts (more than 10 carrier cycles) please see the datasheet for TSOP312.., TSOP314..

Example of compatible products for IR-codes:

-TSOP31336, TSOP31536: MCIR, RCMM

-TSOP31338: Mitsubishi, RECS-80 Code, r-map, XMP-1, XMP-2

-TSOP31358: Mitsubishi, RECS-80 Code, r-map

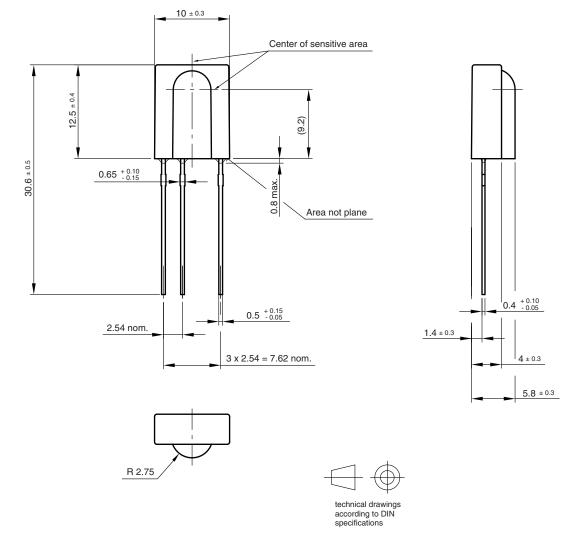
• For SIRCS 15 and 20 bit, Sony 12 bit IR-codes, please see the datasheet for TSOP4S40, TSOP2S40

Rev. 1.1, 18-Sep-13



Vishay Semiconductors

## **PACKAGE DIMENSIONS** in millimeters



Drawing-No.: 6.550-5095.01-4 Issue: 20; 15.03.10 96 12116



Vishay

# Disclaimer

ALL PRODUCT, PRODUCT SPECIFICATIONS AND DATA ARE SUBJECT TO CHANGE WITHOUT NOTICE TO IMPROVE RELIABILITY, FUNCTION OR DESIGN OR OTHERWISE.

Vishay Intertechnology, Inc., its affiliates, agents, and employees, and all persons acting on its or their behalf (collectively, "Vishay"), disclaim any and all liability for any errors, inaccuracies or incompleteness contained in any datasheet or in any other disclosure relating to any product.

Vishay makes no warranty, representation or guarantee regarding the suitability of the products for any particular purpose or the continuing production of any product. To the maximum extent permitted by applicable law, Vishay disclaims (i) any and all liability arising out of the application or use of any product, (ii) any and all liability, including without limitation special, consequential or incidental damages, and (iii) any and all implied warranties, including warranties of fitness for particular purpose, non-infringement and merchantability.

Statements regarding the suitability of products for certain types of applications are based on Vishay's knowledge of typical requirements that are often placed on Vishay products in generic applications. Such statements are not binding statements about the suitability of products for a particular application. It is the customer's responsibility to validate that a particular product with the properties described in the product specification is suitable for use in a particular application. Parameters provided in datasheets and/or specifications may vary in different applications and performance may vary over time. All operating parameters, including typical parameters, must be validated for each customer application by the customer's technical experts. Product specifications do not expand or otherwise modify Vishay's terms and conditions of purchase, including but not limited to the warranty expressed therein.

Except as expressly indicated in writing, Vishay products are not designed for use in medical, life-saving, or life-sustaining applications or for any other application in which the failure of the Vishay product could result in personal injury or death. Customers using or selling Vishay products not expressly indicated for use in such applications do so at their own risk. Please contact authorized Vishay personnel to obtain written terms and conditions regarding products designed for such applications.

No license, express or implied, by estoppel or otherwise, to any intellectual property rights is granted by this document or by any conduct of Vishay. Product names and markings noted herein may be trademarks of their respective owners.

# **Material Category Policy**

Vishay Intertechnology, Inc. hereby certifies that all its products that are identified as RoHS-Compliant fulfill the definitions and restrictions defined under Directive 2011/65/EU of The European Parliament and of the Council of June 8, 2011 on the restriction of the use of certain hazardous substances in electrical and electronic equipment (EEE) - recast, unless otherwise specified as non-compliant.

Please note that some Vishay documentation may still make reference to RoHS Directive 2002/95/EC. We confirm that all the products identified as being compliant to Directive 2002/95/EC conform to Directive 2011/65/EU.

Vishay Intertechnology, Inc. hereby certifies that all its products that are identified as Halogen-Free follow Halogen-Free requirements as per JEDEC JS709A standards. Please note that some Vishay documentation may still make reference to the IEC 61249-2-21 definition. We confirm that all the products identified as being compliant to IEC 61249-2-21 conform to JEDEC JS709A standards.