

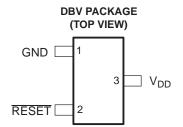
TPS3809L30-EP, TPS3809K33-EP, TPS3809I50-EP 3-PIN SUPPLY VOLTAGE SUPERVISORS

SGLS369A-AUGUST 2006-REVISED NOVEMBER 2006

FEATURES

- Controlled Baseline
 - One Assembly Site
 - One Test Site
 - One Fabrication Site
- Extended Temperature Performance of -55°C to 125°C
- Enhanced Diminishing Manufacturing Sources (DMS) Support
- Enhanced Product-Change Notification
- Qualification Pedigree (1)
- 3-Pin SOT-23 Package
- Supply Current of 9 μA (Typical)
- (1) Component qualification in accordance with JEDEC and industry standards to ensure reliable operation over an extended temperature range. This includes, but is not limited to, Highly Accelerated Stress Test (HAST) or biased 85/85, temperature cycle, autoclave or unbiased HAST, electromigration, bond intermetallic life, and mold compound life. Such qualification testing should not be viewed as justifying use of this component beyond specified performance and environmental limits.

- Precision Supply Voltage Monitor
 2.5 V, 3 V, 3.3 V, 5 V
- Power-On Reset Generator With Fixed Delay Time of 200 ms
- Pin-for-Pin Compatible With MAX 809



DESCRIPTION

The TPS3809 family of supervisory circuits provides circuit initialization and timing supervision, primarily for DSPs and processor-based systems.

During power-on, $\overline{\text{RESET}}$ is asserted when the supply voltage V_{DD} becomes higher than 1.1 V. Thereafter, the supervisory circuit monitors V_{DD} and keeps $\overline{\text{RESET}}$ active as long as V_{DD} remains below the threshold voltage V_{IT} . An internal timer delays the return of the output to the inactive state (high) to ensure proper system reset. The delay time, $t_{d(typ)} = 200$ ms, starts after V_{DD} has risen above the V_{IT} . When the supply voltage drops below the V_{IT} , the output becomes active (low) again. No external components are required. All the devices of this family have a fixed-sense V_{IT} set by an internal voltage divider.

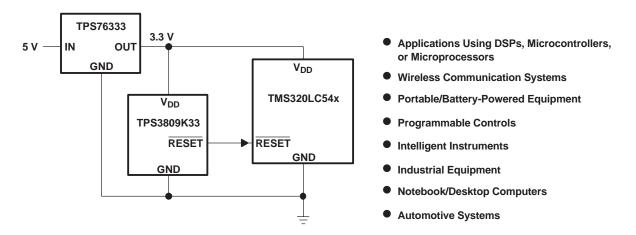
The product spectrum is designed for supply voltages of 2.5 V, 3 V, 3.3 V, and 5 V. The circuits are available in a 3-pin SOT-23 package. The TPS3809 devices are characterized for operation over a temperature range of –55°C to 125°C.



Please be aware that an important notice concerning availability, standard warranty, and use in critical applications of Texas Instruments semiconductor products and disclaimers thereto appears at the end of this data sheet.



TYPICAL APPLICATIONS



AVAILABLE OPTIONS

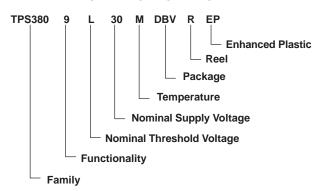
T _A	DEVICE NAME	THRESHOLD VOLTAGE	MARKING
	TPS3809L30MDBVREP(1)	2.64 V	PLYM
–55°C to 125°C	TPS3809K33MDBVREP ⁽¹⁾	2.93 V	PLZM
	TPS3809I50MDBVREP ⁽¹⁾	4.55 V	PMAM

(1) The DBVR passive indicates tape and reel of 3000 parts.

FUNCTION/TRUTH TABLE

V _{DD} > V _{IT}	RESET
0	L
1	Н

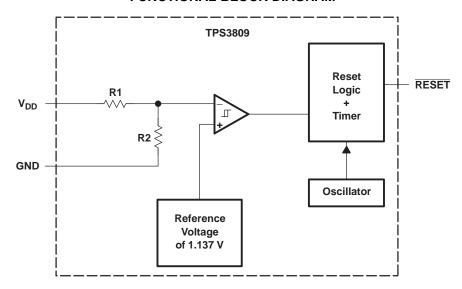
ORDERING INFORMATION



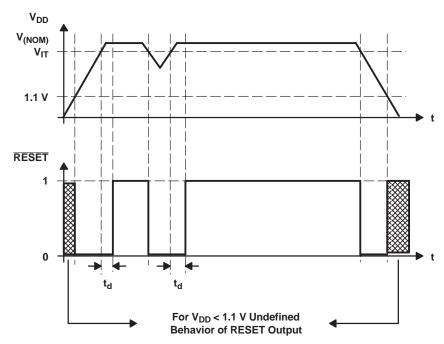


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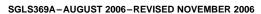
FUNCTIONAL BLOCK DIAGRAM



TIMING DIAGRAM



TPS3809L30-EP, TPS3809K33-EP, TPS3809I50-EP 3-PIN SUPPLY VOLTAGE SUPERVISORS





Absolute Maximum Ratings(1)

over operating free-air temperature range (unless otherwise noted)

			MIN	MAX	UNIT
	Supply voltage (2)	V_{DD}		7	V
	Supply voltage ⁽²⁾	All other pins	-0.3	7	V
I_{OL}	Maximum low output current			5	mA
I _{OH}	Maximum high output current			-5	mA
I_{IK}	Input clamp current	$V_I < 0 \text{ or } V_I > V_{DD}$		±20	mA
I_{OK}	Output clamp current	$V_O < 0$ or $V_O > V_{DD}$		±20	mA
	Continuous total power dissipation		See Dis	ssipation	Rating Table
T_A	Operating free-air temperature range		-55	125	°C
T _{stg}	Storage temperature range			150	°C
	Soldering temperature			260	°C

⁽¹⁾ Stresses beyond those listed under "absolute maximum ratings" may cause permanent damage to the device. These are stress ratings only, and functional operation of the device at these or any other conditions beyond those indicated under "recommended operating conditions" is not implied. Exposure to absolute-maximum-rated conditions for extended periods may affect device reliability.

Dissipation Ratings

PACKAGE	T _A < 25°C	DERATING FACTOR	T _A = 70°C	T _A = 85°C
	POWER RATING	ABOVE T _A = 25°C	POWER RATING	POWER RATING
DBV	437 mW	3.5 mW/°C	280 mW	227 mW

Recommended Operating Conditions

		MIN	MAX	UNIT
V_{DD}	Supply voltage	2	6	V
T _A	Operating free-air temperature	-55	125	°C

⁽²⁾ All voltage values are with respect to GND. For reliable operation the device should not be operated at 7 V for more than t = 1000h continuously.

TPS3809L30-EP, TPS3809K33-EP, TPS3809I50-EP 3-PIN SUPPLY VOLTAGE SUPERVISORS

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Electrical Characteristics

over recommended operating free-air temperature range (unless otherwise noted)

	PARAMETE	R	-	TEST CONDITIONS	3	MIN	TY P	MAX	UNIT		
			$V_{DD} = 2.5 \text{ V to 6 V},$	$I_{OH} = -500 \mu A$		V _{DD} - 0.2					
.,	High lovel output ve	ltogo	$V_{DD} = 3.3 \text{ V},$	$I_{OH} = -2 \text{ mA}$		V _{DD} - 0.4			V		
V _{OH}	High-level output vo	mage	V _{DD} = 6 V,	$I_{OH} = -4 \text{ mA}$		V _{DD} - 0.4			V		
			$V_{DD} = 6 V$,	$I_{OH} = -4 \text{ mA},$	T _A = 125°C	$V_{DD} - 0.5$					
			$V_{DD} = 2 V \text{ to } 6 V,$	$I_{OL} = 500 \mu A$				0.2			
V_{OL}	Low-level output vo	ltage	$V_{DD} = 3.3 V,$	$I_{OL} = 2 \text{ mA}$				0.4	V		
			$V_{DD} = 6 V$,	$I_{OL} = 4 \text{ mA}$				0.4			
	Power-up reset volt	age ⁽¹⁾	$V_{DD} \ge 1.1 V$,	$I_{OL} = 50 \mu A$				0.2	V		
		TPS3809L30				2.58	2.6 4	2.7			
V _{IT-}	Negative-going input threshold voltage (2)	TPS3809K33				2.87	2.9	2.99	V		
	vollago	TPS3809I50				4.45	4.5 5	4.65			
		TPS3809L30					35				
V_{hys}	Hysteresis	TPS3809K33					40		mV		
		TPS3809I50					60				
	Supply current		V _{DD} = 2 V,	Output unconnect	ted		9	12	^		
I _{DD}	Supply current		$V_{DD} = 6 V$,	Output unconnect	ted	·	20	25	μΑ		
C_{i}	Input capacitance		$V_I = 0 V \text{ to } V_{DD}$				5		pF		

Timing Requirements

 R_L = 1 M Ω , C_L = 50 pF, T_A = 25°C

	PARAMETER	TEST CONDITIONS	MIN	MAX	UNIT
t_{w}	Pulse width at V _{DD}	$V_{DD} = V_{IT-} + 0.2 \text{ V}, V_{DD} = V_{IT-} - 0.2 \text{ V}$	3		μs

Switching Characteristics

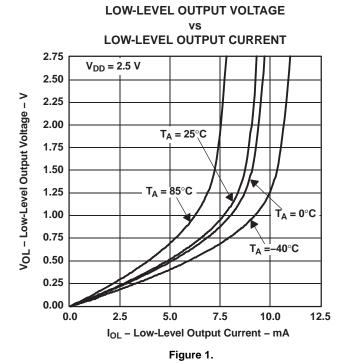
 $R_L = 1 \text{ M}\Omega$, $C_L = 50 \text{ pF}$, $T_A = 25^{\circ}\text{C}$

	PARAMETE	≣R	TEST CONDITIONS	MIN	TYP	MAX	UNIT
t _d	Delay time	$V_{DD} \ge V_{IT-} + 0.2 \text{ V},$ See timing diagram	120	200	280	ms	
t _{PHL}	Propagation (delay) time, high- to low-level output	V _{DD} to RESET delay	$V_{IL} = V_{IT-} - 0.2 \text{ V},$ $V_{IH} = V_{IT-} + 0.2 \text{ V}$		1		ms

 ⁽¹⁾ The lowest supply voltage at which RESET becomes active. t_r, V_{DD} ≥ 15 μs/V.
 (2) To ensure best stability of the threshold voltage, a bypass capacitor (0.1-μF ceramic) should be placed near the supply terminals.



TYPICAL CHARACTERISTICS





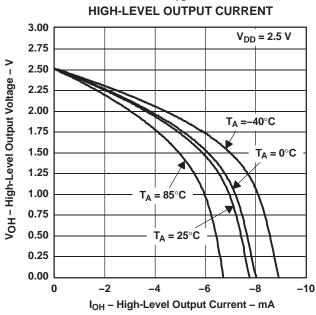
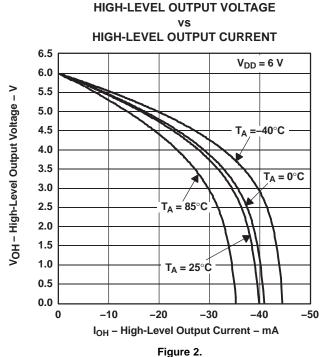


Figure 3.



NORMALIZED INPUT THRESHOLD VOLTAGE vs

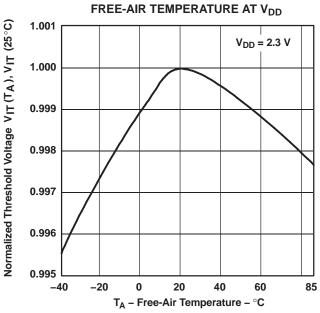
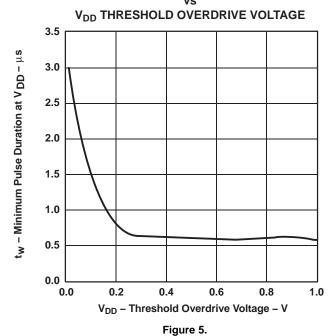


Figure 4.

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TYPICAL CHARACTERISTICS (continued)

MINIMUM PULSE DURATION AT V_{DD}







11-Apr-2013

PACKAGING INFORMATION

Orderable Device	Status	Package Type	Package Drawing	Pins	Package Qty	Eco Plan	Lead/Ball Finish	MSL Peak Temp	Op Temp (°C)	Top-Side Markings	Samples
TPS3809I50MDBVREP	ACTIVE	SOT-23	DBV	3	3000	Green (RoHS & no Sb/Br)	CU NIPDAU	Level-1-260C-UNLIM	-55 to 125	PMAM	Samples
TPS3809K33MDBVREP	ACTIVE	SOT-23	DBV	3	3000	Green (RoHS & no Sb/Br)	CU NIPDAU	Level-1-260C-UNLIM	-55 to 125	PLZM	Samples
TPS3809L30MDBVREP	ACTIVE	SOT-23	DBV	3	3000	Green (RoHS & no Sb/Br)	CU NIPDAU	Level-1-260C-UNLIM	-55 to 125	PLYM	Samples
V62/06636-01XE	ACTIVE	SOT-23	DBV	3	3000	Green (RoHS & no Sb/Br)	CU NIPDAU	Level-1-260C-UNLIM	-55 to 125	PLYM	Samples
V62/06636-02XE	ACTIVE	SOT-23	DBV	3	3000	Green (RoHS & no Sb/Br)	CU NIPDAU	Level-1-260C-UNLIM	-55 to 125	PLZM	Samples
V62/06636-03XE	ACTIVE	SOT-23	DBV	3	3000	Green (RoHS & no Sb/Br)	CU NIPDAU	Level-1-260C-UNLIM	-55 to 125	PMAM	Samples

(1) The marketing status values are defined as follows:

ACTIVE: Product device recommended for new designs.

LIFEBUY: TI has announced that the device will be discontinued, and a lifetime-buy period is in effect.

NRND: Not recommended for new designs. Device is in production to support existing customers, but TI does not recommend using this part in a new design.

PREVIEW: Device has been announced but is not in production. Samples may or may not be available.

OBSOLETE: TI has discontinued the production of the device.

(2) Eco Plan - The planned eco-friendly classification: Pb-Free (RoHS), Pb-Free (RoHS Exempt), or Green (RoHS & no Sb/Br) - please check http://www.ti.com/productcontent for the latest availability information and additional product content details.

TBD: The Pb-Free/Green conversion plan has not been defined.

Pb-Free (RoHS): TI's terms "Lead-Free" or "Pb-Free" mean semiconductor products that are compatible with the current RoHS requirements for all 6 substances, including the requirement that lead not exceed 0.1% by weight in homogeneous materials. Where designed to be soldered at high temperatures, TI Pb-Free products are suitable for use in specified lead-free processes. **Pb-Free** (RoHS Exempt): This component has a RoHS exemption for either 1) lead-based flip-chip solder bumps used between the die and package, or 2) lead-based die adhesive used between

the die and leadframe. The component is otherwise considered Pb-Free (RoHS compatible) as defined above.

Green (RoHS & no Sb/Br): TI defines "Green" to mean Pb-Free (RoHS compatible), and free of Bromine (Br) and Antimony (Sb) based flame retardants (Br or Sb do not exceed 0.1% by weight in homogeneous material)

(3) MSL, Peak Temp. -- The Moisture Sensitivity Level rating according to the JEDEC industry standard classifications, and peak solder temperature.

⁽⁴⁾ Multiple Top-Side Markings will be inside parentheses. Only one Top-Side Marking contained in parentheses and separated by a "~" will appear on a device. If a line is indented then it is a continuation of the previous line and the two combined represent the entire Top-Side Marking for that device.



PACKAGE OPTION ADDENDUM

11-Apr-2013

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OTHER QUALIFIED VERSIONS OF TPS3809I50-EP, TPS3809K33-EP, TPS3809L30-EP:

- Catalog: TPS3809I50, TPS3809K33, TPS3809L30
- Automotive: TPS3809I50-Q1, TPS3809K33-Q1, TPS3809L30-Q1

NOTE: Qualified Version Definitions:

- Catalog TI's standard catalog product
- Automotive Q100 devices qualified for high-reliability automotive applications targeting zero defects



TAPE AND REEL INFORMATION



TAPE DIMENSIONS KO P1 BO W Cavity A0

	Dimension designed to accommodate the component width
B0	Dimension designed to accommodate the component length
K0	Dimension designed to accommodate the component thickness
W	Overall width of the carrier tape
P1	Pitch between successive cavity centers

QUADRANT ASSIGNMENTS FOR PIN 1 ORIENTATION IN TAPE



*All dimensions are nominal

Device	Package Type	Package Drawing		SPQ	Reel Diameter (mm)	Reel Width W1 (mm)	A0 (mm)	B0 (mm)	K0 (mm)	P1 (mm)	W (mm)	Pin1 Quadrant
TPS3809I50MDBVREP	SOT-23	DBV	3	3000	180.0	9.0	3.3	3.2	1.47	4.0	8.0	Q3
TPS3809K33MDBVREP	SOT-23	DBV	3	3000	180.0	9.0	3.3	3.2	1.47	4.0	8.0	Q3
TPS3809L30MDBVREP	SOT-23	DBV	3	3000	180.0	9.0	3.3	3.2	1.47	4.0	8.0	Q3



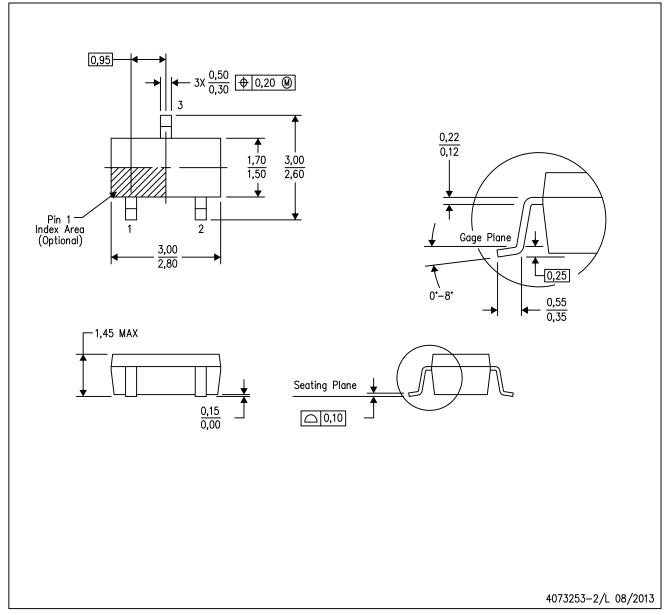


*All dimensions are nominal

7 til dillionolollo alo nominal							
Device	Package Type Package Drawing		Pins	SPQ	Length (mm)	Width (mm)	Height (mm)
TPS3809I50MDBVREP	SOT-23	DBV	3	3000	182.0	182.0	20.0
TPS3809K33MDBVREP	SOT-23	DBV	3	3000	182.0	182.0	20.0
TPS3809L30MDBVREP	SOT-23	DBV	3	3000	182.0	182.0	20.0

DBV (R-PDSO-G3)

PLASTIC SMALL-OUTLINE PACKAGE



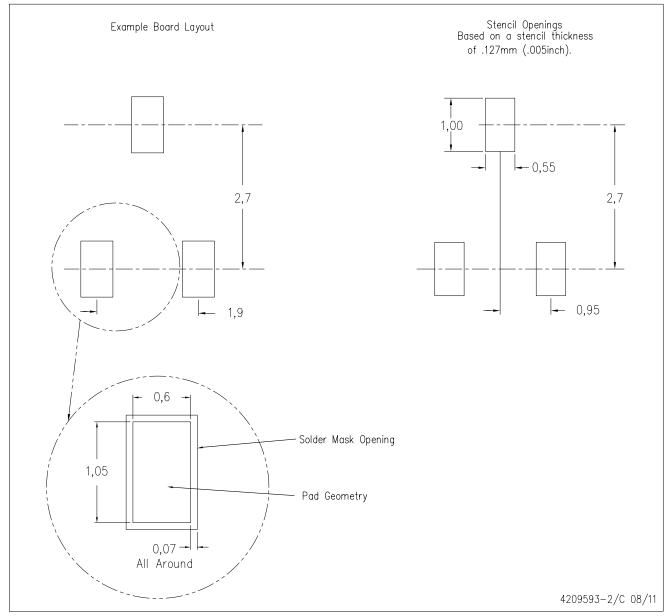
NOTES:

- A. All linear dimensions are in millimeters.
- B. This drawing is subject to change without notice.
- C. Body dimensions do not include mold flash or protrusion. Mold flash and protrusion shall not exceed 0.15 per side.



DBV (R-PDSO-G3)

PLASTIC SMALL OUTLINE



NOTES:

- A. All linear dimensions are in millimeters.
- B. This drawing is subject to change without notice.
- C. Customers should place a note on the circuit board fabrication drawing not to alter the center solder mask defined pad.
- D. Publication IPC-7351 is recommended for alternate designs.
- E. Laser cutting apertures with trapezoidal walls and also rounding corners will offer better paste release. Customers should contact their board assembly site for stencil design recommendations. Example stencil design based on a 50% volumetric metal load solder paste. Refer to IPC-7525 for other stencil recommendations.



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