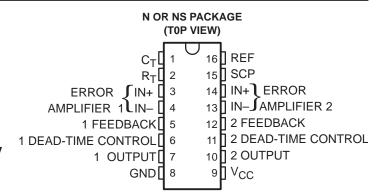
TL1453C DUAL PULSE-WIDTH-MODULATION CONTROL CIRCUIT

SLVS039A - FEBRUARY 1990 - REVISED DECEMBER 1990

- Complete PWM Power Control Circuitry
- Completely Synchronized Operation
- Internal Undervoltage Lockout Protection
- Wide Supply Voltage Range
- Oscillator Frequency . . . 500 kHz Max
 Variable Dead Time Provides Control
- Over Total Range
 Internal Regulator Provides a Stable 2.5-V Reference Supply



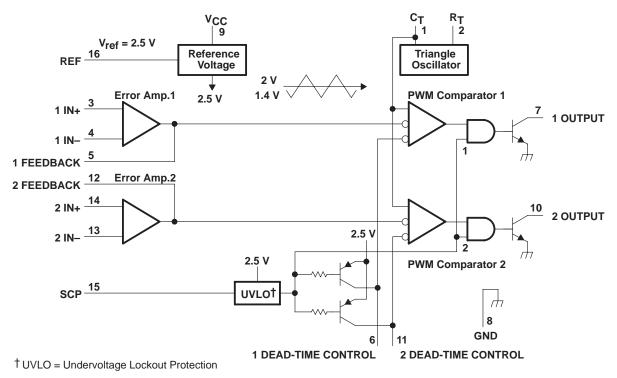
description

The TL1453C incorporates the functions required in the construction of two pulse-width-modulation control circuits on a single monolithic chip. Designed primarily for power supply control, the TL1453C contains an on-chip 2.5-V regulator, two error amplifiers, an adjustable oscillator, two dead-time comparators, undervoltage lockout circuitry, and dual common-emitter output transistor circuits.

The uncommitted output transistors provide common-emitter output capability for each controller. The internal amplifiers exhibit a common-mode voltage range from 1.05 V to 1.45 V. The dead-time control comparator has no offset unless externally altered and may be used to provide 0% to 100% dead time. The on-chip oscillator may be operated by terminating R_T (pin 2) and C_T (pin 1). During low-V_{CC} conditions, the undervoltage lockout control circuit feature inhibits the output until the internal circuitry is operational.

The TL1453C is characterized for operation from –20°C to 85°C.

functional block diagram



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absolute maximum ratings over operating free-air temperature range (unless otherwise noted)

Supply voltage, V _{CC} (see Note 1)	
Amplifier input voltage	
Collector output voltage	
Collector output current	
Continuous total power dissipation	
Continuous total power dissipation Operating free-air temperature range, T _A	
	−20°C to 85°C

NOTE 1: All voltage values are with respect to network ground terminal.

DISSIPATION RATING TABLE							
PACKAGE	T _A ≤ 25°C POWER RATING	DERATING FACTOR ABOVE T _A = 25°C	T _A = 85°C POWER RATING				
N	1000 mW	8 mW/°C	520 mW				
NS	725 mW	5.8 mW/°C	397 mW				

recommended operating conditions

	MIN	MAX	UNIT
Supply voltage, V _{CC}	3.6	40	V
Amplifier input voltage, VI	1.05	1.45	V
Collector output voltage, VO		50	V
Collector output current		20	mA
Current into feedback terminal		45	μΑ
Feedback resistor, RF	100		kΩ
Timing capacitor, CT	150	15000	pF
Timing resistor, R _T	5.1	100	kΩ
Oscillator frequency	1	500	kHz
Operating free-air temperature, T _A	-20	85	°C

electrical characteristics over recommended operating free-air temperature range, V_{CC} = 6 V, f = 200 kHz (unless otherwise noted)

reference section

PARAMETER	TEST CONDITIONS	MIN	TYP [†]	MAX	UNIT
Output voltage (pin 16)	I _O = 1 mA	2.4	2.5	2.6	V
	$T_A = -20^{\circ}C$ to $25^{\circ}C$		-0.1%	±1%	
Output voltage change with temperature	$T_A = 25^{\circ}C$ to $85^{\circ}C$		-0.2%	±1%	
Input regulation	V_{CC} = 3.6 V to 40 V		2	12.5	mV
Output regulation	$I_{O} = 0.1 \text{ mA to } 1 \text{ mA}$		1	7.5	mV
Short-circuit output current	$V_{O} = 0$	3	10	30	mA

[†] All typical values are at $T_A = 25^{\circ}C$.



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electrical characteristics over recommended operating free-air temperature range, V_{CC} = 6 V, f = 200 kHz (unless otherwise noted) (continued)

undervoltage lockout section

PARAMETER	TEST CONDITIONS	MIN TYP [†] MAX	UNIT
Upper threshold voltage (pin 9)	$I_{Oref} = 0.1 \text{ mA}, T_A = 25^{\circ}C$	2.72	V
Lower threshold voltage (pin 9)	$I_{Oref} = 0.1 \text{ mA}, T_A = 25^{\circ}C$	2.6	V
Hysteresis (pin 9)	$I_{Oref} = 0.1 \text{ mA}, T_A = 25^{\circ}C$	80 120	mV

oscillator section

PARAMETER	TEST CONDITIONS	MIN	TYP†	MAX	UNIT
Frequency	$C_{T} = 330 \text{ pF}, R_{T} = 10 \text{ k}\Omega$		200		kHz
Standard deviation of frequency	V _{CC} , T _A , R _T , C _T values are constant		10%		
Frequency change with voltage	V _{CC} = 3.6 V to 40 V		1%		
Frequency change with temperature	$T_A = -20^{\circ}C$ to $25^{\circ}C$		-0.4%	±2%	
	$T_A = 25^{\circ}C$ to $85^{\circ}C$		-0.2%	±2%	

dead-time control section

PARAMETER	TEST CONDITIONS	MIN	түр†	MAX	UNIT
Input bias current (pins 6 and 11)				1	μA
	Zero duty cycle		2.05	2.25	
Input threshold voltage at f = 10kHz (pins 6 and 11)	Maximum duty cycle	1.2	1.45		V

error-amplifier section

PARAMETER	TEST CONDITIONS	MIN	TYP†	MAX	UNIT
Input offset voltage	V _O (pins 5 and 12) = 1.25 V			±6	mV
Input offset current	V _O (pins 5 and 12) = 1.25 V			±100	nA
Input bias current	V _O (pins 5 and 12) = 1.25 V		160	500	nA
Common-mode input voltage range	V _{CC} = 3.6 V to 40 V	1.05 to 1.45			V
Open-loop voltage amplification	R _F = 200 kΩ	70	80		dB
Unity-gain bandwidth			1.5		MHz
Common-mode rejection ratio		60	80		dB
Positive output voltage swing		V _{ref} -0.1			V
Negative output voltage swing				1	V
Output (sink) current (pins 5 and 12)	$V_{ID} = -0.1 \text{ V}, V_O = 1.25 \text{ V}$	0.5	1.6		mA
Output (source) current (pins 5 and 12)	V _{ID} = 0.1 V, V _O = 1.25 V	-45	-70		μΑ

output section

PARAMETER	TEST CONDITIONS	MIN TYP [†]	MAX	UNIT
Collector off-state current	$V_{CC} = 0, V_{O} = 50 \text{ V}$		10	
	V _O = 50 V		10	μA
Output saturation voltage	IO = 10 mA	1.2	2	V
Short-circuit output current	VO = 6 V	90		mA

[†] All typical values are at $T_A = 25^{\circ}C$.



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electrical characteristics over recommended operating free-air temperature range, V_{CC} = 6 V, f = 200 kHz (unless otherwise noted) (continued)

pwm comparator section

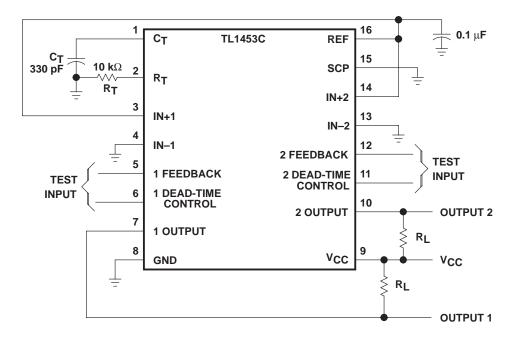
PARAMETER	TEST CONDITIONS	MIN	TYP†	MAX	UNIT
Input threshold voltage at $f = 10 \text{ kHz}$ (pins 5 and 12)	Zero duty cycle		2.05	2.25	V
input the should voltage at $t = 10$ kmz (pins 5 and 12)	Maximum duty cycle	1.2	1.45		v
Input (sink) current (pins 5 and 12)	V _I = 1.25 V	0.5	1.6		mA
Input (source) current (pins 5 and 12)	V _I = 1.25 V	-45	-70		μA

total device

TEST CONDITIONS	MIN	TYP†	MAX	UNIT
Off-state		1.3	1.8	mA
R _T = 10 kΩ		1.7	2.4	mA
	Off-state	Off-state	Off-state 1.3	Off-state 1.3 1.8

[†] All typical values are at $T_A = 25^{\circ}C$.

test circuit





PACKAGING INFORMATION

Orderable Device	Status ⁽¹⁾	Package Type	Package Drawing	Pins	Package Qty	e Eco Plan ⁽²⁾	Lead/Ball Finish	MSL Peak Temp ⁽³⁾
TL1453CD	ACTIVE	SOIC	D	16	40	Green (RoHS & no Sb/Br)	CU NIPDAU	Level-1-260C-UNLIM
TL1453CDG4	ACTIVE	SOIC	D	16	40	Green (RoHS & no Sb/Br)	CU NIPDAU	Level-1-260C-UNLIM
TL1453CN	ACTIVE	PDIP	Ν	16	25	Pb-Free (RoHS)	CU NIPDAU	N / A for Pkg Type
TL1453CNE4	ACTIVE	PDIP	Ν	16	25	Pb-Free (RoHS)	CU NIPDAU	N / A for Pkg Type
TL1453CNSLE	OBSOLETE	SO	NS	16		TBD	Call TI	Call TI
TL1453CNSR	ACTIVE	SO	NS	16	2000	Green (RoHS & no Sb/Br)	CU NIPDAU	Level-1-260C-UNLIM
TL1453CNSRG4	ACTIVE	SO	NS	16	2000	Green (RoHS & no Sb/Br)	CU NIPDAU	Level-1-260C-UNLIM
TL1453CPWR	ACTIVE	TSSOP	PW	16	2000	Green (RoHS & no Sb/Br)	CU NIPDAU	Level-1-260C-UNLIM
TL1453CPWRG4	ACTIVE	TSSOP	PW	16	2000	Green (RoHS & no Sb/Br)	CU NIPDAU	Level-1-260C-UNLIM

⁽¹⁾ The marketing status values are defined as follows:

ACTIVE: Product device recommended for new designs.

LIFEBUY: TI has announced that the device will be discontinued, and a lifetime-buy period is in effect.

NRND: Not recommended for new designs. Device is in production to support existing customers, but TI does not recommend using this part in a new design.

PREVIEW: Device has been announced but is not in production. Samples may or may not be available.

OBSOLETE: TI has discontinued the production of the device.

⁽²⁾ Eco Plan - The planned eco-friendly classification: Pb-Free (RoHS), Pb-Free (RoHS Exempt), or Green (RoHS & no Sb/Br) - please check http://www.ti.com/productcontent for the latest availability information and additional product content details. **TBD**: The Pb-Free/Green conversion plan has not been defined.

Pb-Free (RoHS): TI's terms "Lead-Free" or "Pb-Free" mean semiconductor products that are compatible with the current RoHS requirements for all 6 substances, including the requirement that lead not exceed 0.1% by weight in homogeneous materials. Where designed to be soldered

at high temperatures, TI Pb-Free products are suitable for use in specified lead-free processes. **Pb-Free (RoHS Exempt):** This component has a RoHS exemption for either 1) lead-based flip-chip solder bumps used between the die and package, or 2) lead-based die adhesive used between the die and leadframe. The component is otherwise considered Pb-Free (RoHS

compatible) as defined above. **Green (RoHS & no Sb/Br):** TI defines "Green" to mean Pb-Free (RoHS compatible), and free of Bromine (Br) and Antimony (Sb) based flame retardants (Br or Sb do not exceed 0.1% by weight in homogeneous material)

⁽³⁾ MSL, Peak Temp. -- The Moisture Sensitivity Level rating according to the JEDEC industry standard classifications, and peak solder temperature.

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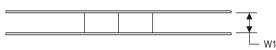
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TAPE AND REEL INFORMATION

REEL DIMENSIONS

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TAPE AND REEL INFORMATION

TAPE DIMENSIONS



A0	Dimension designed to accommodate the component width
B0	Dimension designed to accommodate the component length
K0	Dimension designed to accommodate the component thickness
W	Overall width of the carrier tape
P1	Pitch between successive cavity centers

*All dimensions are nominal												
Device	Package Type	Package Drawing		SPQ	Reel Diameter (mm)	Reel Width W1 (mm)	A0 (mm)	B0 (mm)	K0 (mm)	P1 (mm)	W (mm)	Pin1 Quadrant
TL1453CNSR	SO	NS	16	2000	330.0	16.4	8.2	10.5	2.5	12.0	16.0	Q1
TL1453CPWR	TSSOP	PW	16	2000	330.0	12.4	6.9	5.6	1.6	8.0	12.0	Q1

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PACKAGE MATERIALS INFORMATION

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*All dimensions are nominal

Device	Package Type	Package Drawing	Pins	SPQ	Length (mm)	Width (mm)	Height (mm)
TL1453CNSR	SO	NS	16	2000	367.0	367.0	38.0
TL1453CPWR	TSSOP	PW	16	2000	367.0	367.0	35.0

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