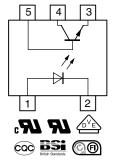
# **TCLT111.** Series

**Vishay Semiconductors** 

## **Optocoupler, Phototransistor Output, SOP-6L5,** 110 °C Rated, Half Pitch, Long Mini-Flat Package



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## DESCRIPTION

The TCLT111. series consists of a phototransistor optically coupled to a gallium arsenide infrared-emitting diode in a 5-lead SOP-6L package.

## **APPLICATIONS**

- Switchmode power supplies
- Computer peripheral interface
- Microprocessor system interface

### **FEATURES**

- SMD low profile 5 pin package
- Isolation test voltage 5000 V<sub>BMS</sub>
- · CTR flexibility available see order information
- Special construction
- Extra low coupling capacitance
- Connected base
- DC input with transistor output
- Temperature range 55 °C to 110 °C
- Creepage distance > 8 mm
- · Material categorization: For definitions of compliance please see www.vishay.com/doc?99912

### AGENCY APPROVALS

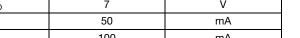
- UL1577, file no. E76222
- cUL file no. E76222, equivalent to CSA 22.2 bulletin 5A
- DIN EN 60747-5-5 (VDE 0884-5)
- FIMKO
- BSI
- CQC

ORDERING	INFORM	IATION									
Т	С	L	Т	1	1	1	#		SOP-6L5	h	
			PART N	IUMBER					■ 10.2 mm	<b></b>	
AGENCY	CTR (%)										
CERTIFIED/ PACKAGE	5 mA		10	mA				5 mA			
UL, cUL, VDE, FIMKO, CQC	50 to 600	40 to 80	63 to 125	100 to 200	160 to 320	50 to 150	100 to 300	80 to 160	130 to 260	200 to 400	
SOP-6L5	TCLT1110	TCLT1111	TCLT1112	TCLT1113	TCLT1114	TCLT1115	TCLT1116	TCLT1117	TCLT1118	TCLT1119	

<b>ABSOLUTE MAXIMUM RATINGS</b> ( $T_{amb} = 25 \text{ °C}$ , unless otherwise specified)								
PARAMETER	TEST CONDITION	SYMBOL	VALUE	UNIT				
INPUT		•						
Reverse voltage		V <sub>R</sub>	6	V				
Forward current		١ <sub>F</sub>	60	mA				
Forward surge current	t <sub>p</sub> ≤ 10 μs	I <sub>FSM</sub>	1.5	А				
Power dissipation		P <sub>diss</sub>	100	mW				
Junction temperature		Tj	125	°C				
OUTPUT								
Collector emitter voltage		V <sub>CEO</sub>	70	V				
Emitter collector voltage		V <sub>ECO</sub>	7	V				
Collector current		Ι <sub>C</sub>	50	mA				
Collector peak current	$t_p/T = 0.5, t_p \le 10 \text{ ms}$	I <sub>CM</sub>	100	mA				
Power dissipation		P <sub>diss</sub>	150	mW				
Junction temperature		Tj	125	°C				

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1 For technical questions, contact: <u>optocoupleranswers@vishay.com</u> Document Number: 81282







COMPLIANT

# TCLT111. Series



## **Vishay Semiconductors**

ABSOLUTE MAXIMUM RATINGS (T <sub>amb</sub> = 25 °C, unless otherwise specified)									
PARAMETER	TEST CONDITION	SYMBOL	VALUE	UNIT					
COUPLER									
Isolation test voltage (RMS)		V <sub>ISO</sub>	5000	V <sub>RMS</sub>					
Total power dissipation		P <sub>tot</sub>	250	mW					
Operating ambient temperature range		T <sub>amb</sub>	- 55 to + 110	°C					
Storage temperature range		T <sub>stg</sub>	- 55 to + 125	°C					
Soldering temperature <sup>(1)</sup>		T <sub>sld</sub>	260	۵°					

Notes

Stresses in excess of the absolute Maximum Ratings can cause permanent damage to the device. Functional operation of the device is not
implied at these or any other conditions in excess of those given in the operational sections of this document. Exposure to absolute
Maximum Rating for extended periods of the time can adversely affect reliability.

<sup>(1)</sup> Wave soldering three cycles are allowed. Also refer to "Assembly Instruction" (<u>www.vishay.com/doc?80054</u>).

<b>ELECTRICAL CHARACTERISTICS</b> ( $T_{amb}$ = 25 °C, unless otherwise specified)								
PARAMETER	TEST CONDITION	SYMBOL	MIN.	TYP.	MAX.	UNIT		
input								
Forward voltage	I <sub>F</sub> = 50 mA	V <sub>F</sub>		1.25	1.6	V		
Junction capacitance	V <sub>R</sub> = 0 V, f = 1 MHz	Cj		50		pF		
output								
Collector emitter voltage	I <sub>C</sub> = 1 mA	V <sub>CEO</sub>	70			V		
Emitter collector voltage	I <sub>E</sub> = 100 μA	V <sub>ECO</sub>	7			V		
Collector emitter leakage current	$V_{CE} = 20 \text{ V}, I_F = 0 \text{ A}$	I <sub>CEO</sub>		10	100	nA		
coupler								
Collector emitter saturation voltage	I <sub>F</sub> = 10 mA, I <sub>C</sub> = 1 mA	V <sub>CEsat</sub>			0.3	V		
Cut-off frequency	$\label{eq:Vce} \begin{array}{l} V_{CE}=5 \text{ V}, \text{ I}_{\text{F}}=10 \text{ mA}, \\ \text{R}_{\text{L}}=100 \ \Omega \end{array}$	f <sub>c</sub>		110		kHz		
Coupling capacitance	f = 1 MHz	C <sub>k</sub>		0.3		pF		

#### Note

• Minimum and maximum values are testing requirements. Typical values are characteristics of the device and are the result of engineering evaluation. Typical values are for information only and are not part of the testing requirements.

<b>CURRENT TRANSFER RATIO</b> ( $T_{amb} = 25$ °C, unless otherwise specified)									
PARAMETER	TEST CONDITION	PART	SYMBOL	MIN.	TYP.	MAX.	UNIT		
	$V_{CE} = 5 V, I_F = 5 mA$	TCLT1110	CTR	50		600	%		
		TCLT1111	CTR	40		80	%		
	V <sub>CE</sub> = 5 V, I <sub>E</sub> = 10 mA	TCLT1112	CTR	63		125	%		
	$v_{CE} = 5 v, i_F = 10 mA$	TCLT1113	CTR	100		200	%		
		TCLT1114	CTR	160		320	%		
		TCLT1111	CTR	13	30		%		
I <sub>C</sub> /I <sub>F</sub>	V <sub>CF</sub> = 5 V, I <sub>F</sub> = 1 mA	TCLT1112	CTR	22	45		%		
IC/IF	$v_{CE} = 5 v$ , $r_F = 1 mA$	TCLT1113	CTR	34	70		%		
		TCLT1114	CTR	56	100		%		
		TCLT1115	CTR	50		150	%		
		TCLT1116	CTR	100		300	%		
	$V_{CE} = 5 \text{ V}, \text{ I}_{F} = 5 \text{ mA}$	TCLT1117	CTR	80		160	%		
		TCLT1118	CTR	130		260	%		
		TCLT1119	CTR	200		400	%		



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SAFETY AND INSULATION RATED PARAMETERS								
PARAMETER	TEST CONDITION	SYMBOL	MIN.	TYP.	MAX.	UNIT		
Partial discharge test voltage - routine test	100 %, t <sub>test</sub> = 1 s	V <sub>pd</sub>	1.6			kV		
Partial discharge test voltage -	t <sub>Tr</sub> = 60 s, t <sub>test</sub> = 10 s,	V <sub>IOTM</sub>	8			kV		
lot test (sample test)	(see figure 2)	V <sub>pd</sub>	1.3			kV		
	V <sub>IO</sub> = 500 V	R <sub>IO</sub>	10 <sup>12</sup>			Ω		
Insulation resistance	$V_{IO} = 500 \text{ V}, \text{ T}_{amb} = 100 ^{\circ}\text{C}$	R <sub>IO</sub>	10 <sup>11</sup>			Ω		
	V <sub>IO</sub> = 500 V, T <sub>amb</sub> = 150 °C (construction test only)	R <sub>IO</sub>	10 <sup>9</sup>			Ω		
Forward current		I <sub>si</sub>	130			mA		
Power dissipation		P <sub>so</sub>	265			mW		
Rated impulse voltage		V <sub>IOTM</sub>	8			kV		
Safety temperature		T <sub>si</sub>	150			°C		
Clearance distance			8.0			mm		
Creepage distance			8.0			mm		
Insulation distance (internal)			0.75			mm		

Note

 According to DIN EN 60747-5-5 (VDE 0884-5) (see figure 2). This optocoupler is suitable for safe electrical isolation only within the safety ratings. Compliance with the safety ratings shall be ensured by means of suitable protective circuits.

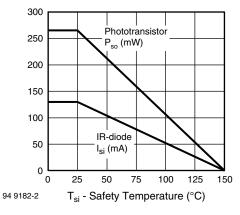


Fig. 1 - Derating Diagram

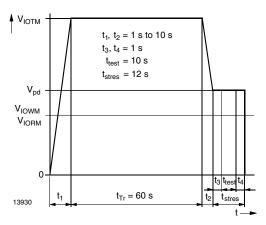
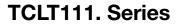


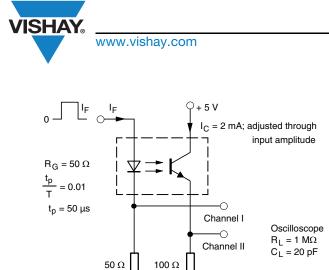
Fig. 2 - Test Pulse Diagram for Sample Test According to DIN EN 60747-5-5 (VDE 0884-5); IEC 60747-5-5

SWITCHING CHARACTERISTICS ( $T_{amb} = 25 \text{ °C}$ , unless otherwise specified)									
PARAMETER	TEST CONDITION	SYMBOL	MIN.	TYP.	MAX.	UNIT			
Delay time	$V_S = 5 \text{ V}, \text{ I}_C = 2 \text{ mA}, \text{ R}_L = 100 \Omega$ , (see figure 3)	t <sub>d</sub>		3		μs			
Rise time	$V_S = 5 \text{ V}, \text{ I}_C = 2 \text{ mA}, \text{ R}_L = 100 \Omega$ , (see figure 3)	t <sub>r</sub>		3		μs			
Fall time	$V_S$ = 5 V, $I_C$ = 2 mA, $R_L$ = 100 $\Omega$ , (see figure 3)	t <sub>f</sub>		4.7		μs			
Storage time	$V_S = 5 \text{ V}, \text{ I}_C = 2 \text{ mA}, \text{ R}_L = 100 \Omega$ , (see figure 3)	t <sub>s</sub>		0.3		μs			
Turn-on time	$V_S = 5 \text{ V}, \text{ I}_C = 2 \text{ mA}, \text{ R}_L = 100 \Omega$ , (see figure 3)	t <sub>on</sub>		6		μs			
Turn-off time	$V_S$ = 5 V, $I_C$ = 2 mA, $R_L$ = 100 $\Omega$ , (see figure 3)	t <sub>off</sub>		5		μs			
Turn-on time	$V_{S} = 5 \text{ V}, \text{ I}_{F} = 10 \text{ mA}, \text{ R}_{L} = 1 \text{ k}\Omega$ , (see figure 4)	t <sub>on</sub>		9		μs			
Turn-off time	$V_S = 5 \text{ V}, \text{ I}_F = 10 \text{ mA}, \text{ R}_L = 1 \text{ k}\Omega$ , (see figure 4)	t <sub>off</sub>		10		μs			

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Fig. 3 - Test Circuit, Non-Saturated Operation

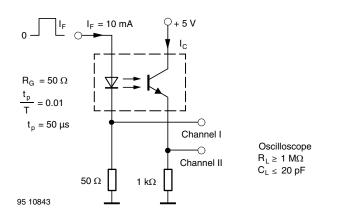
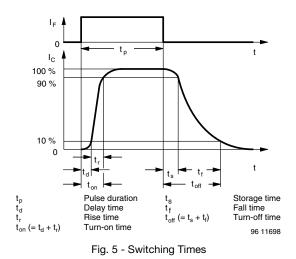


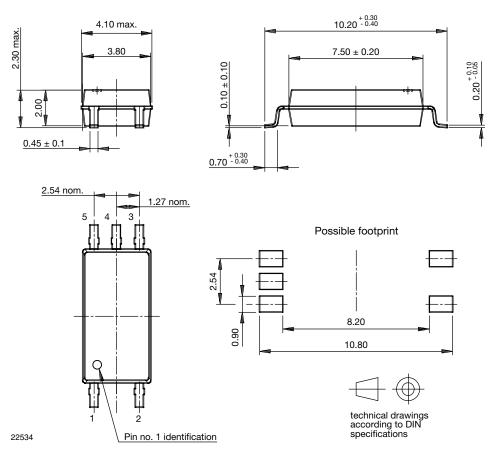
Fig. 4 - Test Circuit, Saturated Operation



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### **PACKAGE DIMENSIONS** in millimeters



## **PACKAGE MARKING**





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