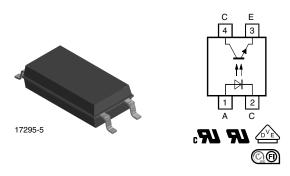


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TCLT101. Series

Vishay Semiconductors

Optocoupler, Phototransistor Output, SOP-6L4, 110 °C Rated, Long Mini-Flat Package



DESCRIPTION

The TCLT101. series consists of a phototransistor optically coupled to a gallium arsenide infrared-emitting diode in a 4-lead SOP-6L package.

APPLICATIONS

- Switchmode power supplies
- Computer peripheral interface
- Microprocessor system interface

FEATURES

- SMD low profile 4 lead package
- High isolation 5000 V_{RMS}
- CTR flexibility available see order information
- Special construction
- Extra low coupling capacitance
- Connected base
- DC input with transistor output
- Temperature range 55 °C to 110 °C
- Creepage distance > 8 mm
- Material categorization: For definitions of compliance please see <u>www.vishay.com/doc?99912</u>

AGENCY APPROVALS

- UL1577, file no. E76222
- cUL file no. E76222, equivalent to CSA bulletin 5A
- DIN EN 60747-5-2 (VDE 0884)/DIN EN 60747-5-5 (pending), available with option 1
- FIMKO (SETI): EN 60950

ORDERING INFORMATION										
Т	С	L	Т	1	0	1	#		SOP-6L4	h
			PART	NUMBER					▲ 10.2 mm	•
AGENCY						CTR (%)				
CERTIFIED/PAC	KAGE	5 mA		10 mA				5 mA		
UL, cUL, VDE, FI	мко	50 to 600	63 to 125	100 to 200	160 to 320	50 to 150	100 to 300	80 to 160	130 to 260	200 to 400
SOP-6L4		TCLT1010	TCLT1012	TCLT1013	TCLT1014	TCLT1015	TCLT1016	TCLT1017	TCLT1018	TCLT1019

ABSOLUTE MAXIMUM RATINGS (T _{amb} = 25 °C, unless otherwise specified)									
PARAMETER	TEST CONDITION	SYMBOL	VALUE	UNIT					
INPUT									
Reverse voltage		V _R	6	V					
Forward current		IF	60	mA					
Forward surge current	$t_p \le 10 \ \mu s$	I _{FSM}	1.5	A					
Power dissipation		P _{diss}	100	mW					
Junction temperature		Tj	125	°C					
OUTPUT									
Collector emitter voltage		V _{CEO}	70	V					
Emitter collector voltage		V _{ECO}	7	V					
Collector current		I _C	50	mA					
Collector peak current	$t_p/T = 0.5, t_p \le 10 \text{ ms}$	I _{CM}	100	mA					
Power dissipation		P _{diss}	150	mW					
Junction temperature		Tj	125	°C					

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Document Number: 81256

COMPLIANT



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ABSOLUTE MAXIMUM RATINGS (T _{amb} = 25 °C, unless otherwise specified)									
PARAMETER	TEST CONDITION	SYMBOL	VALUE	UNIT					
COUPLER									
Isolation test voltage (RMS)		V _{ISO}	5000	V _{RMS}					
Total power dissipation		P _{tot}	250	mW					
Operating ambient temperature range		T _{amb}	- 55 to + 110	°C					
Storage temperature range		T _{stg}	- 55 to + 125	°C					
Soldering temperature ⁽¹⁾		T _{sld}	260	°C					

Notes

Stresses in excess of the absolute maximum ratings can cause permanent damage to the device. Functional operation of the device is not
implied at these or any other conditions in excess of those given in the operational sections of this document. Exposure to absolute
maximum ratings for extended periods of the time can adversely affect reliability.

⁽¹⁾ Wave soldering three cycles are allowed. Also refer to "Assembly Instruction" (<u>www.vishay.com/doc?80054</u>).

ELECTRICAL CHARACTERISTICS (T _{amb} = 25 °C, unless otherwise specified)								
PARAMETER	TEST CONDITION SYMBOL MIN. TYP. M							
INPUT								
Forward voltage	I _F = 50 mA	V _F		1.25	1.6	V		
Junction capacitance	V _R = 0 V, f = 1 MHz	Cj		50		pF		
OUTPUT	OUTPUT							
Collector emitter voltage	I _C = 1 mA	V _{CEO}	70			V		
Emitter collector voltage	I _E = 100 μA	V _{ECO}	7			V		
Collector emitter leakage current	$V_{CE} = 20 \text{ V}, I_F = 0 \text{ A}$	I _{CEO}		10	100	nA		
COUPLER								
Collector emitter saturation voltage	I _F = 10 mA, I _C = 1 mA	V _{CEsat}			0.3	V		
Cut-off frequency	V_{CE} = 5 V, I _F = 10 mA, R _L = 100 Ω	f _c		110		kHz		
Coupling capacitance	f = 1 MHz	C _k		0.3		pF		

Note

• Minimum and maximum values are testing requirements. Typical values are characteristics of the device and are the result of engineering evaluation. Typical values are for information only and are not part of the testing requirements.

CURRENT TRANSFER RATIO ($T_{amb} = 25 \text{ °C}$, unless otherwise specified)										
PARAMETER	TEST CONDITION	PART	SYMBOL	MIN.	TYP.	MAX.	UNIT			
	$V_{CE} = 5 V, I_F = 5 mA$	TCLT1010	CTR	50		600	%			
		TCLT1012	CTR	63		125	%			
	$V_{CE} = 5 \text{ V}, I_F = 10 \text{ mA}$	TCLT1013	CTR	100		200	%			
		TCLT1014	CTR	160		320	%			
	V _{CE} = 5 V, I _F = 1 mA	TCLT1012	CTR	22	45		%			
		TCLT1013	CTR	34	70		%			
I _C /I _F		TCLT1014	CTR	56	100		%			
		TCLT1015	CTR	50		150	%			
		TCLT1016	CTR	100		300	%			
	$V_{CE} = 5 \text{ V}, \text{ I}_{F} = 5 \text{ mA}$	TCLT1017	CTR	80		160	%			
		TCLT1018	CTR	130		260	%			
		TCLT1019	CTR	200		400	%			

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SAFETY AND INSULATION RATED PARAMETERS								
PARAMETER	TEST CONDITION	SYMBOL	MIN.	TYP.	MAX.	UNIT		
Partial discharge test voltage - routine test	100 %, t _{test} = 1 s	V _{pd}	1.6			kV		
Partial discharge test voltage -	t _{Tr} = 60 s, t _{test} = 10 s,	V _{IOTM}	8			kV		
lot test (sample test)	(see figure 2)	V _{pd}	1.3			kV		
	V _{IO} = 500 V	R _{IO}	10 ¹²			Ω		
Insulation resistance	$V_{IO} = 500 \text{ V}, \text{ T}_{amb} = 100 ^{\circ}\text{C}$	R _{IO}	10 ¹¹			Ω		
	V _{IO} = 500 V, T _{amb} = 150 °C (construction test only)	R _{IO}	10 ⁹			Ω		
Forward current		I _{si}	130			mA		
Power dissipation		P _{so}	265			mW		
Rated impulse voltage		V _{IOTM}	8			kV		
Safety temperature		T _{si}	150			°C		
Clearance distance			8.0			mm		
Creepage distance			8.0			mm		
Insulation distance (internal)			0.40			mm		

Note

 According to DIN EN 60747-5-2 (VDE 0884) (see figure 2). This optocoupler is suitable for safe electrical isolation only within the safety ratings. Compliance with the safety ratings shall be ensured by means of suitable protective circuits.

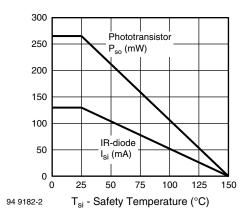


Fig. 1 - Derating Diagram

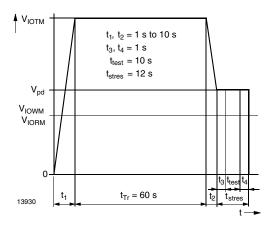


Fig. 2 - Test Pulse Diagram for Sample Test According to DIN EN 60747-5-2 (VDE 0884); IEC 60747-5-5

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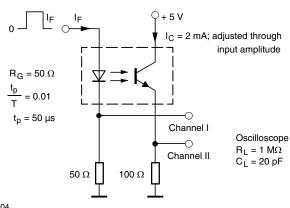


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SWITCHING CHARACTERISTICS ($T_{amb} = 25 \text{ °C}$, unless otherwise specified)									
PARAMETER	TEST CONDITION	SYMBOL	MIN.	TYP.	MAX.	UNIT			
Delay time	$\label{eq:VS} \begin{array}{l} V_S = 5 \ V, \ I_C = 2 \ mA, \ R_L = 100 \ \Omega, \\ (see \ figure \ 3) \end{array}$	t _d		3		μs			
Rise time	$\label{eq:VS} \begin{array}{l} V_S = 5 \mbox{ V}, \mbox{ I}_C = 2 \mbox{ mA}, \mbox{ R}_L = 100 \ \Omega, \\ (see \mbox{ figure 3}) \end{array}$	t _r		3		μs			
Fall time	$\label{eq:VS} \begin{array}{l} V_{S} = 5 \; V, \; I_{C} = 2 \; mA, \; R_{L} = 100 \; \Omega, \\ (\text{see figure 3}) \end{array}$	t _f		4.7		μs			
Storage time	$\label{eq:VS} \begin{array}{l} V_S = 5 \ V, \ I_C = 2 \ mA, \ R_L = 100 \ \Omega, \\ (see \ figure \ 3) \end{array}$	ts		0.3		μs			
Turn-on time	$\label{eq:VS} \begin{array}{l} V_S = 5 \text{ V}, \text{ I}_C = 2 \text{ mA}, \text{ R}_L = 100 \ \Omega, \\ \text{(see figure 3)} \end{array}$	t _{on}		6		μs			
Turn-off time	$\label{eq:VS} \begin{array}{l} V_S = 5 \text{ V}, \text{ I}_C = 2 \text{ mA}, \text{ R}_L = 100 \ \Omega, \\ \text{(see figure 3)} \end{array}$	t _{off}		5		μs			
Turn-on time	$\label{eq:VS} \begin{array}{l} V_S = 5 \ V, \ I_F = 10 \ mA, \ R_L = 1 \ k\Omega, \\ (see \ figure \ 4) \end{array}$	t _{on}		9		μs			
Turn-off time	$\label{eq:VS} \begin{array}{l} V_S = 5 \ V, \ I_F = 10 \ mA, \ R_L = 1 \ k\Omega, \\ (see \ figure \ 4) \end{array}$	t _{off}		10		μs			



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Fig. 3 - Test Circuit, Non-Saturated Operation

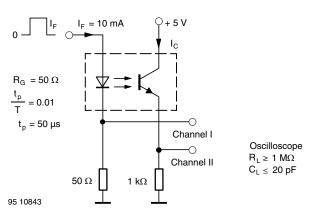
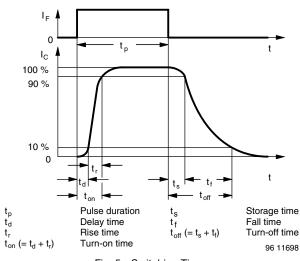


Fig. 4 - Test Circuit, Saturated Operation





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Alternative Device Available

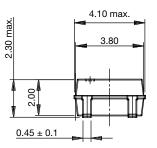
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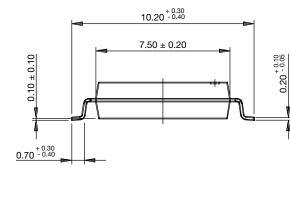


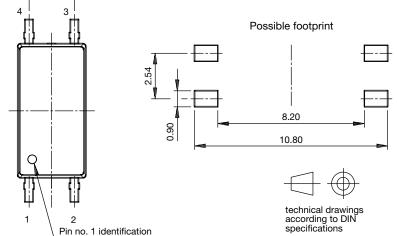
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PACKAGE DIMENSIONS in millimeters



2.54 nom.





PACKAGE MARKING

22533





Vishay

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