

Application Report SPRAB21-October 2009

## **XDS Target Connection Guide**

## ABSTRACT

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Texas Instruments supports a variety of eXtended Development System (XDS<sup>™</sup>) JTAG controllers with various debug capabilities. The *XDS Target Connection Guide* describes target board design requirements that, when followed, allow for compatible operation between most XDS products.

**NOTE:** If your device supports the export of core trace or system trace over the EMU pins and you want your target to be compatible with XDS products capable of acquiring either trace types; for guidelines, see the *Emulation and Trace Headers Technical Reference Manual* (SPRU655).

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