

TPS62125EVM-044 Evaluation Module

This user's guide describes the characteristics, operation, and use of the Texas Instruments TPS62125 evaluation module (EVM). This EVM is designed to help the user easily evaluate and test the operation and functionality of the TPS62125. The EVM converts a 4-V to 17-V input voltage to a regulated 3.3-V output voltage that delivers 300 mA. This user's guide includes setup instructions for the hardware, printed-circuit board layouts for the EVM, a schematic diagram, a bill of materials, and test results for the EVM.

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1 Introduction

The TPS62125 is a 300-mA, synchronous, step-down converter in a 2x2-mm, 8-pin WSON package.

1.1 Background

The TPS62125EVM-044 (PWR044) uses the TPS62125 device and is set to a 3.3-V output. The EVM operates with full-rated performance with an input voltage between 4 V and 17 V, assuming JP1 is connected between ON and EN.

The TPS62125 device contains an adjustable enable threshold and adjustable hysteresis feature. Thus, the input voltage at which the device enables and disables is fully programmable by the user. In the TPS62125EVM-044, the device is set to turn on at 6 V and turn off at 4 V. This feature can be bypassed by using jumper JP1, which overrides the programmed threshold voltages.

1.2 Performance Specification

[Table 1](#) provides a summary of the TPS62125EVM-044 performance specifications. All specifications are given for an ambient temperature of 25°C.

Table 1. Performance Specification Summary

Specification	Test Conditions	Min	Typ	Max	Unit
Input voltage	JP1 connected between EN and ON	4		17	V
Output voltage	PWM Mode of Operation		3.3		V
Output current		0		300	mA
Peak efficiency			93.3%		
Soft-start time	Ramp time of the output voltage		330		μs

1.3 Modifications

The printed-circuit board (PCB) for this EVM accommodates additional input and output capacitors, as well as a feedforward capacitor.

1.3.1 Input and Output Capacitors

C3 is provided for an additional input capacitor. This capacitor is not required for proper operation but can be used to reduce the input voltage ripple.

C4 is provided for an additional output capacitor. This capacitor is not required for proper operation but can be used to reduce the output voltage ripple and to improve the load transient response. The total output capacitance must remain within the recommended range in the data sheet for proper operation.

1.3.2 Feedforward Capacitor

C6 is provided for a feedforward capacitor. This capacitor is not required for proper operation but can be used to improve the transient response. Typically, using a 22-pF feedforward capacitor reduces or eliminates any output voltage overshoot that may occur at start-up. C6 is located on the back side of the PCB.

2 Setup

This section describes how to properly use the TPS62125EVM-044.

2.1 Input/Output Connector Descriptions

J1 – VIN	Positive input connection from the input supply for the EVM.
J2 – S+/S-	Input voltage sense connections. Measure the input voltage at this point.
J3 – GND	Return connection from the input supply for the EVM.
J4 – VOUT	Output voltage connection.
J5 – S+/S-	Output voltage sense connections. Measure the output voltage at this point.
J6 – GND	Output return connection.
J7 – PG/GND	The PG output appears on pin 1 of this header with a convenient ground on pin 2.
JP1 – EN	EN pin input jumper. Place the supplied jumper across ON and EN to turn on the IC. Place the jumper across OFF and EN to turn off the IC. Do not install the supplied jumper to utilize the adjustable enable threshold voltage feature of the IC.
JP2 – PG Pullup Voltage	PG pin pullup voltage jumper. Place the supplied jumper on JP2 to connect the PG pin pullup resistor to Vout. Alternatively, the jumper can be removed and a different voltage can be supplied on pin 1 to pull up the PG pin to a different level. This externally applied voltage must remain below 12 V.

2.2 Setup

To operate the EVM, set jumpers JP1 through JP2 to the desired positions per [Section 2.1](#). Connect the input supply to J1 and J3 and connect the load to J4 and J6.

3 TPS62125EVM-044 Test Results

This section provides test results of the TPS62125EVM-044.

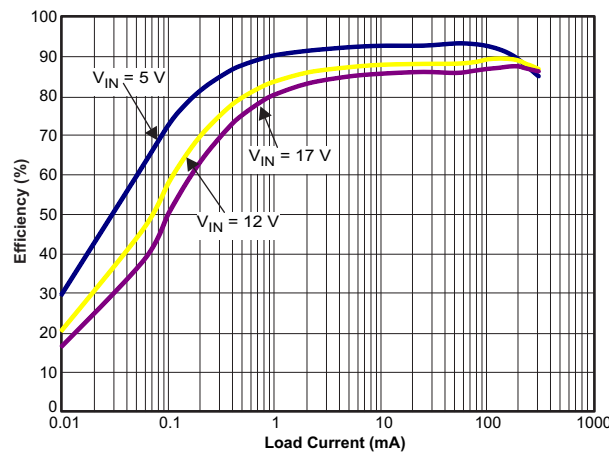


Figure 1. Efficiency (R1, R2, and R3 not installed. JP1 connected between ON and EN.)

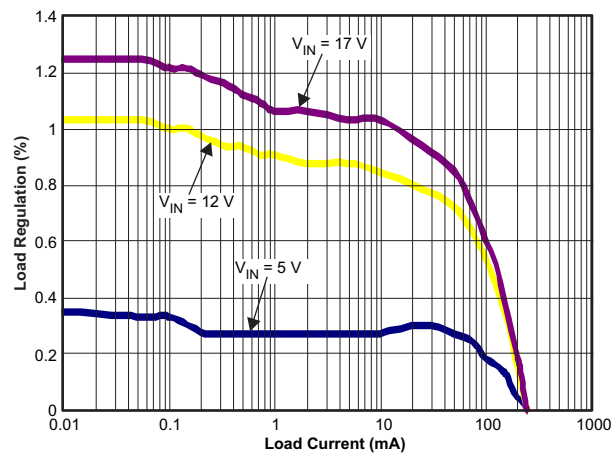


Figure 2. Load Regulation

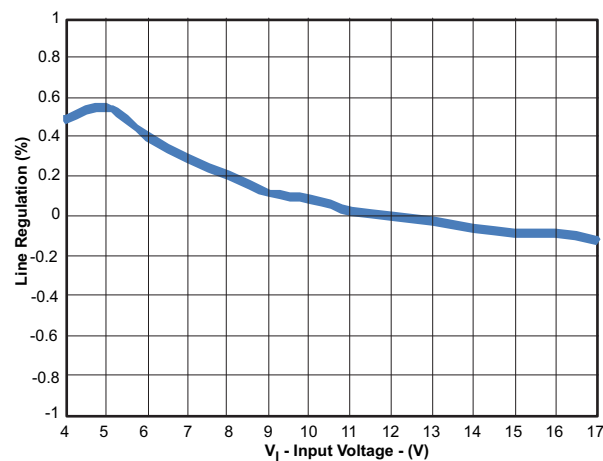


Figure 3. Line Regulation With $I_{out} = 250\text{ mA}$

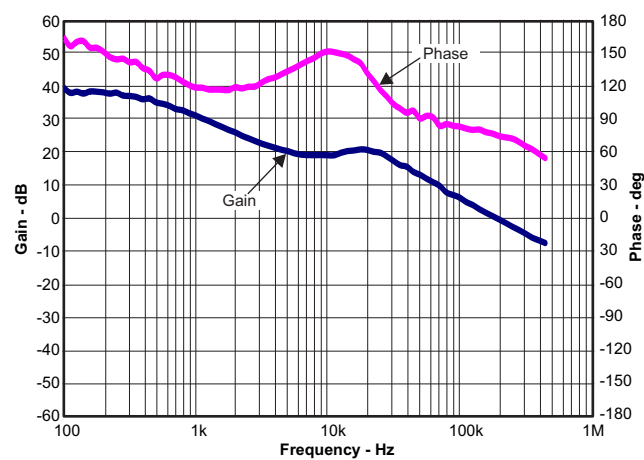


Figure 4. Loop Response With $V_{in} = 12\text{ V}$ and $I_{out} = 250\text{ mA}$

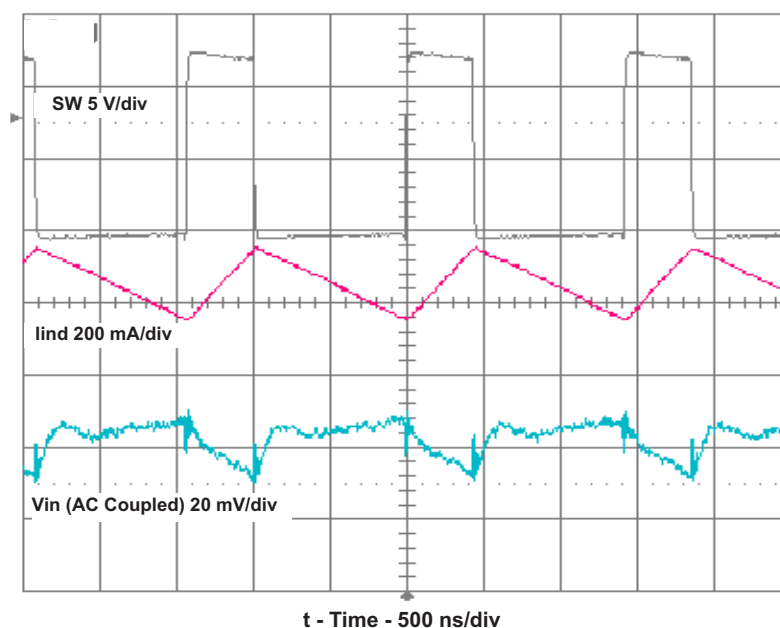


Figure 5. Input Voltage Ripple With $V_{in} = 12\text{ V}$ and $I_{out} = 250\text{ mA}$

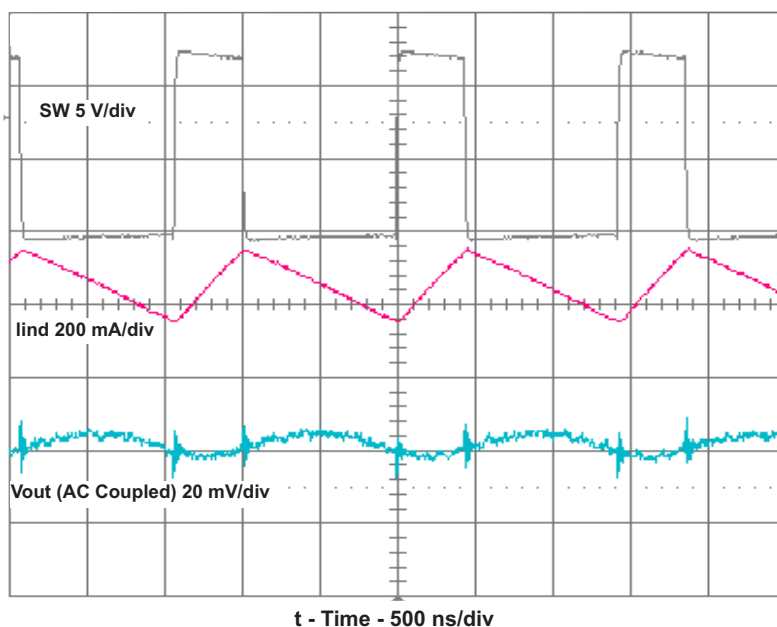


Figure 6. Output Voltage Ripple With $V_{in} = 12\text{ V}$ and $I_{out} = 250\text{ mA}$

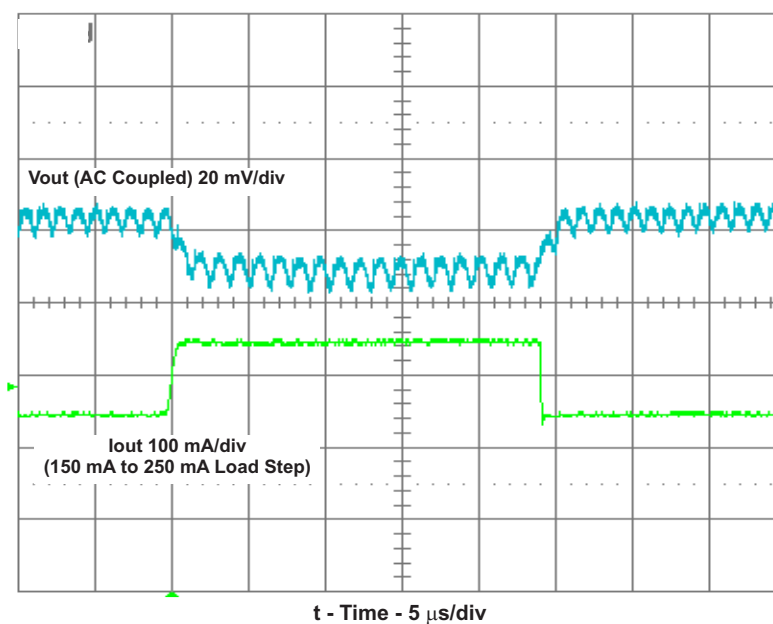


Figure 7. Load Transient Response With Vin = 12 V

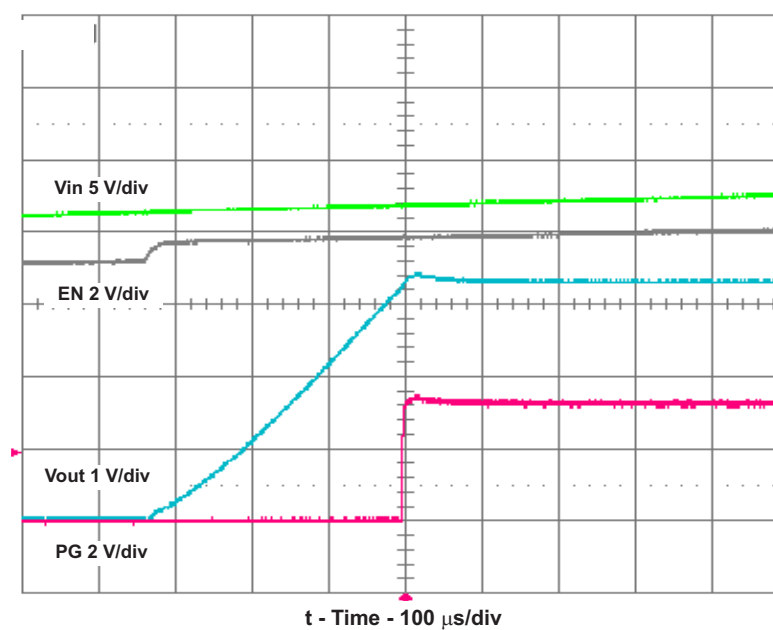


Figure 8. Start-Up on Vin With 250-mA Load

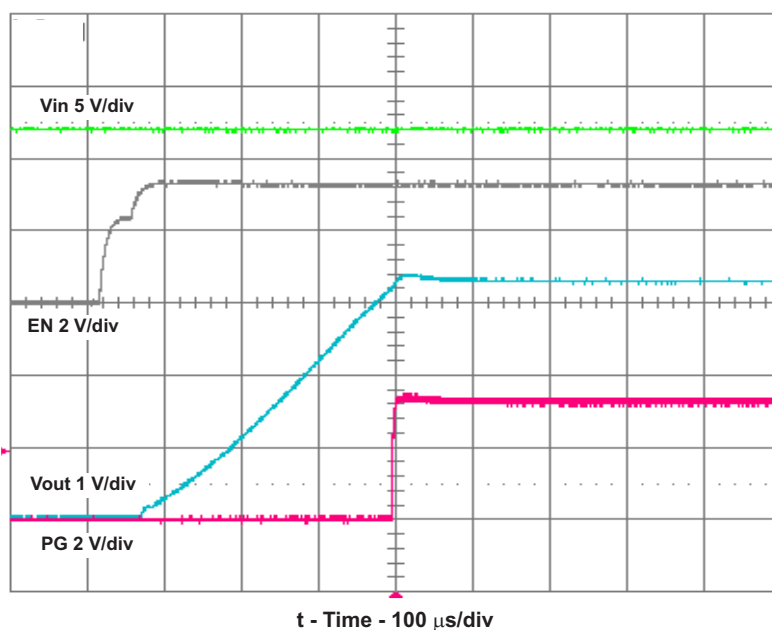


Figure 9. Start-Up on EN With 250-mA Load

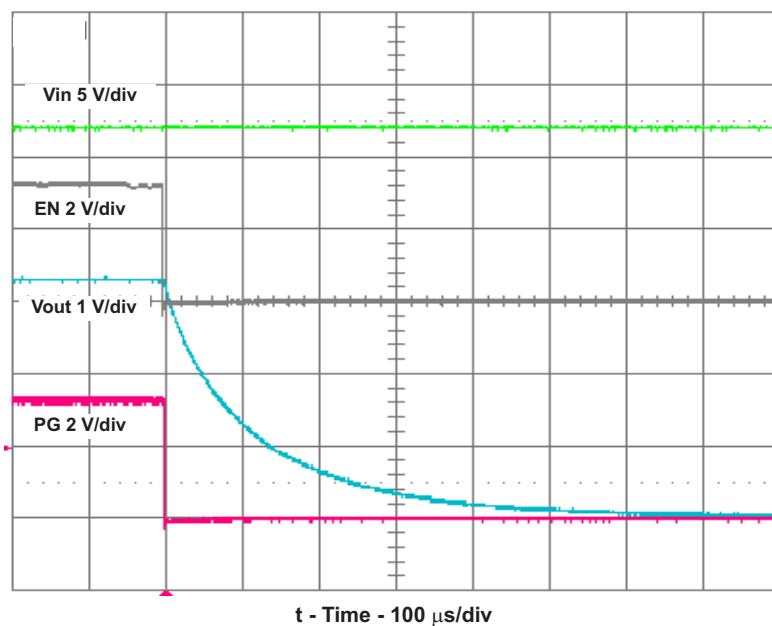


Figure 10. Shutdown on EN With 250-mA Load

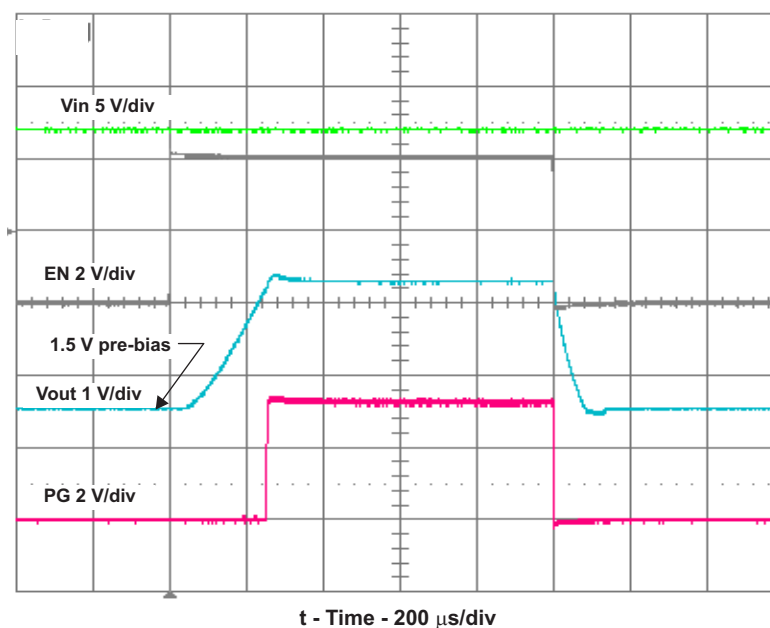


Figure 11. Prebias Start-Up and Shutdown on EN With 250-mA Load

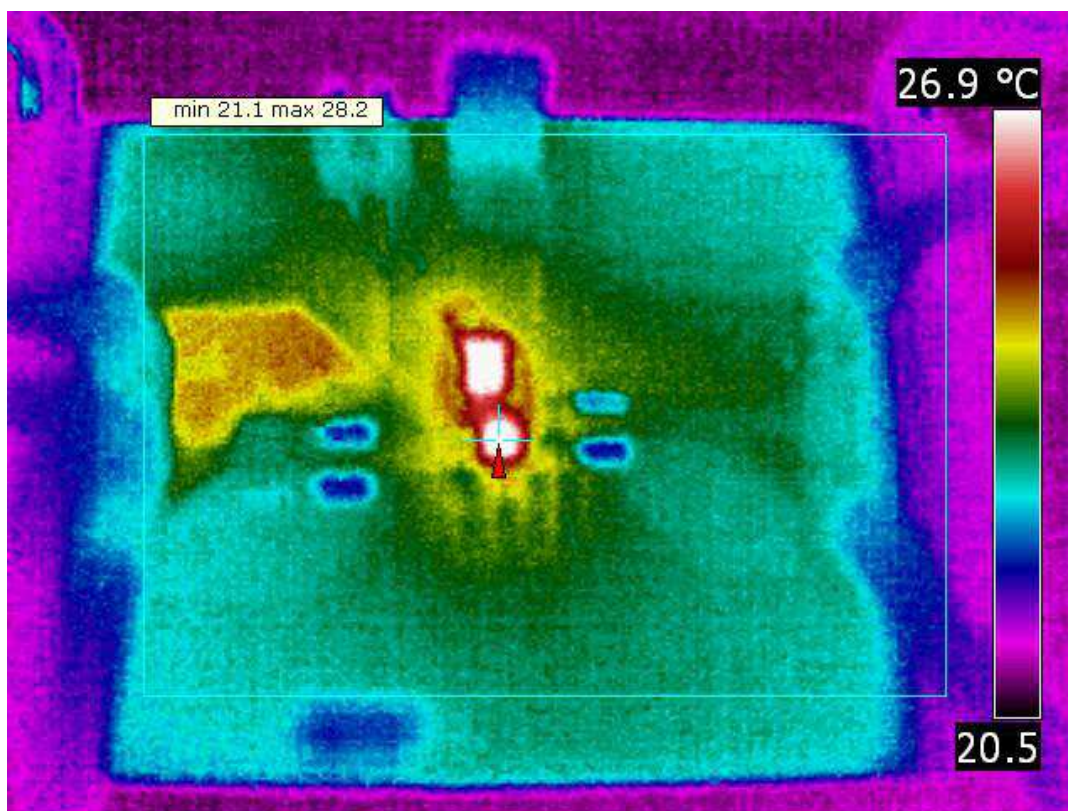


Figure 12. Thermal Performance With Vin = 12 V and Iout = 250 mA

4 Board Layout

This section provides the TPS62125EVM-044 board layout and illustrations.

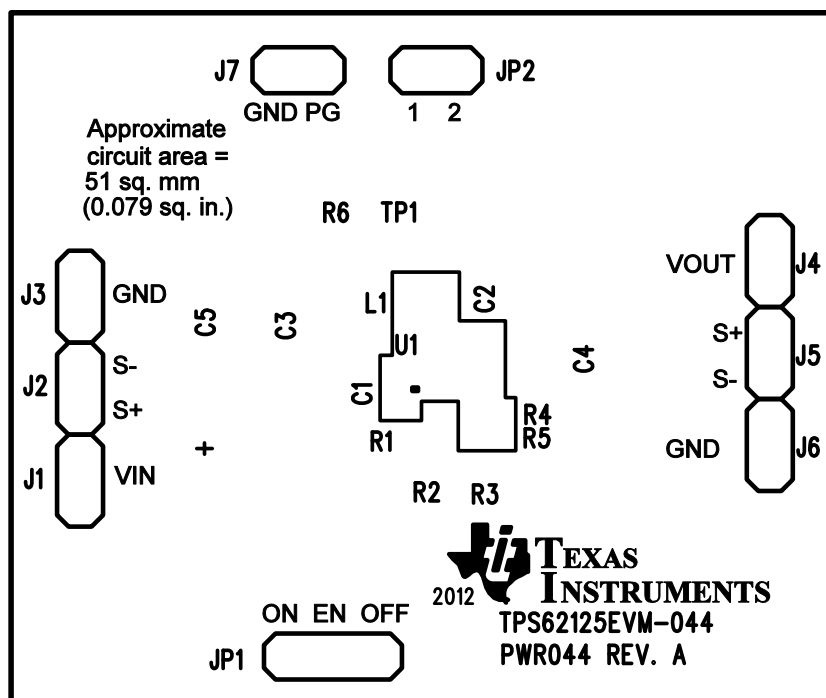


Figure 13. Top Silk Layer

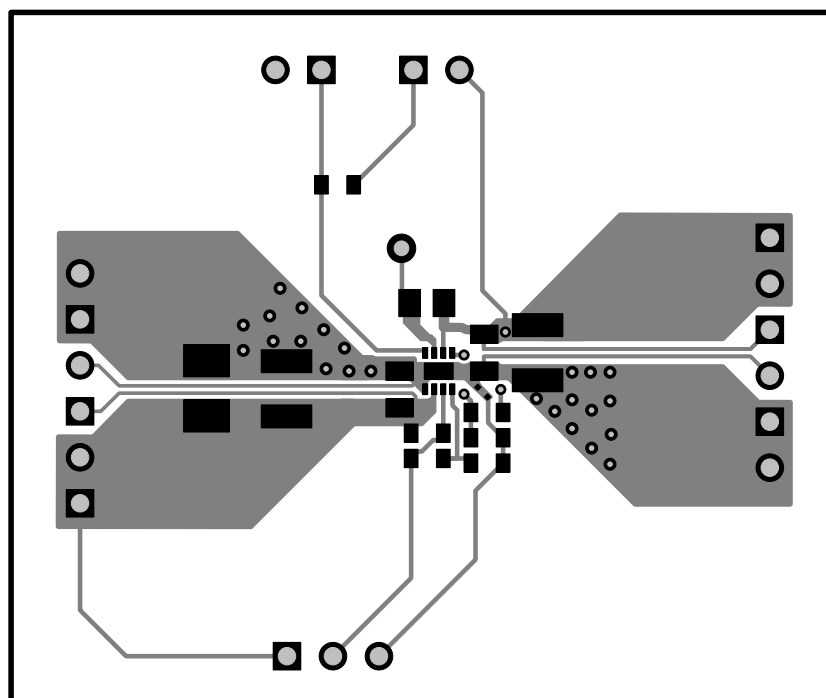


Figure 14. Top Layer

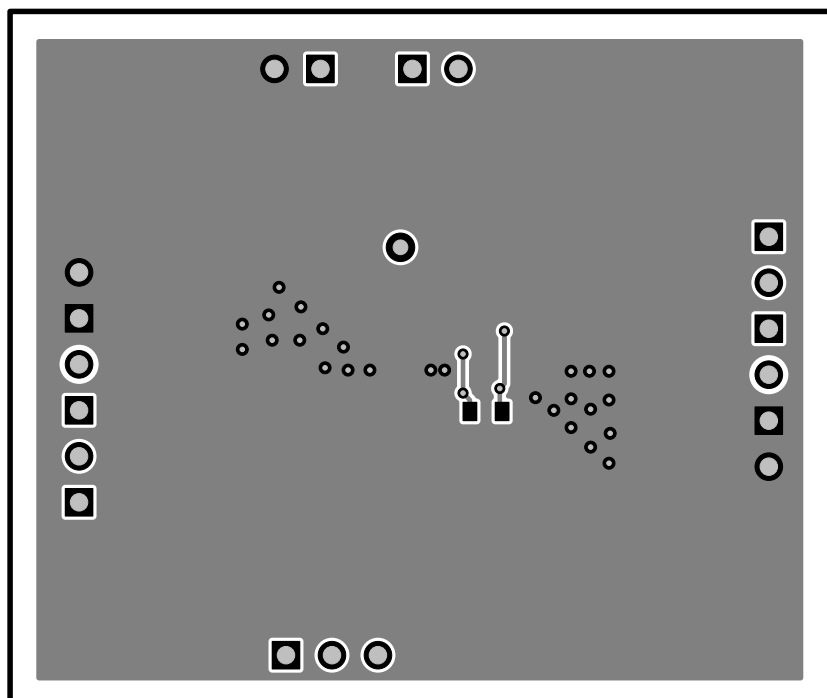


Figure 15. Bottom Layer

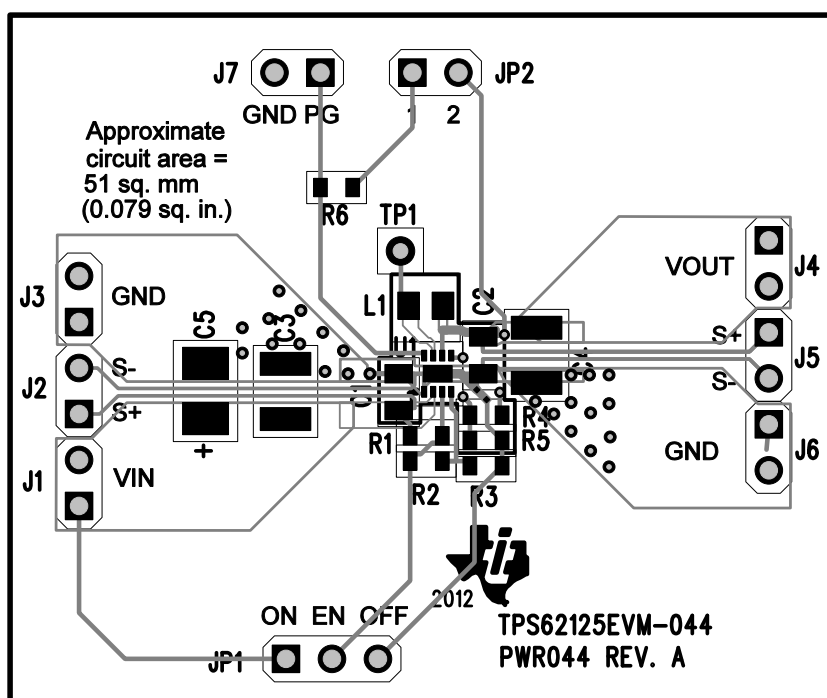


Figure 16. Top Assembly Layer

This section provides the TPS62125EVM-044 schematic and bill of materials.

Figure 17. TPS62125EVM-044 Schematic

5.2 Bill of Materials

Table 2. TPS62125EVM-044 Bill of Materials

Count	RefDes	Value	Description	Size	Part Number	MFR
1	C1	10 μ F	Capacitor, Ceramic, 25V, X5R, \pm 20%	0805	Std	Std
1	C2	10 μ F	Capacitor, Ceramic, 16V, X5R, \pm 20%	0805	Std	Std
1	C5	22 μ F	Capacitor, POSCAP, 20V, \pm 20%	3528[B]	20TQC22MYFB	Sanyo
1	L1	15 μ H	Inductor, Power, 0.56 A, 275-m Ω , \pm 20%	3 X 2.5 mm	VLF302515MT-150M	TDK
2	R1, R4	1.8M	Resistor, Chip, 1/16W, 1%	0603	Std	Std
1	R2	442k	Resistor, Chip, 1/16W, 1%	0603	Std	Std
1	R3	274k	Resistor, Chip, 1/16W, 1%	0603	Std	Std
1	R5	576k	Resistor, Chip, 1/16W, 1%	0603	Std	Std
1	R6	100k	Resistor, Chip, 1/16W, 1%	0603	Std	Std
1	U1	TPS62125	IC, 3V-17V, 300mA Buck Converter with Adjustable Enable Threshold and Hysteresis	WSON-8	TPS62125DSG	TI

The TPS62125EVM-044 may be populated with TPS62125 (U1) devices that do not contain the correct top side markings on the top of the device itself. These devices are still fully tested TPS62125 devices and meet the specified electrical characteristics in the datasheet.

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General Statement for EVMs including a radio

User Power/Frequency Use Obligations: This radio is intended for development/professional use only in legally allocated frequency and power limits. Any use of radio frequencies and/or power availability of this EVM and its development application(s) must comply with local laws governing radio spectrum allocation and power limits for this evaluation module. It is the user's sole responsibility to only operate this radio in legally acceptable frequency space and within legally mandated power limitations. Any exceptions to this are strictly prohibited and unauthorized by Texas Instruments unless user has obtained appropriate experimental/development licenses from local regulatory authorities, which is responsibility of user including its acceptable authorization.

For EVMs annotated as FCC – FEDERAL COMMUNICATIONS COMMISSION Part 15 Compliant

Caution

This device complies with part 15 of the FCC Rules. Operation is subject to the following two conditions: (1) This device may not cause harmful interference, and (2) this device must accept any interference received, including interference that may cause undesired operation.

Changes or modifications not expressly approved by the party responsible for compliance could void the user's authority to operate the equipment.

FCC Interference Statement for Class A EVM devices

This equipment has been tested and found to comply with the limits for a Class A digital device, pursuant to part 15 of the FCC Rules. These limits are designed to provide reasonable protection against harmful interference when the equipment is operated in a commercial environment. This equipment generates, uses, and can radiate radio frequency energy and, if not installed and used in accordance with the instruction manual, may cause harmful interference to radio communications. Operation of this equipment in a residential area is likely to cause harmful interference in which case the user will be required to correct the interference at his own expense.

FCC Interference Statement for Class B EVM devices

This equipment has been tested and found to comply with the limits for a Class B digital device, pursuant to part 15 of the FCC Rules. These limits are designed to provide reasonable protection against harmful interference in a residential installation. This equipment generates, uses and can radiate radio frequency energy and, if not installed and used in accordance with the instructions, may cause harmful interference to radio communications. However, there is no guarantee that interference will not occur in a particular installation. If this equipment does cause harmful interference to radio or television reception, which can be determined by turning the equipment off and on, the user is encouraged to try to correct the interference by one or more of the following measures:

- Reorient or relocate the receiving antenna.
- Increase the separation between the equipment and receiver.
- Connect the equipment into an outlet on a circuit different from that to which the receiver is connected.
- Consult the dealer or an experienced radio/TV technician for help.

For EVMs annotated as IC – INDUSTRY CANADA Compliant

This Class A or B digital apparatus complies with Canadian ICES-003.

Changes or modifications not expressly approved by the party responsible for compliance could void the user's authority to operate the equipment.

Concerning EVMs including radio transmitters

This device complies with Industry Canada licence-exempt RSS standard(s). Operation is subject to the following two conditions: (1) this device may not cause interference, and (2) this device must accept any interference, including interference that may cause undesired operation of the device.

Concerning EVMs including detachable antennas

Under Industry Canada regulations, this radio transmitter may only operate using an antenna of a type and maximum (or lesser) gain approved for the transmitter by Industry Canada. To reduce potential radio interference to other users, the antenna type and its gain should be so chosen that the equivalent isotropically radiated power (e.i.r.p.) is not more than that necessary for successful communication.

This radio transmitter has been approved by Industry Canada to operate with the antenna types listed in the user guide with the maximum permissible gain and required antenna impedance for each antenna type indicated. Antenna types not included in this list, having a gain greater than the maximum gain indicated for that type, are strictly prohibited for use with this device.

Cet appareil numérique de la classe A ou B est conforme à la norme NMB-003 du Canada.

Les changements ou les modifications pas expressément approuvés par la partie responsable de la conformité ont pu vider l'autorité de l'utilisateur pour actionner l'équipement.

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Le présent appareil est conforme aux CNR d'Industrie Canada applicables aux appareils radio exempts de licence. L'exploitation est autorisée aux deux conditions suivantes : (1) l'appareil ne doit pas produire de brouillage, et (2) l'utilisateur de l'appareil doit accepter tout brouillage radioélectrique subi, même si le brouillage est susceptible d'en compromettre le fonctionnement.

Concernant les EVMs avec antennes détachables

Conformément à la réglementation d'Industrie Canada, le présent émetteur radio peut fonctionner avec une antenne d'un type et d'un gain maximal (ou inférieur) approuvé pour l'émetteur par Industrie Canada. Dans le but de réduire les risques de brouillage radioélectrique à l'intention des autres utilisateurs, il faut choisir le type d'antenne et son gain de sorte que la puissance isotrope rayonnée équivalente (p.i.r.e.) ne dépasse pas l'intensité nécessaire à l'établissement d'une communication satisfaisante.

Le présent émetteur radio a été approuvé par Industrie Canada pour fonctionner avec les types d'antenne énumérés dans le manuel d'usage et ayant un gain admissible maximal et l'impédance requise pour chaque type d'antenne. Les types d'antenne non inclus dans cette liste, ou dont le gain est supérieur au gain maximal indiqué, sont strictement interdits pour l'exploitation de l'émetteur.

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If you use this product in Japan, you are required by Radio Law of Japan to follow the instructions below with respect to this product:

1. Use this product in a shielded room or any other test facility as defined in the notification #173 issued by Ministry of Internal Affairs and Communications on March 28, 2006, based on Sub-section 1.1 of Article 6 of the Ministry's Rule for Enforcement of Radio Law of Japan,
2. Use this product only after you obtained the license of Test Radio Station as provided in Radio Law of Japan with respect to this product, or
3. Use of this product only after you obtained the Technical Regulations Conformity Certification as provided in Radio Law of Japan with respect to this product. Also, please do not transfer this product, unless you give the same notice above to the transferee. Please note that if you could not follow the instructions above, you will be subject to penalties of Radio Law of Japan.

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4. You will take care of proper disposal and recycling of the EVM's electronic components and packing materials.

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