

Octal Line Receiver

FEATURES

- Meets EIA 232E/423A/422A and CCITT V.10,V.11, V.28, X.26, X.27
- Single +5V Supply--TTL Compatible Outputs
- Differential Inputs Withstand ± 25V
- Low Open Circuit Voltage for Improved Failsafe Characteristic
- Reduced Supply Current--35 mA Max
- Input Noise Filter
- Internal Hysteresis

DESCRIPTION

The UC5180C is an octal line receiver designed to meet a wide range of digital communications requirements as outlined in EIA standards EIA232E, EIA423A, EIA422A, and CCITT V.10, V.11, V.28, X.26, and X.27. The UC5180C includes an input noise filter and is intended for applications employing data rates up to 200 KBPS. A failsafe function allows these devices to "fail" to a known state under a wide variety of fault conditions at the inputs.

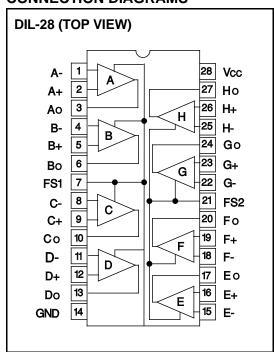
ABSOLUTE MAXIMUM RATINGS (Note 1)

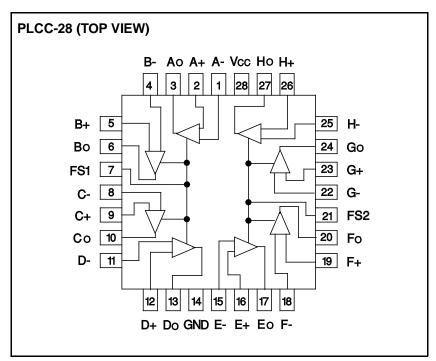
Supply Voltage, Vcc	7V
Output Sink Current	50 mA
Output Short Circuit Time	1 Sec
Common Mode Input Range	15V
Differential Input Range	25V
Failsafe Voltage	0.3 to Vcc
PLCC Power Dissipation, TA = 25°C (Note 2)	1000 mW
DIP Power Dissipation, TA = 25°C (Note 2)	1200 mW
Storage Temperature Range	65°C to +150°C
Lead Temperature (Soldering, 10 Seconds)	300°C
Note 1: All voltages are with respect to ground pin 14 Current	s are positive

Note 1: All voltages are with respect to ground, pin 14. Currents are positive into, negative out of the specified terminal

Note 2: Consult Packaging Section of Databook for thermal limitations and considerations of package.

CONNECTION DIAGRAMS





DC ELECTRICAL CHARACTERISTICS: Unless otherwise stated these specifications apply for TA = 0°C to +70°C, Vcc = $5V \pm 5\%$, Input Common Mode Range $\pm 7V$, TA =TJ

PARAMETERS	SYMBOL	TEST C	UC5180C		UNITS		
					MIN	MAX	
DC Input Resistance	RIN	3V ≤ VIN ≤ 25V				7	kΩ
Failsafe Output Voltage	Vofs	Inputs Open or Shorted	$\begin{array}{l} 0 \leq \text{IOUT} \leq 8\text{mA}, \ \text{VFAILSAFE} = 0 \text{V} \\ 0 \geq \text{IOUT} \geq \text{-} \ 400 \ \mu\text{A}, \\ \text{VFAILSAFE} = \text{VCC} \end{array}$			0.45	V
		Together, or One Input Open and One Grounded			2.7		
Differential Input High	Vтн	Vout = 2.7V, lout = 440 μA		Rs = 0 (Note 2)	50	200	mV
Threshold		(See Figure 1)	Rs = 500 (Note 2)		400		
Differential Input Low	VTL	Vout = 0.45V, Iout = 440 mA		Rs = 0 (Note 2)	-200	-50	mV
Threshold		(See Figure 1)		Rs = 500 (Note 2)	-400		
Hysteresis	VH	Fs = 0V or Vcc (See Figure 1)				140	mV
Open Circuit Input Voltage	Vicc					75	mV
Input Capacitance	Сі					20	pF
High Level Output Voltage	Vсн	VID = 1V, IOUT = - 440μA	2.7		V		
Low Level Output Voltage	Vol	VID = -1V		IOUT = 4 mA		0.4	V
		(Note 3)		IOUT = 8 mA		0.45	
Short Circuit Output Current	los	Note 4 4.75V ≤ Vcc ≤ 5.25V				100	mA
Supply Current	Icc					35	mA
Input Current	lın	Other Inputs Grounded		VIN = +10V		3.25	mA
				VIN = -10V	-3.25		

Note 2: Rs is a resistor in series with each input.

Note 3: Measured after 100ms warm up (at 0°C)

Note 4: Only 1 output may be shorted at one time and then only for a maximum of 1 sec.

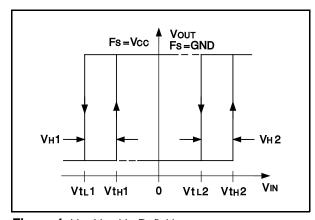


Figure 1. VtL, VtH, VH Definition

AC ELECTRICAL CHARACTERISTICS: $Vcc = 5V \pm 5\%$, TA = 0°C to + 70°C, Figure 2, TA = TJ.

PARAMETERS	SYMBOL	TEST CONDITIONS	UC5180C		UNITS
			MIN	MAX	
Propagation Delay - Low to High	tPLH	$CL = 50pF, VIN = \pm 500mV$		550	ns
Propagation Delay - High to Low	tPHL	$CL = 50pF, VIN = \pm 500mV$		550	ns
Acceptance Input Frequency	fA	Unused Input Grounded, VIN = ± 200mV		0.1	MHz
Rejectable Input Frequency	fR	Unused Input Grounded, VIN = ± 500mV	5.5		MHz

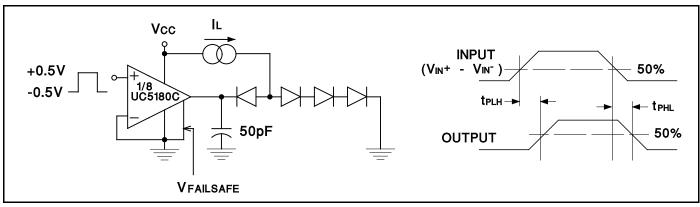


Figure 2. AC Test Circuit

APPLICATIONS INFORMATION

Failsafe Operation

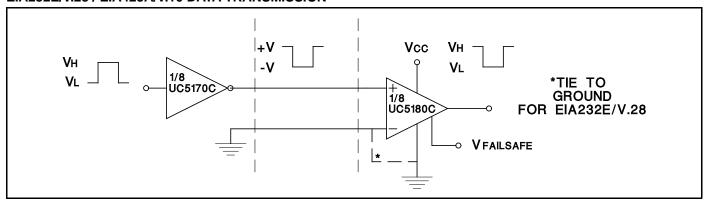
These devices provide a failsafe operating mode to guard against input fault conditions as defined in EIA422A and EIA423A standards. These fault conditions are (1) drive in power-off condition, (2) receiver not interconnected with driver, (3) open-circuited interconnecting cable, and (4) short-circuited interconnecting cable. If one of these four fault conditions occurs at the inputs of a receiver, then the output of that receiver is driven to a known logic level. The receiver is programmed by connecting the failsafe input to Vcc or ground. A connection to Vcc provides a logic "1" output

under fault conditions, while a connection to ground provides a logic "0". There are two failsafe pins (Fs1 and Fs2) on the UC5180C where each provides common failsafe control for four receivers.

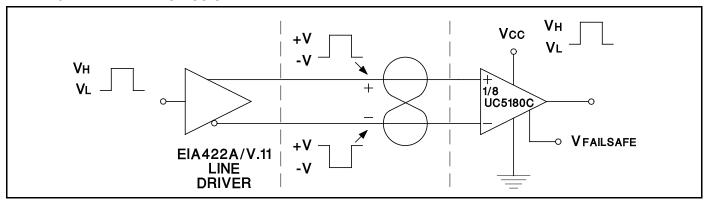
Input Filtering (UC5180C)

The UC5180C has input filtering for additional noise rejection. This filtering is a function of both signal level and frequency. For the specified input (5.5 MHz at ± 500 mV) the input stage filter attenuates the signal such that the output stage threshold levels are not exceeded and no change of state occurs at the output.

EIA232E/V.28 / EIA423A/V.10 DATA TRANSMISSION



EIA422A/V.11 DATA TRANSMISSION



UNITRODE INTEGRATED CIRCUITS
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PACKAGE OPTION ADDENDUM

7-Oct-2013

PACKAGING INFORMATION

www.ti.com

Orderable Device	Status	Package Type	Package	Pins	Package	Eco Plan	Lead/Ball Finish	MSL Peak Temp	Op Temp (°C)	Device Marking	Samples
	(1)		Drawing		Qty	(2)		(3)		(4/5)	
UC5180CJ	OBSOLETE		UTR			TBD	Call TI	Call TI			
UC5180CQ	LIFEBUY	PLCC	FN	28	37	Green (RoHS	CU NIPDAU	Level-3-260C-168 HR		UC5180CQ	
						& no Sb/Br)					
UC5180CQTR	OBSOLETE	PLCC	FN	28		TBD	Call TI	Call TI		UC5180CQ	

(1) The marketing status values are defined as follows:

ACTIVE: Product device recommended for new designs.

LIFEBUY: TI has announced that the device will be discontinued, and a lifetime-buy period is in effect.

NRND: Not recommended for new designs. Device is in production to support existing customers, but TI does not recommend using this part in a new design.

PREVIEW: Device has been announced but is not in production. Samples may or may not be available.

OBSOLETE: TI has discontinued the production of the device.

(2) Eco Plan - The planned eco-friendly classification: Pb-Free (RoHS), Pb-Free (RoHS Exempt), or Green (RoHS & no Sb/Br) - please check http://www.ti.com/productcontent for the latest availability information and additional product content details.

TBD: The Pb-Free/Green conversion plan has not been defined.

Pb-Free (RoHS): TI's terms "Lead-Free" or "Pb-Free" mean semiconductor products that are compatible with the current RoHS requirements for all 6 substances, including the requirement that lead not exceed 0.1% by weight in homogeneous materials. Where designed to be soldered at high temperatures, TI Pb-Free products are suitable for use in specified lead-free processes.

Pb-Free (RoHS Exempt): This component has a RoHS exemption for either 1) lead-based flip-chip solder bumps used between the die and package, or 2) lead-based die adhesive used between the die and leadframe. The component is otherwise considered Pb-Free (RoHS compatible) as defined above.

Green (RoHS & no Sb/Br): TI defines "Green" to mean Pb-Free (RoHS compatible), and free of Bromine (Br) and Antimony (Sb) based flame retardants (Br or Sb do not exceed 0.1% by weight in homogeneous material)

- (3) MSL, Peak Temp. -- The Moisture Sensitivity Level rating according to the JEDEC industry standard classifications, and peak solder temperature.
- (4) There may be additional marking, which relates to the logo, the lot trace code information, or the environmental category on the device.
- (5) Multiple Device Markings will be inside parentheses. Only one Device Marking contained in parentheses and separated by a "~" will appear on a device. If a line is indented then it is a continuation of the previous line and the two combined represent the entire Device Marking for that device.

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