

N-Channel 20-V (D-S) MOSFET with Schottky Diode

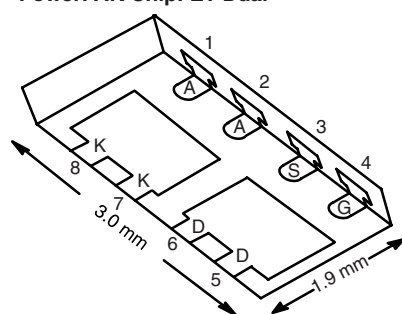
MOSFET PRODUCT SUMMARY

V_{DS} (V)	$R_{DS(on)}$ (Ω)	I_D (A) ^a	Q_g (Typ.)
20	0.039 at $V_{GS} = 4.5$ V	6	6 nC
	0.045 at $V_{GS} = 2.5$ V	6	
	0.055 at $V_{GS} = 1.8$ V	6	

SCHOTTKY PRODUCT SUMMARY

V_{KA} (V)	V_F (V) Diode Forward Voltage	I_F (A) ^a
20	0.375 at 1 A	1

PowerPAK ChipFET Dual



Bottom View

Marking Code



FEATURES

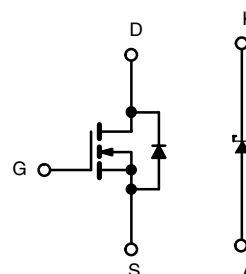
- Halogen-free According to IEC 61249-2-21 Definition
- LITTLE FOOT[®] Plus Power MOSFET
- New Thermally Enhanced PowerPAK[®] ChipFET[®] Package
 - Small Footprint Area
 - Low On-Resistance
 - Thin 0.8 mm Profile
- Compliant to RoHS Directive 2002/95/EC



RoHS
COMPLIANT
HALOGEN
FREE

APPLICATIONS

- Load Switch for Portable Applications
- Ideal for Boost Circuits



N-Channel MOSFET

Ordering Information: Si5858DU-T1-GE3 (Lead (Pb)-free and Halogen-free)

ABSOLUTE MAXIMUM RATINGS $T_A = 25$ °C, unless otherwise noted

Parameter		Symbol	Limit	Unit	
Drain-Source Voltage (MOSFET)		V _{DS}	20	V	
Reverse Voltage (Schottky)		V _{KA}	20		
Gate-Source Voltage (MOSFET)		V _{GS}	± 8		
Continuous Drain Current (T _J = 150 °C) (MOSFET)	T _C = 25 °C	I _D	6 ^a	A	
	T _C = 70 °C		6 ^a		
	T _A = 25 °C		7.2 ^{b, c}		
	T _A = 70 °C		5.8 ^{b, c}		
Pulsed Drain Current (MOSFET)		I _{DM}	20		
Continuous Source Current (MOSFET Diode Conduction)	T _C = 25 °C	I _S	6.9		
	T _A = 25 °C		1.9 ^{b, c}		
Average Forward Current (Schottky)		I _F	1 ^b		
Pulsed Forward Current (Schottky)		I _{FM}	7		
Maximum Power Dissipation (MOSFET)	T _C = 25 °C	P _D	8.3	W	
	T _C = 70 °C		5.3		
	T _A = 25 °C		2.3 ^{b, c}		
	T _A = 70 °C		1.5 ^{b, c}		
Maximum Power Dissipation (Schottky)	T _C = 25 °C	P _D	7.8	W	
	T _C = 70 °C		5		
	T _A = 25 °C		2.1 ^{b, c}		
	T _A = 70 °C		1.3 ^{b, c}		
Operating Junction and Storage Temperature Range		T _J , T _{stg}	- 55 to 150	°C	
Soldering Recommendations (Peak Temperature) ^{d, e}			260		

THERMAL RESISTANCE RATINGS

Parameter	Symbol	Typical	Maximum	Unit
Maximum Junction-to-Ambient (MOSFET) ^{b, f}	R_{thJA}	45	55	°C/W
Maximum Junction-to-Case (Drain) (MOSFET)	R_{thJC}	12	15	
Maximum Junction-to-Ambient (Schottky)	R_{thJA}	49	61	
Maximum Junction-to-Case (Drain) (Schottky)	R_{thJC}	13	16	

Notes:

a. Package limited.

b. Surface Mounted on FR4 board.

c. $t = 5$ s.d. See Solder Profile (www.vishay.com/ppg?73257). The PowerPAK ChipFET is a leadless package. The end of the lead terminal is exposed copper (not plated) as a result of the singulation process in manufacturing. A solder fillet at the exposed copper tip cannot be guaranteed and is not required to ensure adequate bottom side solder interconnection.

e. Rework Conditions: manual soldering with a soldering iron is not recommended for leadless components.

f. Maximum under Steady State conditions for MOSFETS is 105 °C/W.

g. Maximum under Steady State conditions for Schottky is 110 °C/W.

SPECIFICATIONS $T_J = 25$ °C, unless otherwise noted

Parameter	Symbol	Test Conditions	Min.	Typ.	Max.	Unit
Static						
Drain-Source Breakdown Voltage	V_{DS}	$V_{GS} = 0$ V, $I_D = 250$ μ A	20			V
V_{DS} Temperature Coefficient	$\Delta V_{DS}/T_J$	$I_D = 250$ μ A		17.4		mV/°C
$V_{GS(th)}$ Temperature Coefficient	$\Delta V_{GS(th)}/T_J$			- 2.6		
Gate-Source Threshold Voltage	$V_{GS(th)}$	$V_{DS} = V_{GS}$, $I_D = 250$ μ A	0.4		1.0	V
Gate-Source Leakage	I_{GSS}	$V_{DS} = 0$ V, $V_{GS} = \pm 8$ V			± 100	nA
Zero Gate Voltage Drain Current	I_{DSS}	$V_{DS} = 20$ V, $V_{GS} = 0$ V			- 1	μ A
		$V_{DS} = 20$ V, $V_{GS} = 0$ V, $T_J = 55$ °C			- 10	
On-State Drain Current ^a	$I_{D(on)}$	$V_{DS} \leq 5$ V, $V_{GS} = 4.5$ V	- 20			A
Drain-Source On-State Resistance ^a	$R_{DS(on)}$	$V_{GS} = 4.5$ V, $I_D = 4.4$ A		0.032	0.039	Ω
		$V_{GS} = 2.5$ V, $I_D = 4.1$ A		0.037	0.045	
		$V_{GS} = 1.8$ V, $I_D = 1.8$ A		0.0455	0.055	
Forward Transconductance ^a	g_{fs}	$V_{DS} = 10$ V, $I_D = 4.4$ A		22		S
Dynamic^b						
Input Capacitance	C_{iss}	$V_{DS} = 10$ V, $V_{GS} = 0$ V, $f = 1$ MHz		520		pF
Output Capacitance	C_{oss}			100		
Reverse Transfer Capacitance	C_{rss}			60		
Total Gate Charge	Q_g	$V_{DS} = 10$ V, $V_{GS} = 8$ V, $I_D = 4.4$ A		10.5	16	nC
Gate-Source Charge	Q_{gs}	$V_{DS} = 10$ V, $V_{GS} = 4.5$ V, $I_D = 4.4$ A		6	9	
Gate-Drain Charge	Q_{gd}			0.91		
Gate Resistance	R_g	$f = 1$ MHz		1.9		Ω
Turn-On Delay Time	$t_{d(on)}$	$V_{DD} = 10$ V, $R_L = 2.8$ Ω $I_D \cong 3.6$ A, $V_{GEN} = 4.5$ V, $R_g = 1$ Ω		20	30	ns
Rise Time	t_r			65	100	
Turn-Off Delay Time	$t_{d(off)}$			40	60	
Fall Time	t_f			10	15	
Turn-On Delay Time	$t_{d(on)}$	$V_{DD} = 10$ V, $R_L = 2.8$ Ω $I_D \cong 3.6$ A, $V_{GEN} = 8$ V, $R_g = 1$ Ω		5	10	
Rise Time	t_r			12	20	
Turn-Off Delay Time	$t_{d(off)}$			26	40	
Fall Time	t_f			8	15	



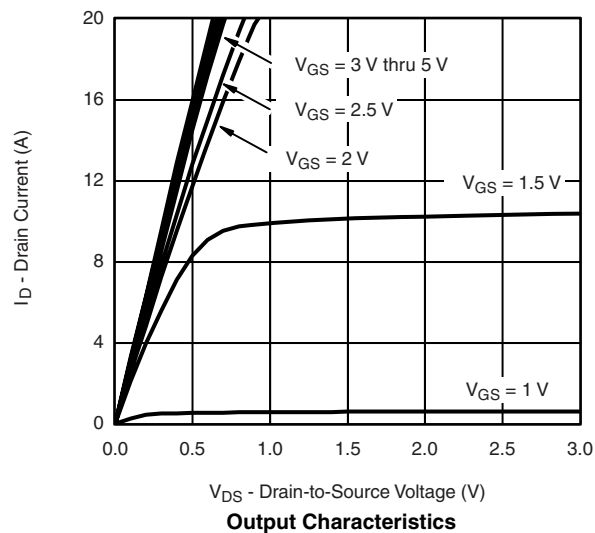
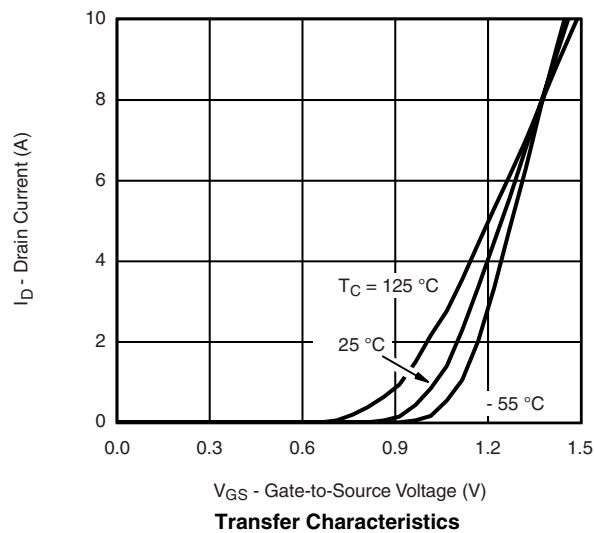
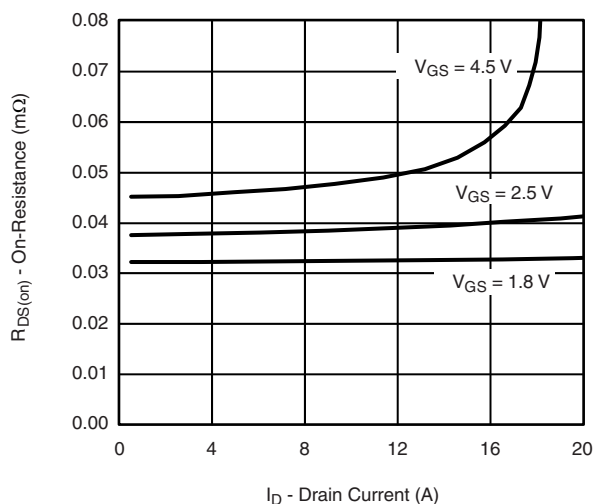
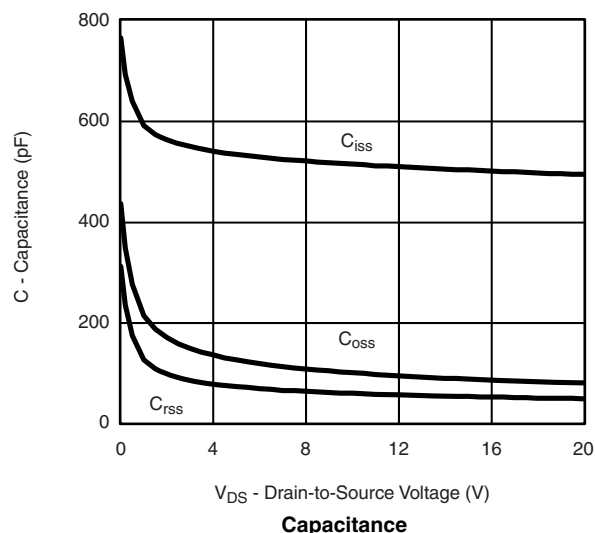
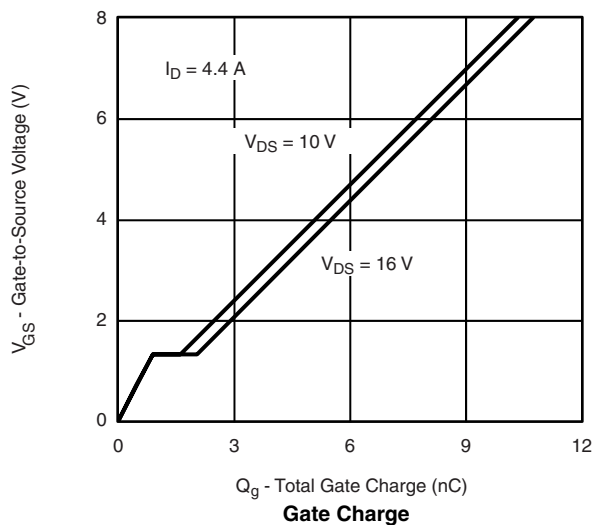
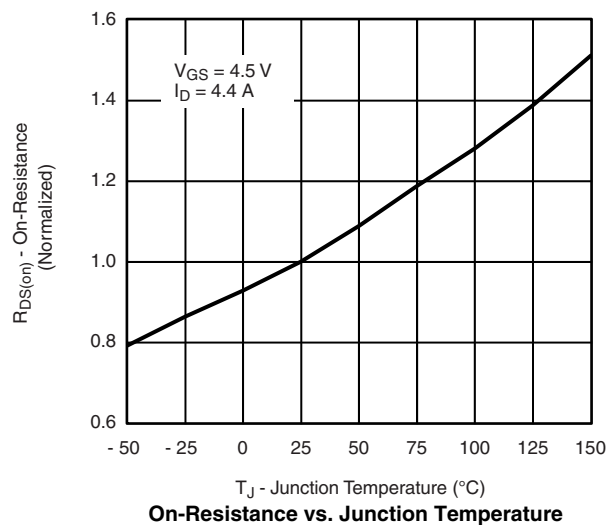
SPECIFICATIONS $T_J = 25\text{ }^{\circ}\text{C}$, unless otherwise noted						
Parameter	Symbol	Test Conditions	Min.	Typ.	Max.	Unit
Drain-Source Body Diode Characteristics						
Continuous Source-Drain Diode Current	I_S	$T_C = 25\text{ }^{\circ}\text{C}$			14.8	A
Pulse Diode Forward Current	I_{SM}				20	
Body Diode Voltage	V_{SD}	$I_S = 1.2\text{ A}$, $V_{GS} = 0\text{ V}$		0.8	1.2	V
Body Diode Reverse Recovery Time	t_{rr}	$I_F = -2\text{ A}$, $dI/dt = 100\text{ A}/\mu\text{s}$, $T_J = 25\text{ }^{\circ}\text{C}$		45	70	ns
Body Diode Reverse Recovery Charge	Q_{rr}			21	32	nC
Reverse Recovery Fall Time	t_a			29		ns
Reverse Recovery Rise Time	t_b			16		

Notes:

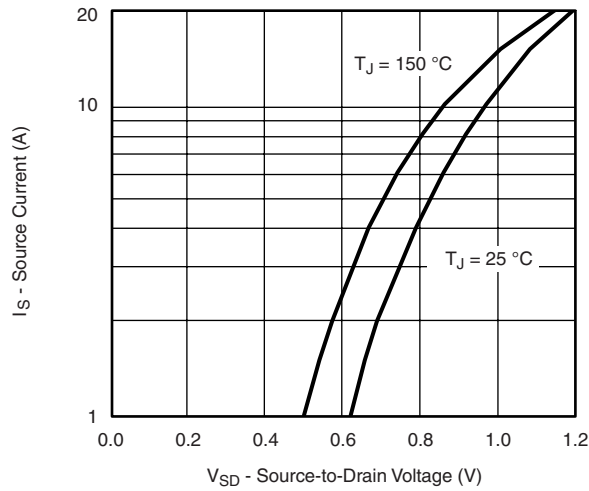
- a. Pulse test; pulse width $\leq 300\text{ }\mu\text{s}$, duty cycle $\leq 2\%$.
b. Guaranteed by design, not subject to production testing.

SCHOTTKY SPECIFICATIONS $T_J = 25\text{ }^{\circ}\text{C}$, unless otherwise noted						
Parameter	Symbol	Test Conditions	Min.	Typ.	Max.	Unit
Forward Voltage Drop	V_F	$I_F = 1\text{ A}$		0.34	0.375	V
		$I_F = 1\text{ A}$, $T_J = 125\text{ }^{\circ}\text{C}$		0.255	0.290	
Maximum Reverse Leakage Current	I_{rm}	$V_R = 20\text{ V}$		0.05	0.500	mA
		$V_R = 20\text{ V}$, $T_J = 85\text{ }^{\circ}\text{C}$		2	20	
		$V_R = 20\text{ V}$, $T_J = 125\text{ }^{\circ}\text{C}$		10	100	
Junction Capacitance	C_T	$V_R = 10\text{ V}$		90		pF

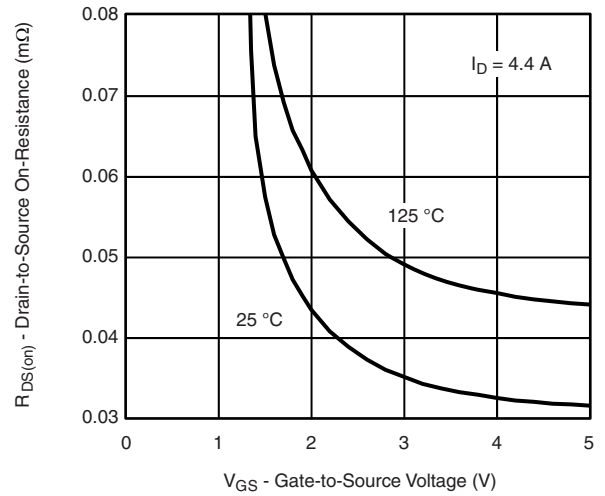
Stresses beyond those listed under "Absolute Maximum Ratings" may cause permanent damage to the device. These are stress ratings only, and functional operation of the device at these or any other conditions beyond those indicated in the operational sections of the specifications is not implied. Exposure to absolute maximum rating conditions for extended periods may affect device reliability.

MOSFET TYPICAL CHARACTERISTICS 25 °C, unless otherwise noted**Output Characteristics****Transfer Characteristics****On-Resistance vs. Drain Current and Gate Voltage****Capacitance****Gate Charge****On-Resistance vs. Junction Temperature**

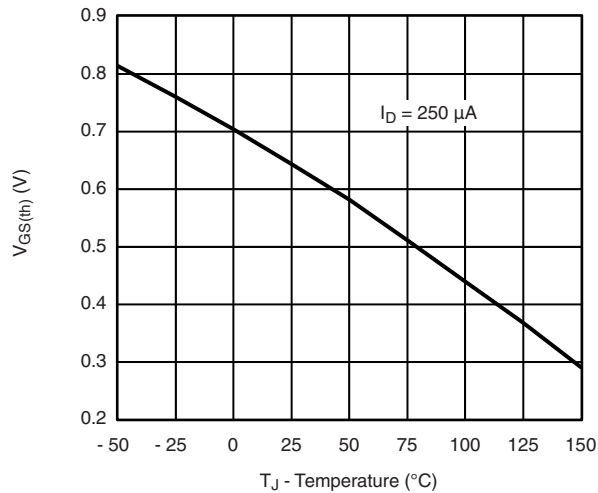
MOSFET TYPICAL CHARACTERISTICS 25 °C, unless otherwise noted



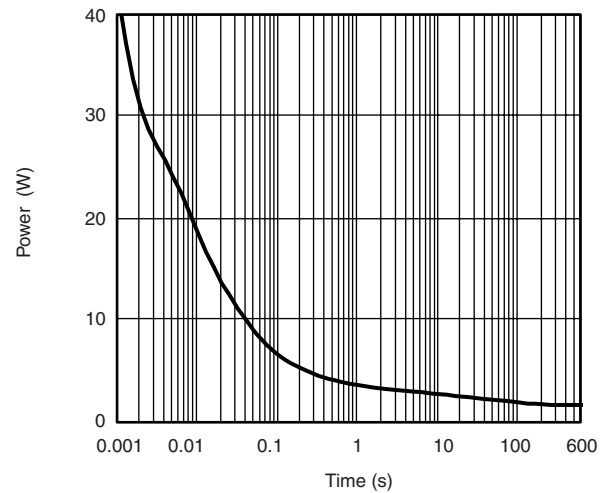
Source-Drain Diode Forward Voltage



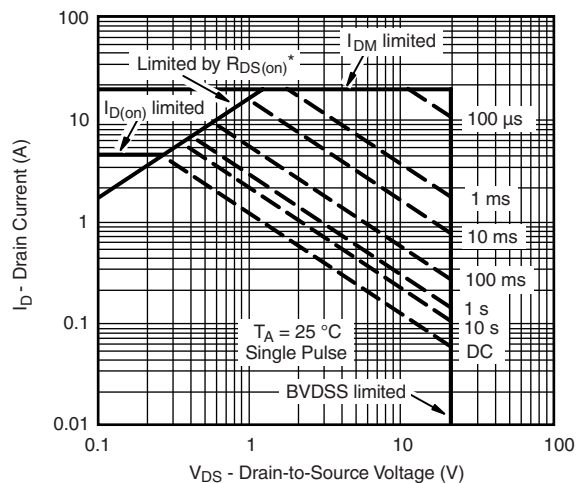
On-Resistance vs. Gate-to-Source Voltage



Threshold Voltage

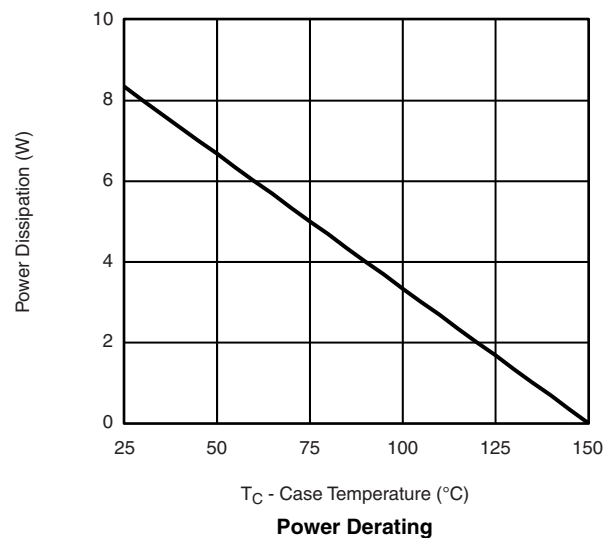
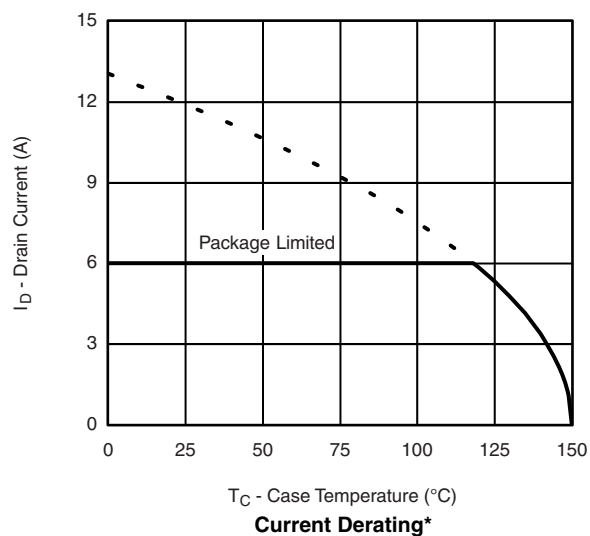


Single Pulse Power, Junction-to-Ambient



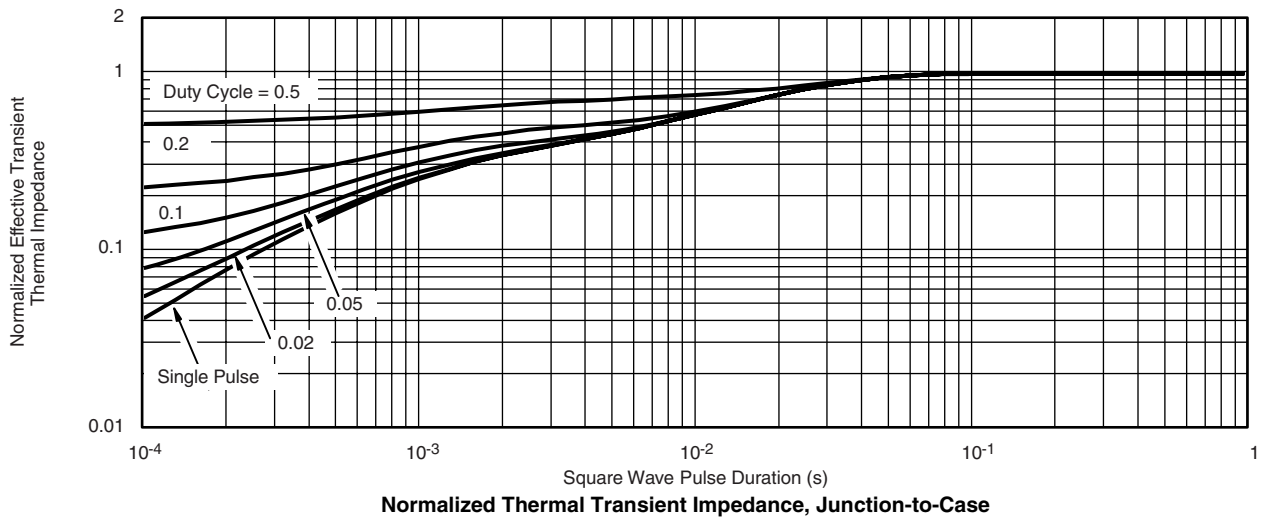
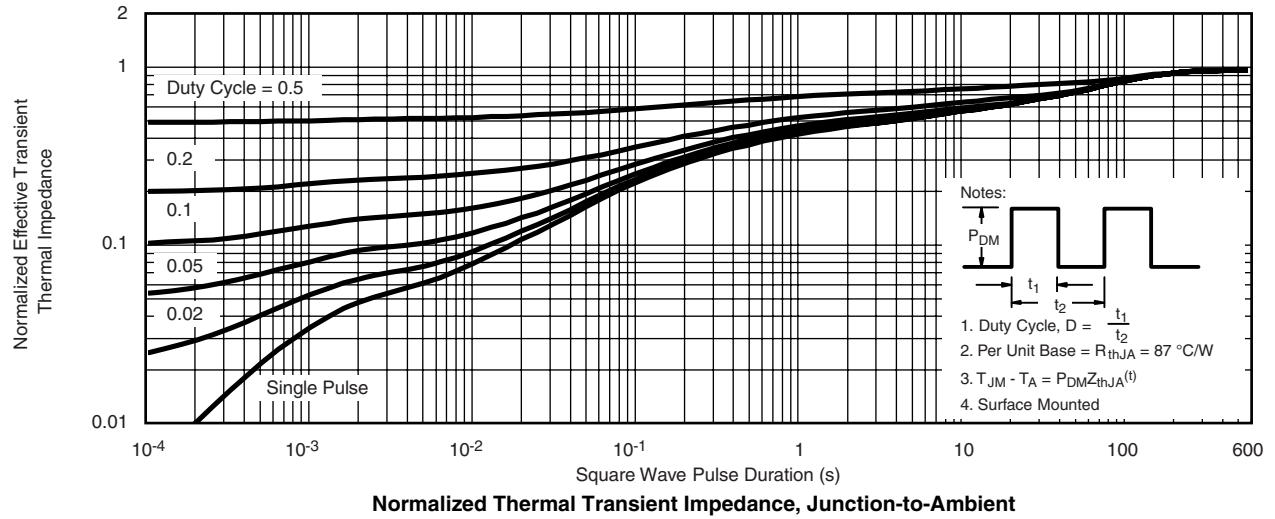
* $V_{GS} >$ minimum V_{GS} at which $R_{DS(on)}$ is specified

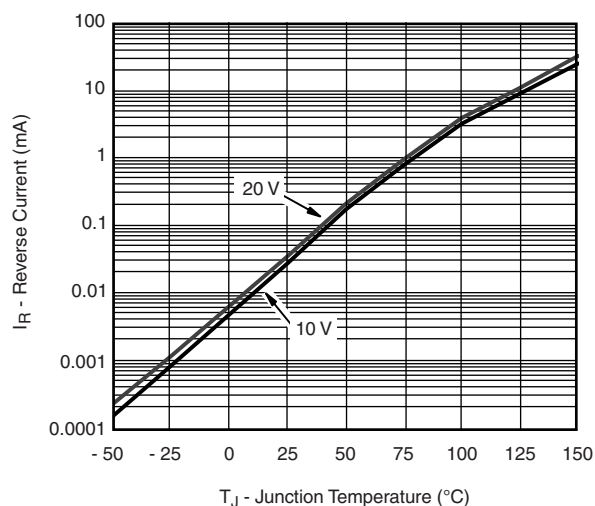
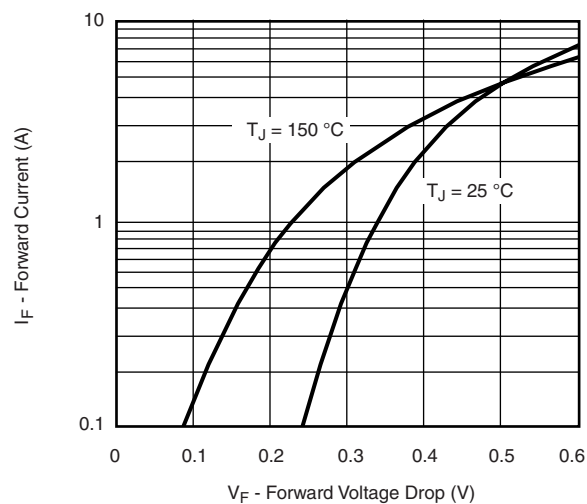
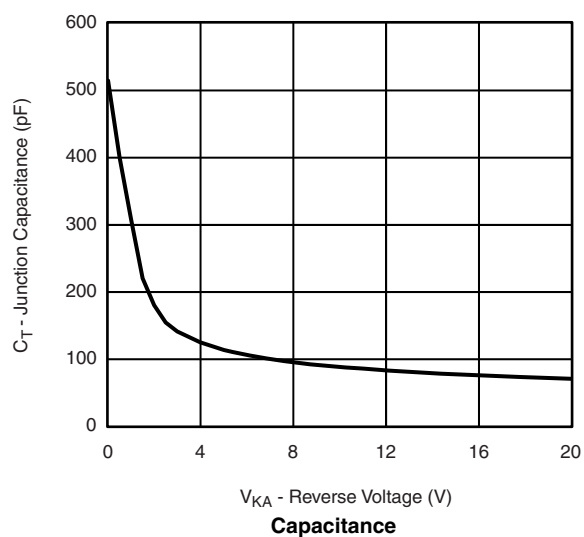
Safe Operating Area, Junction-to-Ambient

MOSFET TYPICAL CHARACTERISTICS 25 °C, unless otherwise noted

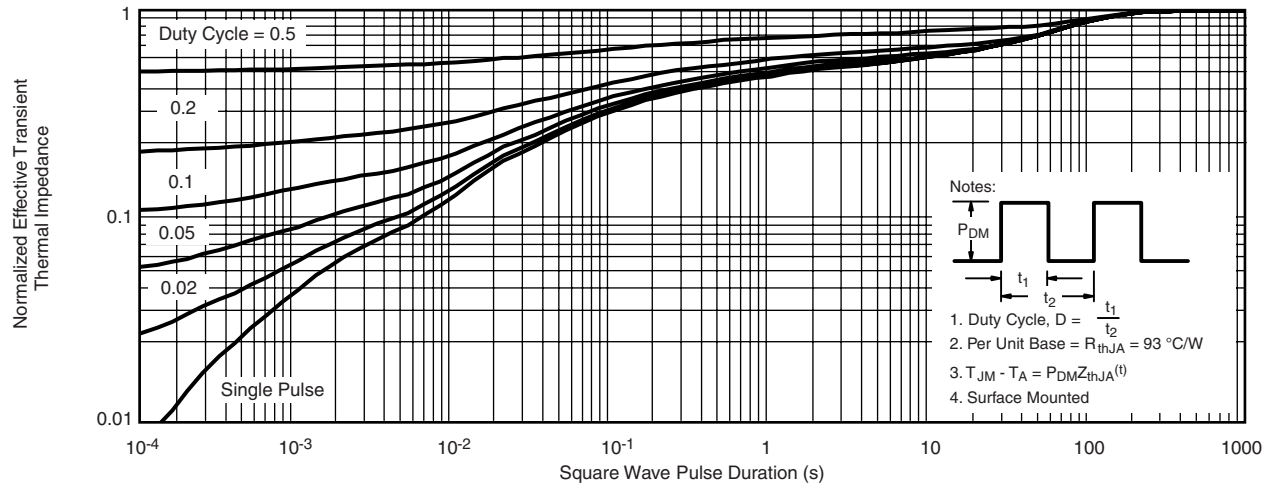
* The power dissipation P_D is based on $T_{J(max)} = 150$ °C, using junction-to-case thermal resistance, and is more useful in settling the upper dissipation limit for cases where additional heatsinking is used. It is used to determine the current rating, when this rating falls below the package limit.

MOSFET TYPICAL CHARACTERISTICS 25 °C, unless otherwise noted

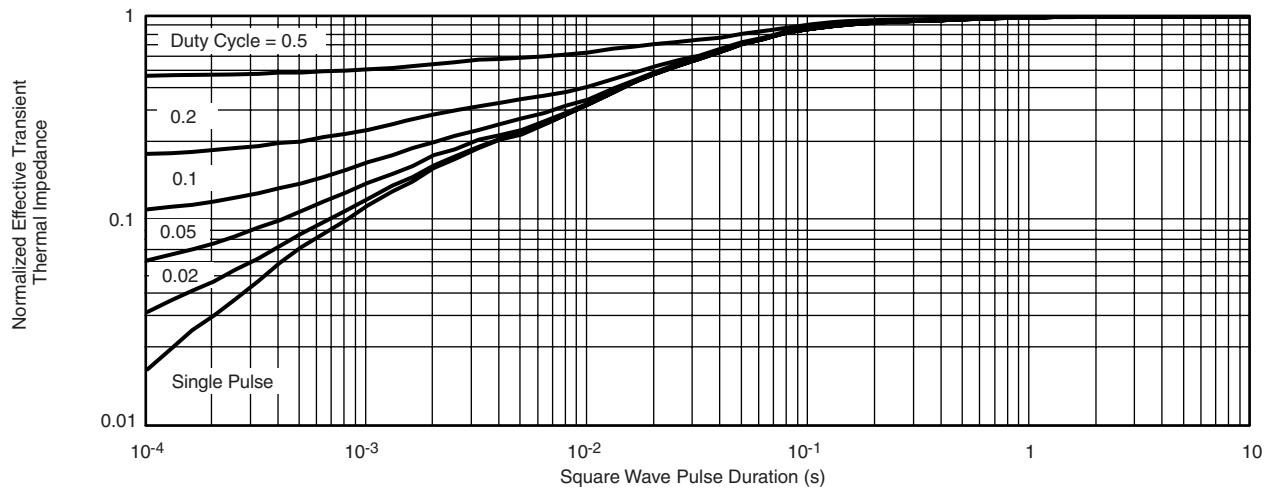


SCHOTTKY TYPICAL CHARACTERISTICS 25 °C, unless otherwise noted**Reverse Current vs. Junction Temperature****Forward Voltage Drop****Capacitance**

SCHOTTKY TYPICAL CHARACTERISTICS 25 °C, unless otherwise noted



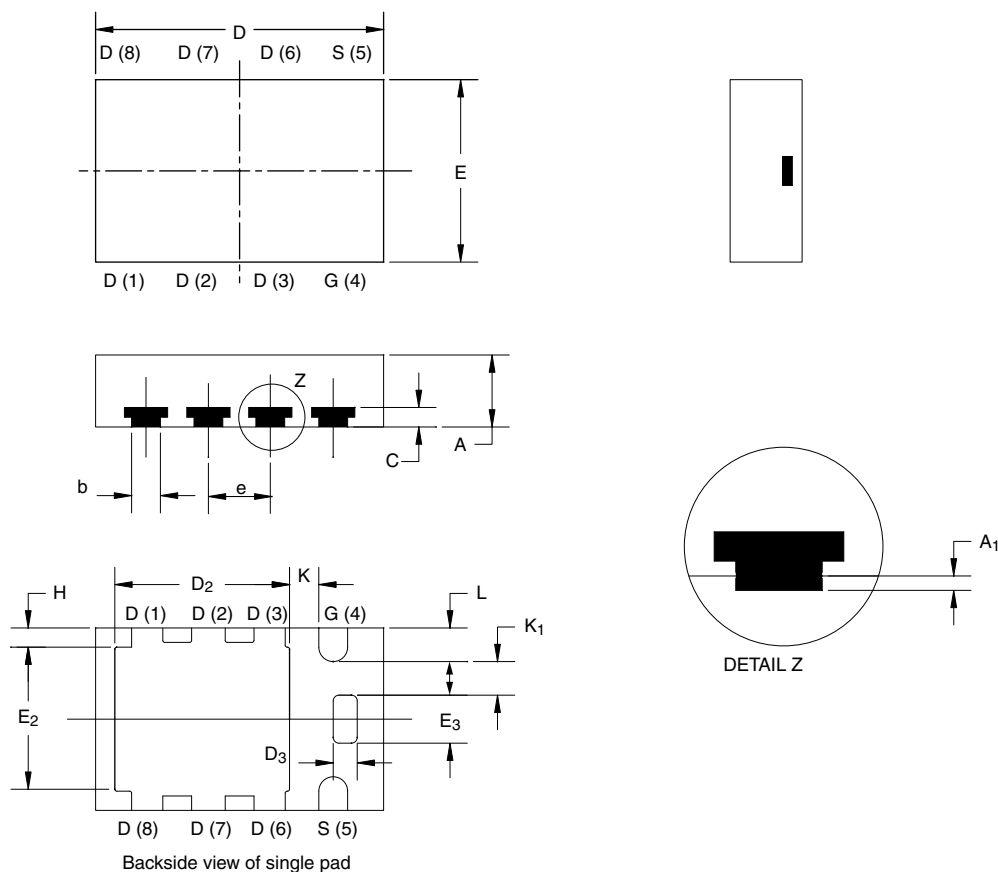
Normalized Thermal Transient Impedance, Junction-to-Ambient



Normalized Thermal Transient Impedance, Junction-to-Foot

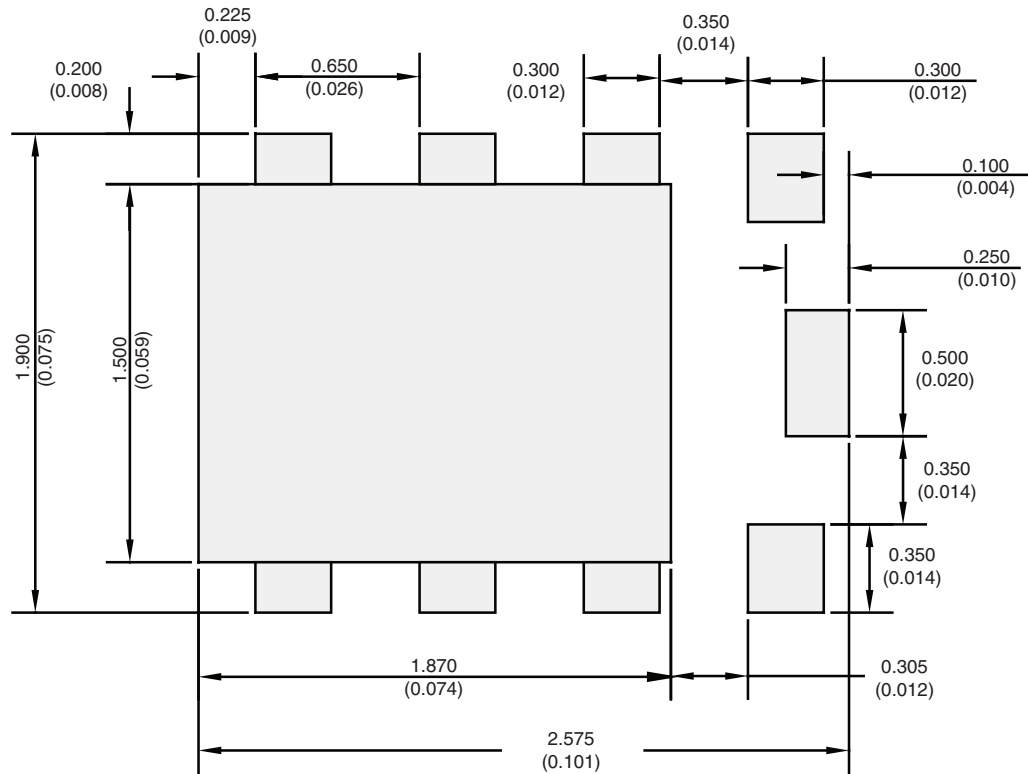
Vishay Siliconix maintains worldwide manufacturing capability. Products may be manufactured at one of several qualified locations. Reliability data for Silicon Technology and Package Reliability represent a composite of all qualified locations. For related documents such as package/tape drawings, part marking, and reliability data, see www.vishay.com/ppg?73460.

PowerPAK® ChipFET® SINGLE PAD



DIM.	MILLIMETERS			INCHES		
	MIN.	NOM.	MAX.	MIN.	NOM.	MAX.
A	0.70	0.75	0.85	0.028	0.030	0.033
A ₁	0	-	0.05	0	-	0.002
b	0.25	0.30	0.35	0.010	0.012	0.014
C	0.15	0.20	0.25	0.006	0.008	0.010
D	2.92	3.00	3.08	0.115	0.118	0.121
D ₂	1.75	1.87	2.00	0.069	0.074	0.079
D ₃	0.20	0.25	0.30	0.008	0.010	0.012
E	1.82	1.90	1.98	0.072	0.075	0.078
E ₂	1.38	1.50	1.63	0.054	0.059	0.064
E ₃	0.45	0.50	0.55	0.018	0.020	0.022
e	0.65 BSC			0.026 BSC		
H	0.15	0.20	0.25	0.006	0.008	0.010
K	0.25	-	-	0.010	-	-
K ₁	0.30	-	-	0.012	-	-
L	0.30	0.35	0.40	0.012	0.014	0.016

RECOMMENDED MINIMUM PADS FOR PowerPAK® ChipFET® Single



Recommended Minimum Pads
Dimensions in mm/(Inches)

[Return to Index](#)



Disclaimer

ALL PRODUCT, PRODUCT SPECIFICATIONS AND DATA ARE SUBJECT TO CHANGE WITHOUT NOTICE TO IMPROVE RELIABILITY, FUNCTION OR DESIGN OR OTHERWISE.

Vishay Intertechnology, Inc., its affiliates, agents, and employees, and all persons acting on its or their behalf (collectively, "Vishay"), disclaim any and all liability for any errors, inaccuracies or incompleteness contained in any datasheet or in any other disclosure relating to any product.

Vishay makes no warranty, representation or guarantee regarding the suitability of the products for any particular purpose or the continuing production of any product. To the maximum extent permitted by applicable law, Vishay disclaims (i) any and all liability arising out of the application or use of any product, (ii) any and all liability, including without limitation special, consequential or incidental damages, and (iii) any and all implied warranties, including warranties of fitness for particular purpose, non-infringement and merchantability.

Statements regarding the suitability of products for certain types of applications are based on Vishay's knowledge of typical requirements that are often placed on Vishay products in generic applications. Such statements are not binding statements about the suitability of products for a particular application. It is the customer's responsibility to validate that a particular product with the properties described in the product specification is suitable for use in a particular application. Parameters provided in datasheets and/or specifications may vary in different applications and performance may vary over time. All operating parameters, including typical parameters, must be validated for each customer application by the customer's technical experts. Product specifications do not expand or otherwise modify Vishay's terms and conditions of purchase, including but not limited to the warranty expressed therein.

Except as expressly indicated in writing, Vishay products are not designed for use in medical, life-saving, or life-sustaining applications or for any other application in which the failure of the Vishay product could result in personal injury or death. Customers using or selling Vishay products not expressly indicated for use in such applications do so at their own risk. Please contact authorized Vishay personnel to obtain written terms and conditions regarding products designed for such applications.

No license, express or implied, by estoppel or otherwise, to any intellectual property rights is granted by this document or by any conduct of Vishay. Product names and markings noted herein may be trademarks of their respective owners.

Material Category Policy

Vishay Intertechnology, Inc. hereby certifies that all its products that are identified as RoHS-Compliant fulfill the definitions and restrictions defined under Directive 2011/65/EU of The European Parliament and of the Council of June 8, 2011 on the restriction of the use of certain hazardous substances in electrical and electronic equipment (EEE) - recast, unless otherwise specified as non-compliant.

Please note that some Vishay documentation may still make reference to RoHS Directive 2002/95/EC. We confirm that all the products identified as being compliant to Directive 2002/95/EC conform to Directive 2011/65/EU.

Vishay Intertechnology, Inc. hereby certifies that all its products that are identified as Halogen-Free follow Halogen-Free requirements as per JEDEC JS709A standards. Please note that some Vishay documentation may still make reference to the IEC 61249-2-21 definition. We confirm that all the products identified as being compliant to IEC 61249-2-21 conform to JEDEC JS709A standards.