

RKD703KK

Silicon Schottky Barrier Diode for High Speed Switching

REJ03G1833-0200 Rev.2.00 Nov 20, 2009

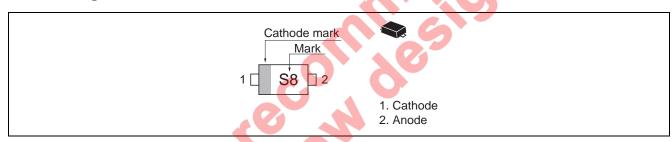
Features

- Low Power consumption (Low reverse leak current) and high speed (Low capacitance).
- We can support the lineup of environmental friendly halogen free type on your demand.
- Extremely small Flat Lead Package (SFP) is suitable for compact and high-density surface mount design.

Ordering Information

Part No	Laser Mark	Package Name	Package Code	Taping Abbreviation (Quantity)
RKD703KK R	S8	SFP	PUSF0002ZB-A	R (8,000 pcs / reel)

Pin Arrangement



Absolute Maximum Ratings

 $(Ta = 25^{\circ}C)$

Item	Symbol	Value	Unit
Repetitive peak reverse voltage	V_{RRM}	30	V
Average forward current	I ₀ *1	100	mA
Non-Repetitive Peak forward surge current	I _{FSM} * ²	200	mA
Junction temperature	Tj	125	°C
Storage temperature	Tstg	-55 to +125	°C

Notes: 1. See from Fig.4 to Fig.6.

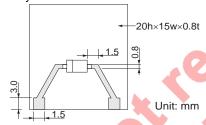
2. 10 ms sine wave 1 pulse.

Electrical Characteristics

 $(Ta = 25^{\circ}C)$

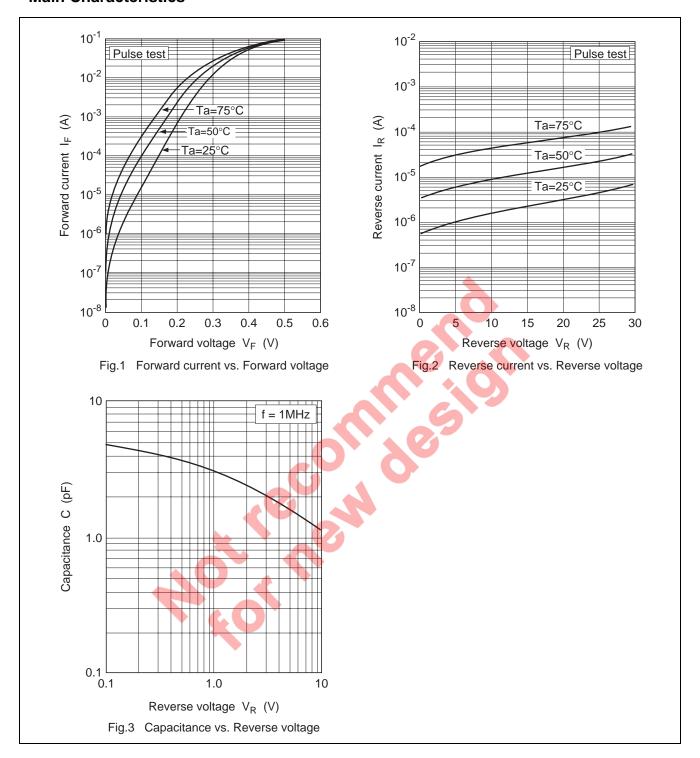
Item	Symbol	Min	Тур	Max	Unit	Test Condition
Forward voltage	V_{F1}	_	_	0.25	V	I _F = 1 mA
	V_{F2}	_	_	0.30	V	I _F = 5 mA
	V_{F3}	_	_	0.35	V	T _F = 20 mA
	V_{F4}	_	_	0.60	V	$I_F = 100 \text{ mA}$
Reverse current	I _{R1}	_	_	6	μA	V _R =10 V
	I _{R2}	_	_	50	μΑ	V _R = 30 V
Capacitance	С	_	_	5	pF	$V_R = 1 \text{ V, f} = 1 \text{ MHz}$
Thermal resistance	Rth <j-a></j-a>	_	600		°C/W	Polyimide board *1

Notes: 1. Polyimide board



2. In the SFP package, some lead is exposed because the tip of the lead is used as the cutting plane. Therefore, the solderability of the lead tip has been ignored. Please test and confirm before use.

Main Characteristics



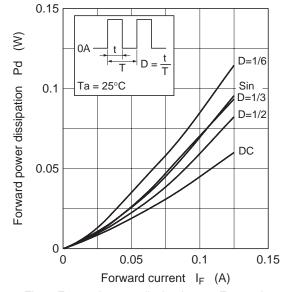


Fig.4 Forward power dissipation vs. Forward current

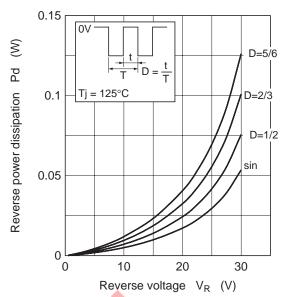


Fig.5 Reverse power dissipation vs. Reverse voltage

on desir

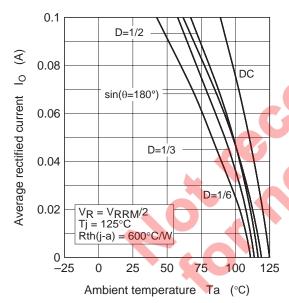
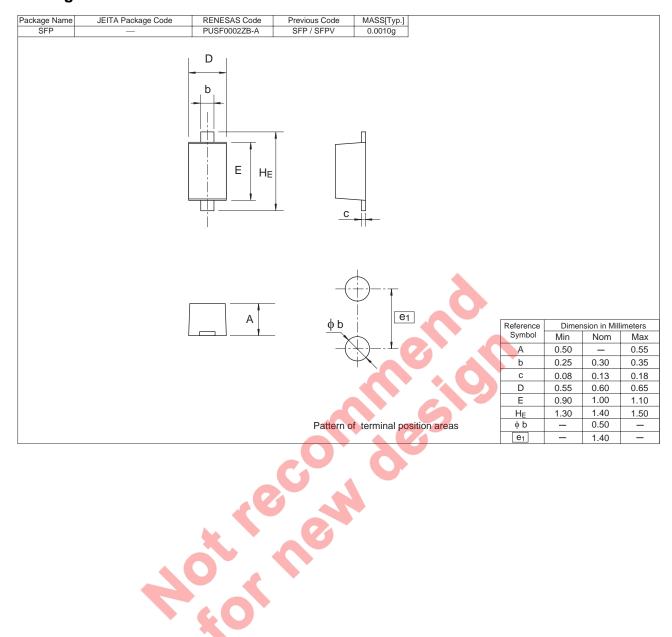


Fig.6 Average rectified current vs. Ambient temperature

Package Dimensions



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April 1st, 2010 Renesas Electronics Corporation

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