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Renesas Electronics website: http://www.renesas.com

April 1st, 2010 Renesas Electronics Corporation

Issued by: Renesas Electronics Corporation (http://www.renesas.com)

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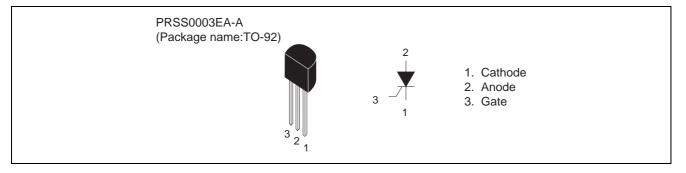
Thyristor Low Power Use

> REJ03G0352-0200 Rev.2.00 Mar.01.2005

Features

- $I_{T(AV)}: 0.3 A$
- V_{DRM} : 600 V
- I_{GT} : 100 μA

Outline



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Non-Insulated Type

Glass Passivation Type

Applications

Leakage protector, timer, and gas igniter

Maximum Ratings

Parameter	Symbol Voltage class		Unit	
Falameter	Symbol	12	ont	
Repetitive peak reverse voltage	V _{RRM}	600	V	
Non-repetitive peak reverse voltage	V _{RSM}	800	V	
DC reverse voltage	V _{R(DC)}	480	V	
Repetitive peak off-state voltage ^{Note1}	V _{DRM}	600	V	
Non-repetitive peak off-state voltage ^{Note1}	V _{DSM}	800	V	
DC off-state voltage ^{Note1}	V _{D(DC)}	480	V	

CR03AM-12

Parameter	Symbol	Ratings	Unit	Conditions
RMS on-state current	I _{T (RMS)}	0.47	А	
Average on-state current	I _{T (AV)}	0.3	A	Commercial frequency, sine half wave 180° conduction, Ta = 47°C
Surge on-state current	I _{TSM}	20	A	60Hz sine half wave 1 full cycle, peak value, non-repetitive
I ² t for fusing	l ² t	1.6	A ² s	Value corresponding to 1 cycle of half wave 60Hz, surge on-state current
Peak gate power dissipation	P _{GM}	0.5	W	
Average gate power dissipation	P _{G (AV)}	0.1	W	
Peak gate forward voltage	V _{FGM}	6	V	
Peak gate reverse voltage	V _{RGM}	6	V	
Peak gate forward current	I _{FGM}	0.3	А	
Junction temperature	Tj	- 40 to +110	°C	
Storage temperature	Tstg	- 40 to +125	°C	
Mass	—	0.23	g	Typical value

Notes: 1. With gate to cathode resistance $R_{GK} = 1 \ k\Omega$.

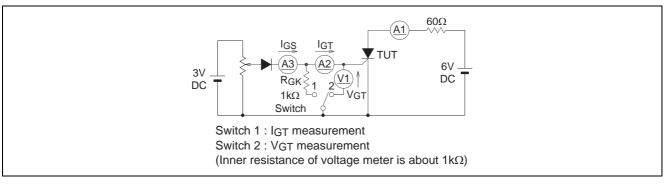
Electrical Characteristics

Parameter	Symbol	Min.	Тур.	Max.	Unit	Test conditions
Repetitive peak reverse current	I _{RRM}	_	_	0.1	mA	Tj = 110°C, V _{RRM} applied
Repetitive peak off-state current	I _{DRM}	_	_	0.1	mA	Tj = 110°C, V _{DRM} applied, R _{GK} = 1 k Ω
On-state voltage	V _{TM}	_	—	1.8	V	Ta = 25°C, I_{TM} = 4 A, instantaneous value
Gate trigger voltage	V _{GT}	_	—	0.8	V	$Tj = 25^{\circ}C, V_D = 6 V,$ $I_T = 0.1 A^{Note3}$
Gate non-trigger voltage	V _{GD}	0.2	_	—	V	$\label{eq:transform} \begin{array}{l} Tj = 110^{\circ}C, \ V_{D} = 1/2 \ V_{DRM}, \\ R_{GK} = 1 \ k\Omega \end{array}$
Gate trigger current	I _{GT}	1	_	100 ^{Note2}	μA	$Tj = 25^{\circ}C, V_D = 6 V,$ $I_T = 0.1 A^{Note3}$
Holding current	Ι _Η	_	1.5	3	mA	$\label{eq:tilde} \begin{split} T j &= 25^\circ C, \ V_D = 12 \ V, \\ R_GK &= 1 \ k \Omega \end{split}$
Thermal resistance	R _{th (j-a)}	_		180	°C/W	Junction to ambient

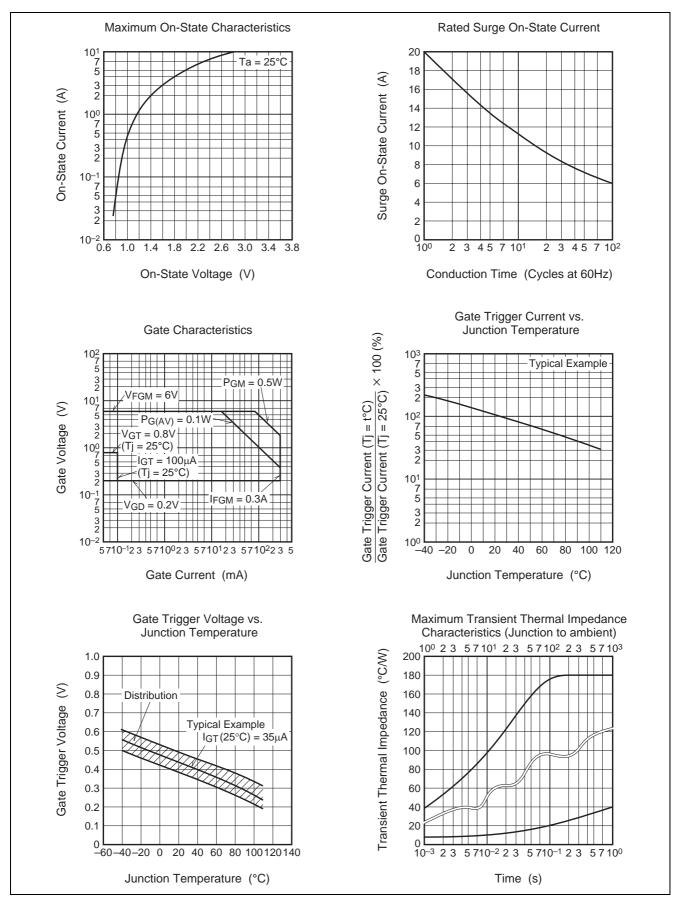
Item	A	В	С	D	E
Ι _{GT} (μΑ)	1 to 30	20 to 50	40 to 100	1 to 50	20 to 100

The above values do not include the current flowing through the 1 $k\Omega$ resistance between the gate and cathode.

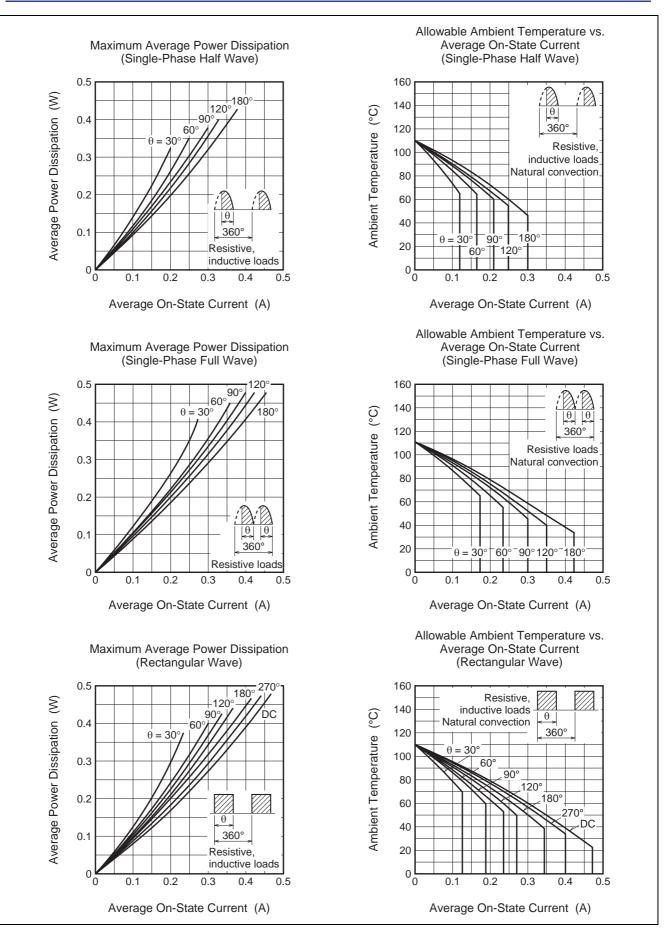
3 I_{GT}, V_{GT} measurement circuit.



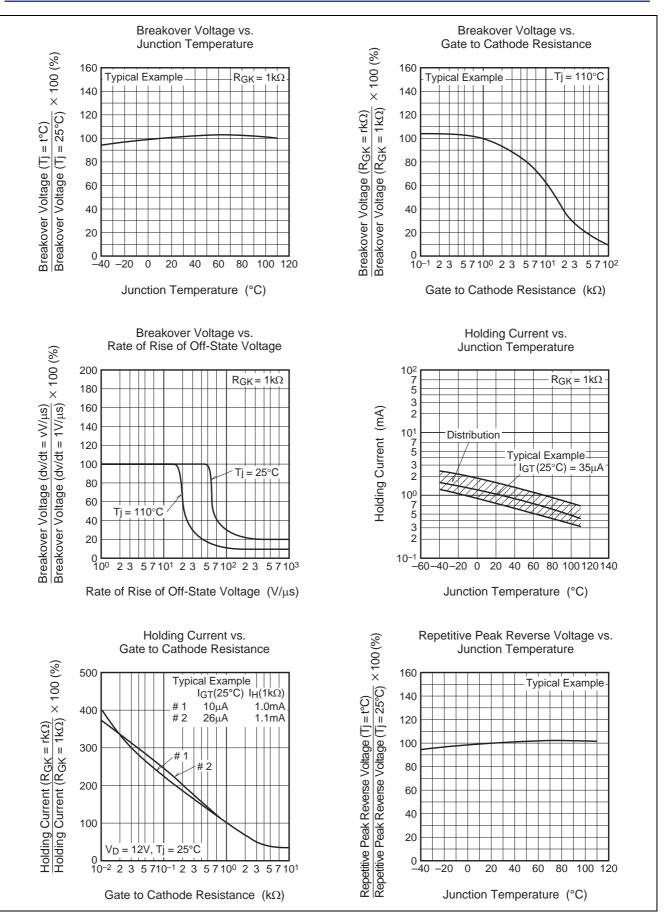
Performance Curves

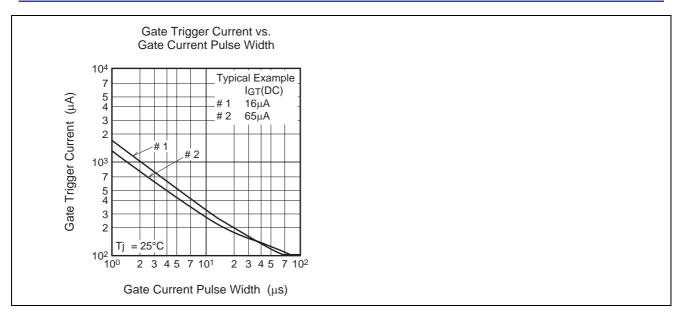


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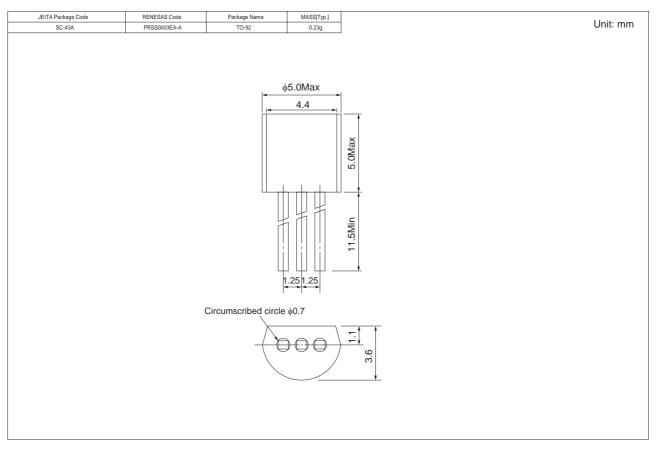


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Package Dimensions



Order Code

Lead form	Standard packing	Quantity	Standard order code	Standard order code example
Straight type	Vinyl sack	500	Type name	CR03AM-12
Lead form	Vinyl sack	500	Type name – Lead forming code	CR03AM-12-A6
Form A8	Taping	2000	Type name – TB	CR03AM-12-TB

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