

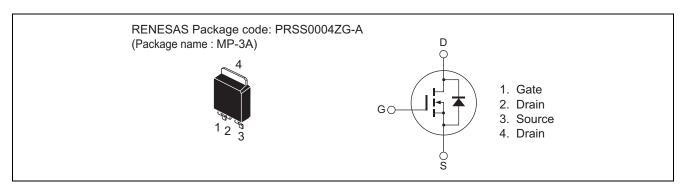
# RJK4002DPD

400V - 3A - MOS FET High Speed Power Switching R07DS0835EJ0200 Rev.2.00 Aug 08, 2012

### **Features**

- Low on-state resistance  $R_{DS(on)}=2.4~\Omega~typ.~(at~I_D=1.5~A,~V_{GS}=10~V,~Ta=25^{\circ}C)$
- Low drive current
- High speed switching

### **Outline**



### **Absolute Maximum Ratings**

 $(Ta = 25^{\circ}C)$ 

Item	Symbol	Value	Unit
Drain to source voltage	V <sub>DSS</sub>	400	V
Gate to source voltage	V <sub>GSS</sub>	±30	V
Drain current	I <sub>D</sub>	3	A
Drain peak current	I <sub>D (pulse)</sub> Note1	6	A
Body-drain diode reverse drain current	I <sub>DR</sub>	3	Α
Body-drain diode reverse drain peak current	I <sub>DR (pulse)</sub> Note1	6	Α
Avalanche current	I <sub>AP</sub> Note2	2.5	Α
Avalanche energy	E <sub>AR</sub> Note2	0.357	mJ
Channel dissipation	Pch Note3	30	W
Channel to case thermal Impedance	θch-c	4.17	°C/W
Channel temperature	Tch	150	°C
Storage temperature	Tstg	-55 to +150	°C

Notes: 1. Pulse width limited by safe operating area.

- 2. STch =  $25^{\circ}$ C, Tch  $\leq 150^{\circ}$ C
- 3. Value at Tc = 25°C

### **Electrical Characteristics**

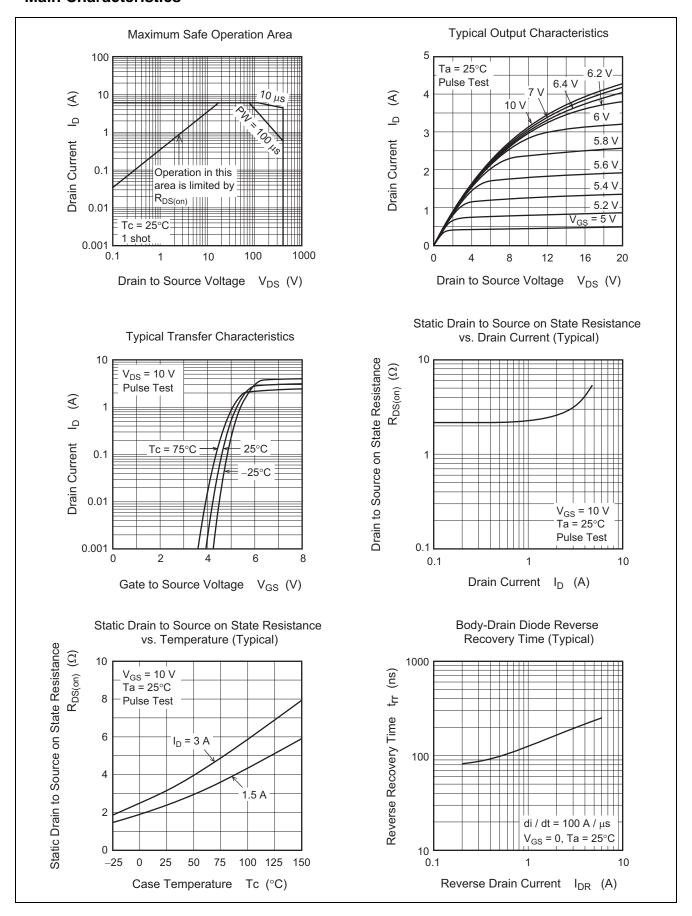
 $(Ta = 25^{\circ}C)$ 

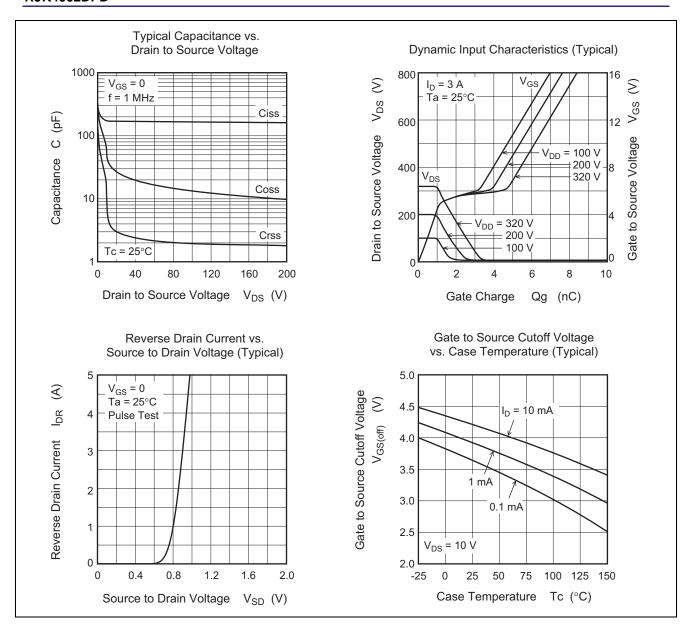
Item	Symbol	Min	Тур	Max	Unit	Test Conditions
Drain to source breakdown voltage	V <sub>(BR)DSS</sub>	400	_	_	V	$I_D = 1 \text{ mA}, V_{GS} = 0$
Zero gate voltage drain current	I <sub>DSS</sub>	_	_	1	μΑ	$V_{DS} = 400 \text{ V}, V_{GS} = 0$
Gate to source leak current	I <sub>GSS</sub>	_	_	±0.1	μΑ	$V_{GS} = \pm 30 \text{ V}, V_{DS} = 0$
Gate to source cutoff voltage	V <sub>GS(off)</sub>	3.5	_	4.5	V	$V_{DS} = 10 \text{ V}, I_{D} = 1 \text{ mA}$
Static drain to source on state resistance	R <sub>DS(on)</sub>	_	2.4	2.9	Ω	$I_D = 1.5 \text{ A}, V_{GS} = 10 \text{ V}^{\text{Note 4}}$
Input capacitance	Ciss	_	165	_	рF	V <sub>DS</sub> = 25 V
Output capacitance	Coss	_	25	_	рF	$V_{GS} = 0$ f = 1 MHz
Reverse transfer capacitance	Crss	_	2.6	_	рF	
Turn-on delay time	t <sub>d(on)</sub>	_	11	_	ns	I <sub>D</sub> = 1.5 A
Rise time	t <sub>r</sub>	_	12	_	ns	$V_{GS} = 10 \text{ V}$ $R_L = 133 \Omega$ $Rg = 10 \Omega$
Turn-off delay time	t <sub>d(off)</sub>	_	23	_	ns	
Fall time	t <sub>f</sub>	_	20	_	ns	
Total gate charge	Qg	_	6.0	_	nC	$V_{DD} = 320 \text{ V}$ $V_{GS} = 10 \text{ V}$ $I_D = 3 \text{ A}$
Gate to source charge	Qgs	_	1.2	_	nC	
Gate to drain charge	Qgd	_	3.4	_	nC	
Body-drain diode forward voltage	$V_{DF}$	_	0.9	1.5	V	$I_F = 3 \text{ A}, V_{GS} = 0^{\text{Note 4}}$
Body-drain diode reverse recovery time	t <sub>rr</sub>	_	200	_	ns	$I_F = 3 \text{ A}, V_{GS} = 0$
						di <sub>F</sub> /dt = 100 A/μs

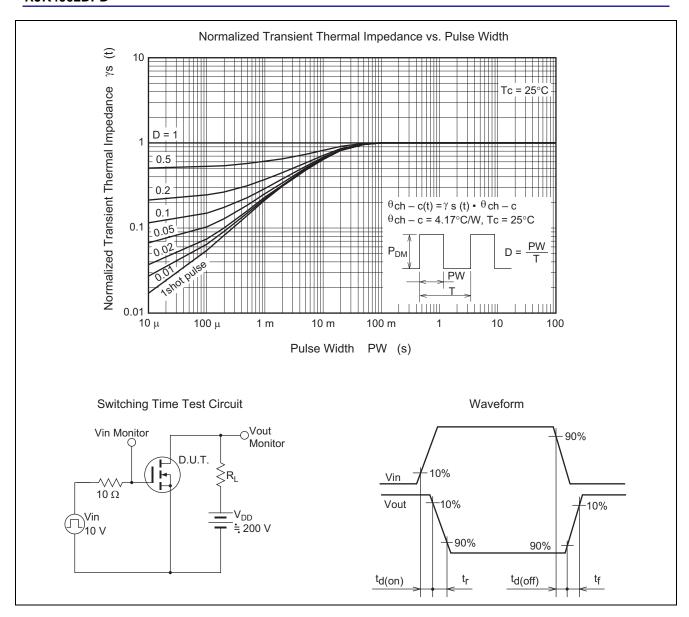
Note: 4. Pulse test

This device is sensitive to electrostatic discharge.
 It is recommended to adopt appropriate cautions when handling this product.

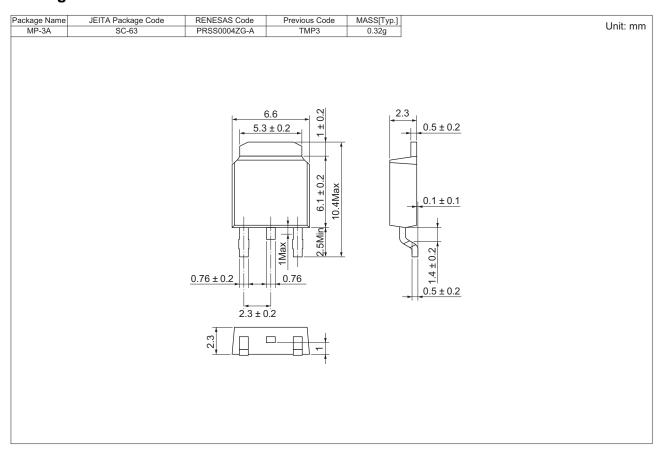
### **Main Characteristics**







### **Package Dimensions**



## **Ordering Information**

Orderable Part Number	Quantity	Shipping Container
RJK4002DPD-00#J2	3000 pcs	Taping

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