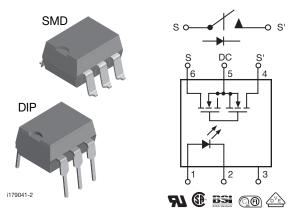


Vishay Semiconductors



1 Form A Solid-State Relay



DESCRIPTION

The LH1546 is robust, ideal for telecom and ground fault applications. It is a SPST normally open switch (1 form A) that replaces electromechanical relays in many applications. It is constructed using a GaAIAs LED for actuation control and an integrated monolithic die for the switch output. The die, fabricated in a high-voltage dielectrically isolated technology, is comprised of a photodiode array, switch control circuitry and MOSFET switches. In addition, it employs current-limiting circuitry which meets lightning surge testing as per ANSI/TIA-968-B and other regulatory voltage surge requirements when overvoltage protection is provided.

FEATURES

- Current limit protection
- Isolation test voltage 5300 V_{BMS}
- Typical R_{ON} 28 Ω
- Load voltage 350 V
- Load current 120 mA
- High surge capability
- Clean bounce free switching
- Low power consumption
- · SMD lead available on tape and reel
- Equivalent to CP Clare LCA110
- · Compliant to RoHS Directive 2002/95/EC and in accordance to WEEE 2002/96/EC

APPLICATIONS

- General telecom switching
- Instrumentation
- Industrial controls

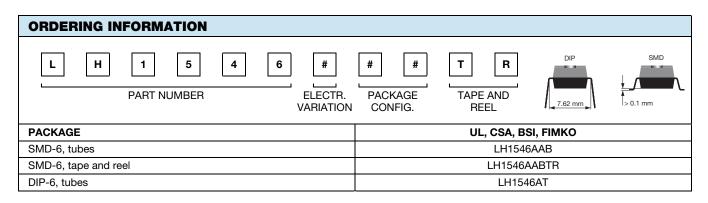
Note

• See "solid-state relays" (application note 56)

AGENCY APPROVALS

UL1577: file no. E52744 system code H, double protection

- CSA: certification no. 093751
- DIN EN: 60747-5-2 (VDE 0884)/60747-5-5 (pending), available with option 1
- BSI: certification no. 7979/7980
- FIMKO: 25419





RoHS

COMPLIAN



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ABSOLUTE MAXIMUM RATINGS (T _{amb} = 25 °C, unless otherwise specified)							
PARAMETER	TEST CONDITION	SYMBOL	VALUE	UNIT			
INPUT							
LED continuous forward current		I _F	50	mA			
LED reverse voltage	$I_R \le 10 \ \mu A$	V _R	8	V			
OUTPUT							
DC or peak AC load voltage	$I_L \le 50 \ \mu A$	VL	350	V			
Continuous DC load current at 25 °C, bidirectional		ΙL	120	mA			
Continuous DC load current at 25 °C, unidirectional		۱L	200	mA			
SSR							
Ambient temperature range		T _{amb}	- 40 to + 85	°C			
Storage temperature range		T _{stg}	- 40 to + 150	°C			
Soldering temperature ⁽¹⁾	t = 10 s max.	T _{sld}	260	°C			
Isolation test voltage	for 1 s	V _{ISO}	5300	V _{RMS}			
Isolation resistance	$V_{IO} = 500 \text{ V}, \text{ T}_{amb} = 25 ^{\circ}\text{C}$	R _{IO}	≥ 10 ¹²	Ω			
ISUIALIULI LESISLALICE	$V_{IO} = 500 \text{ V}, \text{ T}_{amb} = 100 ^{\circ}\text{C}$	R _{IO}	≥ 10 ¹¹	Ω			
SSR output power dissipation (continuous)		P _{diss}	550	mW			

Notes

Stresses in excess of the absolute maximum ratings can cause permanent damage to the device. Functional operation of the device is not
implied at these or any other conditions in excess of those given in the operational sections of this document. Exposure to absolute
maximum ratings for extended periods of the time can adversely affect reliability.

⁽¹⁾ Refer to reflow profile for soldering conditions for surface mounted devices (SMD). Refer to wave profile for soldering conditions for through hole devices (DIP).

ELECTRICAL CHARACTERISTICS (T _{amb} = 25 °C, unless otherwise specified)							
PARAMETER	TEST CONDITION	SYMBOL	MIN.	TYP.	MAX.	UNIT	
INPUT							
LED forward current, switch turn-on	I _L = 100 mA, t = 10 ms	I _{Fon}		1.1	2	mA	
LED forward current, switch turn-off	$V_L = \pm 350 V$	I _{Foff}	0.2	1		mA	
LED forward voltage	I _F = 10 mA	V _F	1.15	1.26	1.45	V	
OUTPUT							
On-resistance, AC/DC: pin 4 (±) to 6 (±)	$I_{\rm F} = 5 {\rm mA}, I_{\rm L} = 50 {\rm mA}$	R _{ON}		28	35	Ω	
On-resistance, DC: pin 4, 6 (+) to 5 (-)	$I_{\rm F} = 5 {\rm mA}, I_{\rm L} = 50 {\rm mA}$	R _{ON}		7	10	Ω	
Off-resistance	$I_{F} = 0 \text{ mA}, V_{L} = \pm 100 \text{ V}$	R _{OFF}	0.5	300		GΩ	
Current limit AC ⁽¹⁾ : pin 4 (±) to 6 (±)	$I_F = 5 \text{ mA}, t = 5 \text{ ms}, V_L = 6 \text{ V}$	I _{LMT}	170	210	250	mA	
Off-state leakage current	$I_F = 0 \text{ mA}, V_L = \pm 100 \text{ V}$	Ι _Ο		0.35	200	nA	
	$I_F = 0 \text{ mA}, V_L = \pm 350 \text{ V}$	Ι _Ο		0.096	1	μA	
Output capacitance pin 4 to 6	$I_{\rm F} = 0 {\rm mA}, V_{\rm L} = 1 {\rm V}$	Co		18		pF	
	$I_{\rm F} = 0 {\rm mA}, {\rm V_L} = 50 {\rm V}$	Co		6.7		pF	
Switch offset	$I_F = 5 \text{ mA}$	V _{OS}		0.3		μV	
TRANSFER							
Capacitance (input to output)	V _{ISO} = 1 V	C _{IO}		0.67		pF	

Notes

• Minimum and maximum values are testing requirements. Typical values are characteristics of the device and are the result of engineering evaluations. Typical values are for information only and are not part of the testing requirements.

⁽¹⁾ No DC mode current limit available.

SWITCHING CHARACTERISTICS (T _{amb} = 25 °C, unless otherwise specified)						
PARAMETER	TEST CONDITION	SYMBOL	MIN.	TYP.	MAX.	UNIT
Turn-on time	I _F = 5 mA, I _L = 50 mA	t _{on}		1.14	3	ms
Turn-off time	$I_{F} = 5 \text{ mA}, I_{L} = 50 \text{ mA}$	t _{off}		0.71	3	ms

Rev. 1.8, 25-Jul-11

Document Number: 83838



Vishay Semiconductors

SAFETY AND INSULATION RATINGS							
PARAMETER		TEST CONDITION	SYMBOL	VALUE	UNIT		
Climatic classification		IEC 68 part 1		40/85/21			
Pollution degree		DIN VDE 0109		2			
Tracking resistance (comparative tracking index	x)	Insulation group Illa	CTI	175			
Highest allowable overvolta	ige	Transient overvoltage	V _{IOTM}	8000	V _{peak}		
Max. working insulation vol	tage	Recurring peak voltage	V _{IORM}	890	V _{peak}		
Insulation resistance at 25 °C			R _{IS}	≥ 10 ¹²	Ω		
Insulation resistance at T _S		V _{IO} = 500 V	R _{IS}	≥ 10 ⁹	Ω		
Insulation resistance at 100 °C			R _{IS}	≥ 10 ¹¹	Ω		
Partial discharge test voltage	je	Methode a, V _{pd} = V _{IORM} x 1.875	V _{pd}	1669	V _{peak}		
Safety limiting values - maximum values allowed in the event of a failure	Case temperature		T _{SI}	175	°C		
	Input current		I _{SI}	300	mA		
	Output power		P _{SO}	700	mW		
Minimum external air gap (o	clearance)	Measured from input terminals to output terminals, shortest distance through air	>/		mm		
Minimum external tracking (creepage)		Measured from input terminals to output terminals, shortest distance path along body		≥ 7	mm		

TYPICAL CHARACTERISTICS (T_{amb} = 25 °C, unless otherwise specified)

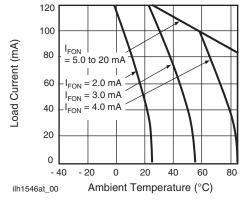
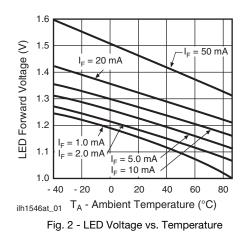


Fig. 1 - Recommended Operating Conditions



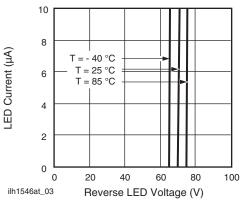


Fig. 3 - LED Reverse Current vs. LED Reverse Voltage

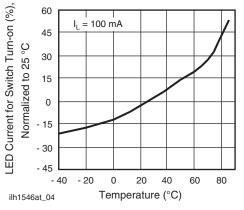


Fig. 4 - LED Current for Switch Turn-on vs. Temperature

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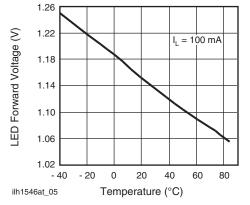


Fig. 5 - LED Dropout Voltage vs. Temperature

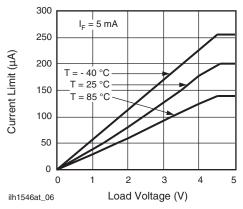


Fig. 6 - Load Current vs. Load Voltage

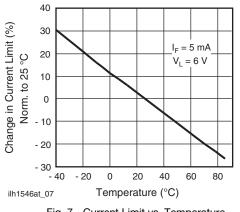


Fig. 7 - Current Limit vs. Temperature

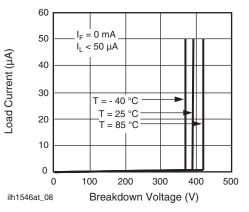


Fig. 8 - Switch Breakdown Voltage vs. Load Current

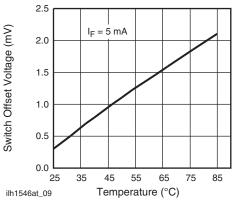
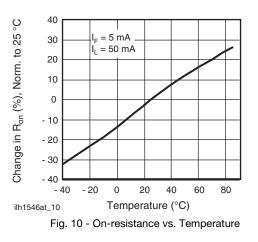


Fig. 9 - Switch Offset Voltage vs. LED Current



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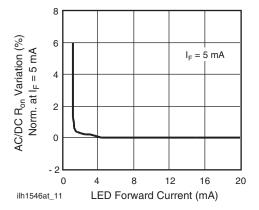


Fig. 11 - Variation in On-resistance vs. LED Current

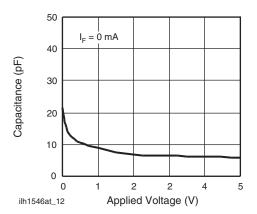


Fig. 12 - Switch Capacitance vs. Applied Voltage

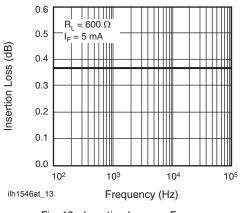


Fig. 13 - Insertion Loss vs. Frequency

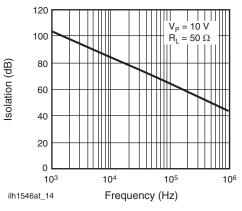


Fig. 14 - Output Isolation

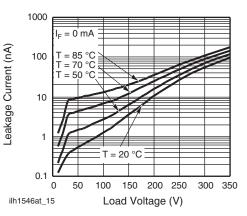
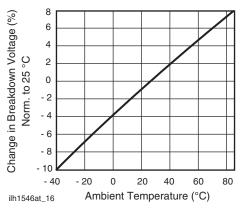
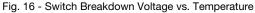


Fig. 15 - Leakage Current vs. Applied Voltage at Elevated Temperatures





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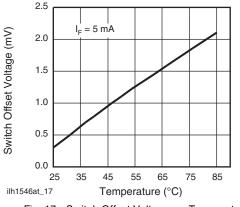


Fig. 17 - Switch Offset Voltage vs. Temperature

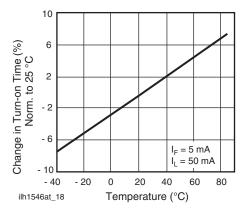
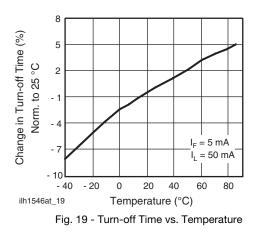
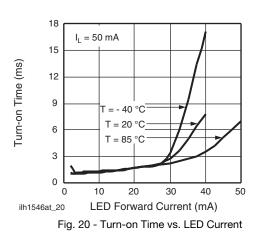


Fig. 18 - Turn-on Time vs. Temperature





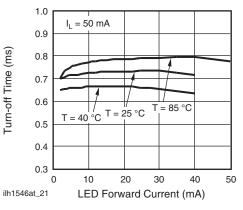


Fig. 21 - Turn-off Time vs. LED Current

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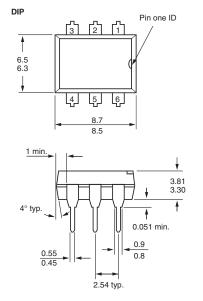


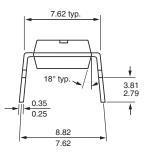


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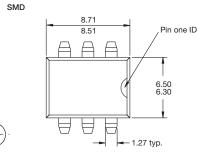
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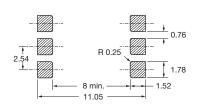
PACKAGE DIMENSIONS in millimeters





ISO method A



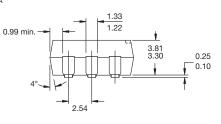


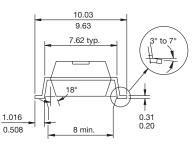


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PACKAGE MARKING



Note

• Tape and reel suffix (TR) is no part of the package marking.

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