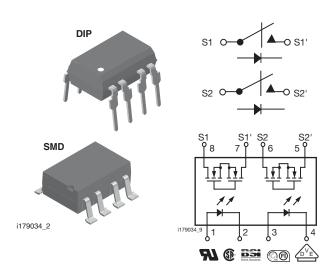


**Vishay Semiconductors** 

RoHS

COMPLIANT

### **Dual 1 Form A Solid State Relay (Low Capacitance)**



#### DESCRIPTION

These dual SSRs (LH1544, dual 1 form A) are SPST normally open switches which can replace electromechanical relays in many applications. The relays provide a low-capacitance, high-voltage switch contact with high off-resistance and low switch-offset voltage. These characteristics, combined with high-speed actuation, result in an SSR which is ideal for small signal and DC instrumentation applications.

The relays are constructed by using a GaAlAs LED for actuation control and an integrated monolithic die for the switch output. The die is comprised of a photodiode array, switch-control circuity, and low-capacitance MOSFET switches.

#### **FEATURES**

- Dual channel, LH1541 type
- Low capacitance switch (5 pF)
- Isolation test voltage 5300 V<sub>RMS</sub>
- Extremely high off-resistance
- Load voltage 200 V
- Clean bounce free switching
- Low power consumption
- High reliability monolithic detector
- Compliant to RoHS Directive 2002/95/EC and in accordance to WEEE 2002/96/EC

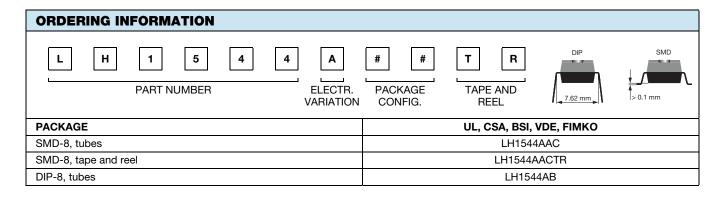
#### **APPLICATIONS**

- Instrumentation
  - Thermocouple switching
  - Analog multiplexing
- Reed relay replacement
- Programmable logic controllers
- · Data acquisition
- Test equipment

#### **AGENCY APPROVALS**

UL1577:	file	no.	E52744	system	code	Н,	double
	prot	ectio	n				
CSA:	cert	ificati	on no. 09	3751			

- BSI/BABT: certification no. 7980
- DIN EN: 60747-5-2 (VDE 0884)/60747-5-5 (pending), available with option 1
- FIMKO: approval



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### Dual 1 Form A Solid State Relay (Low Capacitance)



<b>ABSOLUTE MAXIMUM RATINGS</b> ( $T_{amb} = 25 \degree C$ , unless otherwise specified)							
PARAMETER	TEST CONDITION	SYMBOL	VALUE	UNIT			
INPUT							
LED continuous forward current		I <sub>F</sub>	50	mA			
LED reverse voltage	$I_R \le 10 \ \mu A$	V <sub>R</sub>	8	V			
OUTPUT							
DC or peak AC load voltage	$I_L \le 50 \ \mu A$	VL	200	V			
Continuous DC load current, one pole operating		ΙL	55	mA			
Continuous DC load current, two poles operating		۱L	40	mA			
SSR							
Peak load current (single shot)	t = 100 ms	I <sub>P</sub>	100	mA			
Ambient temperature range		T <sub>amb</sub>	- 40 to + 85	°C			
Storage temperature range		T <sub>stg</sub>	- 40 to + 150	°C			
Pin soldering temperature <sup>(1)</sup>	t = 10 s max.	T <sub>sld</sub>	260	°C			
Input to output isolation voltage		V <sub>ISO</sub>	5300	V <sub>RMS</sub>			
Pole-to-pole isolation voltage (S1 to S2) (2)	dry air, dust free, at sea level		1600	V			
Output power dissipation (continuous)		P <sub>diss</sub>	600	mW			

#### Notes

Stresses in excess of the absolute maximum ratings can cause permanent damage to the device. Functional operation of the device is not implied at these or any other conditions in excess of those given in the operational sections of this document. Exposure to absolute maximum ratings for extended periods of the time can adversely affect reliability. Refer to reflow profile for soldering conditions for surface mounted devices (SMD). Refer to wave profile for soldering conditions for through hole

(1) devices (DIP).

(2) Breakdown occurs between the output pins external to the package.

ELECTRICAL CHARACTERISTICS (T <sub>amb</sub> = 25 °C, unless otherwise specified)							
PARAMETER	TEST CONDITION	SYMBOL	MIN.	TYP.	MAX.	UNIT	
INPUT	·			•			
LED forward current, switch turn-on	I <sub>L</sub> = 100 mA, t = 10 ms	I <sub>Fon</sub>		0.9	2	mA	
LED forward current, switch turn-off	$V_L = \pm 150 V$	I <sub>Foff</sub>	0.2	0.8		mA	
LED forward voltage	$I_F = 5 \text{ mA}$	V <sub>F</sub>	1.1	1.19	1.45	V	
OUTPUT	• •						
On-resistance	$I_{\rm F} = 5  {\rm mA},  I_{\rm L} = 50  {\rm mA}$	R <sub>ON</sub>	70	110	160	Ω	
Off-resistance	$I_F = 0 \text{ mA}, V_L = \pm 100 \text{ V}$	R <sub>OFF</sub>	0.5	10 000		GΩ	
Off state lookage ourrent	$I_F = 0 \text{ mA}, V_L = \pm 100 \text{ V}$	Ιo		0.01	200	nA	
Off-state leakage current	$I_F = 0 \text{ mA}, V_L = \pm 200 \text{ V}$	Ι <sub>Ο</sub>			1	μA	
Output capacitance	$I_{\rm F} = 0  {\rm mA},  {\rm V_L} = 1  {\rm V}$	Co		0		pF	
Output capacitance pin 4 to 6	$I_{\rm F} = 0  {\rm mA},  V_{\rm L} = 50  {\rm V}$	Co		0.5		pF	
Pole-to-pole Capacitance (S1 to S2)	$I_F = 5 \text{ mA}$			0.5		pF	
Switch offset	$I_F = 5 \text{ mA}$	V <sub>OS</sub>		0.1		μV	
TRANSFER	·	·		•	•	•	
Capacitance (input to output)	V <sub>ISO</sub> = 1 V	C <sub>IO</sub>		1.1		pF	

#### Note

Minimum and maximum values are testing requirements. Typical values are characteristics of the device and are the result of engineering evaluations. Typical values are for information only and are not part of the testing requirements.

SWITCHING CHARACTERISTICS (T <sub>amb</sub> = 25 °C, unless otherwise specified)						
PARAMETER	TEST CONDITION	SYMBOL	MIN.	TYP.	MAX.	UNIT
Turn-on time	I <sub>F</sub> = 5 mA, I <sub>L</sub> = 50 mA	t <sub>on</sub>		0.24	0.5	ms
Turn-off time	$I_{F} = 5 \text{ mA}, I_{L} = 50 \text{ mA}$	t <sub>off</sub>		0.13	0.5	ms

#### Footnotes

The following information refers to the SSR recommended operation conditions:

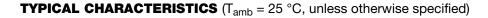
- Both relays on with equal load currents. For single relay operation, refer to the LH1541 recommended operating conditions graph.

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Dual 1 Form A Solid State Relay (Low Capacitance)

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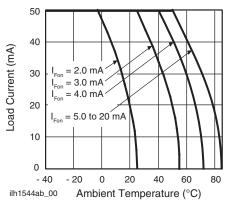


Fig. 1 - Recommended Operating Conditions

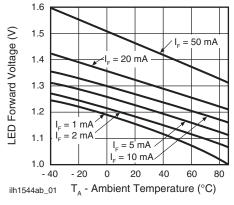
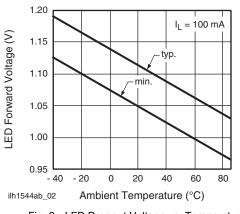
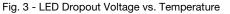


Fig. 2 - LED Voltage vs. Temperature





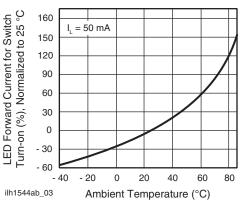
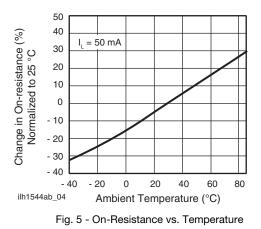


Fig. 4 - LED Current for Switch Turn-on vs. Temperature



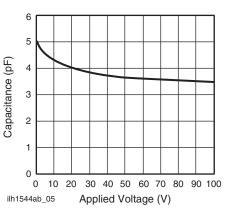


Fig. 6 - Switch Capacitance vs. Applied Voltage

Document Number: 83835 Rev. 1.6, 17-Mar-11

For technical questions, contact: optocoupleranswers@vishay.com

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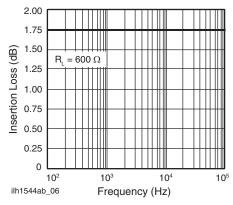
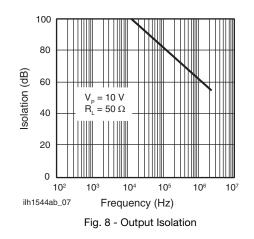
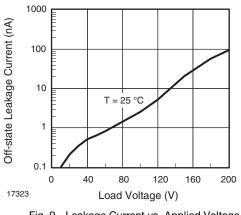
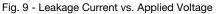


Fig. 7 - Insertion Loss vs. Frequency







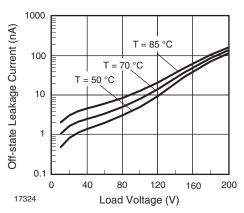


Fig. 10 - Leakage Current vs. Applied Voltage at Elevated Temperatures

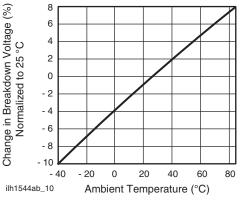
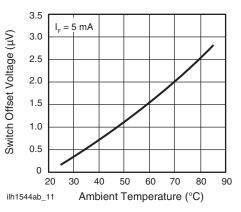
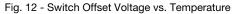


Fig. 11 - Switch Breakdown Voltage vs. Temperature





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Dual 1 Form A Solid State Relay (Low Capacitance)

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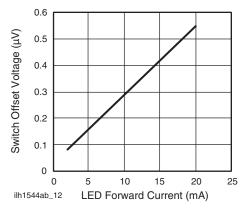


Fig. 13 - Switch Offset Voltage vs. LED Current

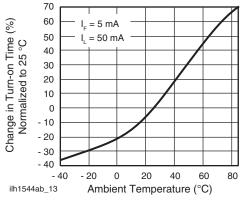


Fig. 14 - Turn-on Time vs. Temperature

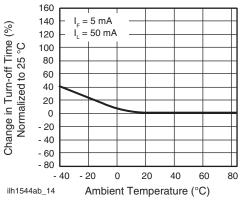


Fig. 15 - Turn-off Time vs. Temperature

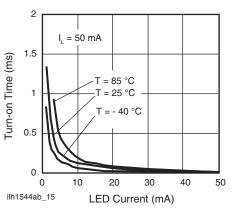
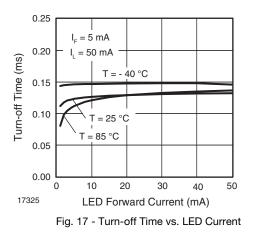


Fig. 16 - Turn-on Time vs. LED Current



Document Number: 83835 Rev. 1.6, 17-Mar-11

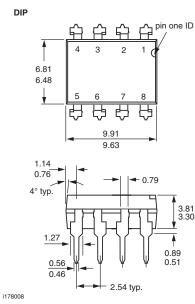
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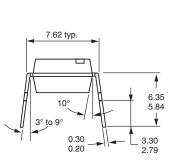
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Dual 1 Form A Solid State Relay (Low Capacitance)



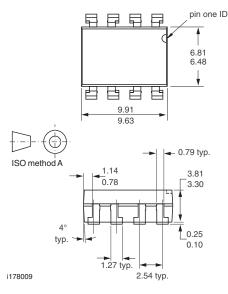
### **PACKAGE DIMENSIONS** in millimeters

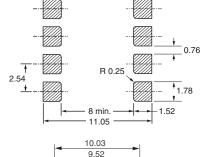


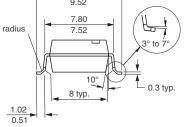


ISO method A

SMD







### **PACKAGE MARKING** (example)



#### Note

• Tape and reel suffix (TR) is not part of the package marking.

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Document Number: 83835 Rev. 1.6, 17-Mar-11



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