Notice for TAIYO YUDEN products

Please read this notice before using the TAIYO YUDEN products.

REMINDERS

Product information in this catalog is as of October 2013. All of the contents specified herein are subject to change without notice due to technical improvements, etc. Therefore, please check for the latest information carefully before practical application or usage of the Products.

Please note that TAIYO YUDEN CO., LTD. shall not be responsible for any defects in products or equipment incorporating such products, which are caused under the conditions other than those specified in this catalog or individual specification.

Please contact TAIYO YUDEN CO., LTD. for further details of product specifications as the individual specification is available.

Please conduct validation and verification of products in actual condition of mounting and operating environment before commercial shipment of the equipment.

All electronic components or functional modules listed in this catalog are developed, designed and intended for use in general electronics equipment.(for AV, office automation, household, office supply, information service, telecommunications, (such as mobile phone or PC) etc.). Before incorporating the components or devices into any equipment in the field such as transportation,(automotive control, train control, ship control), transportation signal, disaster prevention, medical, public information network (telephone exchange, base station) etc. which may have direct influence to harm or injure a human body, please contact TAIYO YUDEN CO., LTD. for more detail in advance.

Do not incorporate the products into any equipment in fields such as aerospace, aviation, nuclear control, submarine system, military, etc. where higher safety and reliability are especially required.

In addition, even electronic components or functional modules that are used for the general electronic equipment, if the equipment or the electric circuit require high safety or reliability function or performances, a sufficient reliability evaluation check for safety shall be performed before commercial shipment and moreover, due consideration to install a protective circuit is strongly recommended at customer's design stage.

The contents of this catalog are applicable to the products which are purchased from our sales offices or distributors (so called "TAIYO YUDEN' s official sales channel").
It is apply applied to the products our sales of TAIYO YUDEN' sofficial sales channel".

It is only applicable to the products purchased from any of TAIYO YUDEN's official sales channel.

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Certain items in this catalog may require specific procedures for export according to "Foreign Exchange and Foreign Trade Control Law" of Japan, "U.S. Export Administration Regulations", and other applicable regulations. Should you have any question or inquiry on this matter, please contact our sales staff.

TAIYO YUDEN 2014

MULTILAYER EMI SUPPRESSION FILTERS



PARTS NUMBER			*Operating Temp.	*Operating Temp. : −25~+85°C			
[T Series]							
F K 2	1 2 5 T \triangle			=Blank space			
(1)	2 3	4 5 6	7 8				
①Series name			⑤Characteristic				
Code	Series	name	Code	Characteristic			
FK	Multilayer EMI s	uppression filter	(example)				
			A	Sharp			
②Dimensions(L	× W)						
Code	Type(inch)	Dimensions (L×W)[mm]	6 Rated voltage Code	Rated voltage[V]			
2125	2125(0805)	2.0 × 1.25	L				
2120	2120(00007	2.0741.20		10			
③Equivalence ci	ircuit		⑦Packaging				
Code	Equivalen	ce circuit	Code	Packaging			
Т	Τt	уре	-T	Taping			
_							
4 Cutoff frequer	ю		⑧Internal code				
Code	Cutoff fr	requency	Code	Internal code			
(example) △186	18 1	ALL_	Δ	Standard			
<u>∆186</u> ∆256	25 M						
	201	VII 12					
【TZ Series】							
【TZ Series】 FK2	1 2 5 T Z	2 0 1 C 8	5 0 T 🛆	$\Delta=$ Blank space			
			50 T A	$\Delta=$ Blank space			
F K 2		2 0 1 C 8 ④ ⑤		$\Delta=$ Blank space			
F K 2							
F K 2		4 5	6 7				
FK2 ①	2 3	(4) (5)	6 7 5Nominal capacit	ance			
F K ① ①Series name Code FK	② ③ Series Multilayer EMI s	(4) (5)	6 7 SNominal capacit Code C170 C500	ance Nominal capacitance[1MHz] 17pF 50pF			
F K 2 ① ① ①Series name Code	② ③ Series Multilayer EMI s	(4) (5) name uppression filter	6 7 SNominal capacit Code C170	ance Nominal capacitance[1MHz] 17pF			
F K ① ①Series name Code FK	② ③ Series Multilayer EMI s	(4) (5) iname uppression filter Dimensions	6 7 §Nominal capacit Code C170 C500 C850	ance Nominal capacitance[1MHz] 17pF 50pF			
F K 2 ① ①Series name Code FK ②Dimensions(L Code	② ③ Series Multilayer EMI s × W) Type (inch)	(4) (5) iname uppression filter Dimensions (L × W) [mm]	6 7 §Nominal capacit Code C170 C500 C850 6Packaging	Nominal capacitance[1MHz] 17pF 50pF 85pF			
F K 2 ① ① ①Series name Code FK 2 ②Dimensions(L	② ③ Series Multilayer EMI s × W)	(4) (5) iname uppression filter Dimensions	© ⑦ ©Nominal capacit Code C170 C500 C850 ©Packaging Code	Nominal capacitance[1MHz] 17pF 50pF 85pF Packaging			
F K 2 ① ① ① ①Series name Code FK ②Dimensions (L Code 2125	2 3 Series Multilayer EMI s × W) Type (inch) 2125 (0805)	(4) (5) iname uppression filter Dimensions (L × W) [mm]	6 7 §Nominal capacit Code C170 C500 C850 6Packaging	Nominal capacitance[1MHz] 17pF 50pF 85pF			
F K 2 ① ①Series name Code FK ②Dimensions(L Code	2 3 Series Multilayer EMI s × W) Type (inch) 2125 (0805)	A (5) iname uppression filter Dimensions (L × W) [mm] 2.0 × 1.25	© ⑦ ©Nominal capacit Code C170 C500 C850 ©Packaging Code T	Nominal capacitance[1MHz] 17pF 50pF 85pF Packaging			
F K 2 ① ①Series name Code FK ②Dimensions(L Code 2125 ③Equivalence ci	2 3 Series Multilayer EMI s × W) Type (inch) 2125 (0805)	(4) (5) iname uppression filter Dimensions (L × W) [mm] 2.0 × 1.25 ce circuit	© ⑦ ©Nominal capacit Code C170 C500 C850 ©Packaging Code	Nominal capacitance[1MHz] 17pF 50pF 85pF Packaging			
F K 2 ① ①Series name Code FK ②Dimensions(L Code 2125 ③Equivalence ci Code	2 3 Series Multilayer EMI s × W) Type (inch) 2125 (0805) ircuit Equivalen	(4) (5) iname uppression filter Dimensions (L × W) [mm] 2.0 × 1.25 ce circuit	© ⑦ ©Nominal capacit Code C170 C500 C850 ©Packaging Code T ⑦Internal code	Ance Nominal capacitance[1MHz] 17pF 50pF 85pF Packaging Taping			
F K 2 ① ①Series name Code FK ②Dimensions(L Code 2125 ③Equivalence ci Code	2 3 Series Multilayer EMI s × W) Type (inch) 2125 (0805) ircuit Equivalen T tr	(4) (5) iname uppression filter Dimensions (L × W) [mm] 2.0 × 1.25 ce circuit ype	© ⑦ SNominal capacit Code C170 C500 C850 ©Packaging Code T ⑦Internal code Code	Annce Nominal capacitance[1MHz] 17pF 50pF 85pF Packaging Taping Internal code			
F K 2 ① ①Series name Code FK ②Dimensions (L Code 2125 ③Equivalence ci Code T ④Nominal imped Code	2 3 Series Multilayer EMI s × W) Type (inch) 2125 (0805) ircuit Equivalen T t lance Nominal impedi	(4) (5) iname uppression filter Dimensions (L × W) [mm] 2.0 × 1.25 ce circuit ype ance[100MHz]	© ⑦ SNominal capacit Code C170 C500 C850 ©Packaging Code T ⑦Internal code Code	Annce Nominal capacitance[1MHz] 17pF 50pF 85pF Packaging Taping Internal code			
F K 2 ① ①Series name Code FK ②Dimensions (L Code 2125 ③Equivalence ci Code T ④Nominal imped Code Z700	2 3 Series Multilayer EMI s × W) Type (inch) 2125 (0805) ircuit Equivalen T t lance Nominal impedia 70	(4) (5) in name uppression filter uppression filter (L × W) [mm] 2.0 × 1.25 (L × W) [mm] ce circuit (L × W) ype (L × W) ance[100MHz] (L × Q) Ω (L × Q)	© ⑦ SNominal capacit Code C170 C500 C850 ©Packaging Code T ⑦Internal code Code	Annce Nominal capacitance[1MHz] 17pF 50pF 85pF Packaging Taping Internal code			
F K 2 ① ①Series name Code FK ②Dimensions (L Code 2125 ③Equivalence ci Code T ④Nominal imped Code Z700 Z101	2 3 Series Multilayer EMI s × W) Type (inch) 2125 (0805) ircuit Equivalen T t lance Nominal impeda 70 100	(4) (5) in name uppression filter uppression filter (L × W) [mm] 2.0 × 1.25 (L × W) [mm] ce circuit (L × W) [mm] gance[100MHz] (L × Q) Ω (L × Q) Ω (L × Q)	© ⑦ SNominal capacit Code C170 C500 C850 ©Packaging Code T ⑦Internal code Code	Annce Nominal capacitance[1MHz] 17pF 50pF 85pF Packaging Taping Internal code			
F K 2 ① ①Series name Code FK ②Dimensions (L Code 2125 ③Equivalence ci Code T ④Nominal imped Code Z700	2 3 Series Multilayer EMI s × W) Type (inch) 2125 (0805) ircuit Equivalen T t lance Nominal impedia 70	(4) (5) in name uppression filter uppression filter (L × W) [mm] 2.0 × 1.25 (L × W) [mm] ce circuit (L × W) [mm] gance[100MHz] (L × Q) Ω (L × Q) Ω (L × Q)	© ⑦ SNominal capacit Code C170 C500 C850 ©Packaging Code T ⑦Internal code Code	Annce Nominal capacitance[1MHz] 17pF 50pF 85pF Packaging Taping Internal code			

STANDARD EXTERNAL DIMENSIONS/STANDARD QUANTITY



	L	W	т	e ¹	e ²	Standard quantity[pcs] Embossed tape
	2.0 ± 0.2	1.25 ± 0.2	1.0 ± 0.2	0.3±0.2	0.4 ± 0.2	2000
	(0.079 ± 0.008)	(0.049 ± 0.008)	(0.039 ± 0.008)	(0.012 ± 0.008)	(0.016 ± 0.008)	3000
-						Unit:mm(inch)
						,

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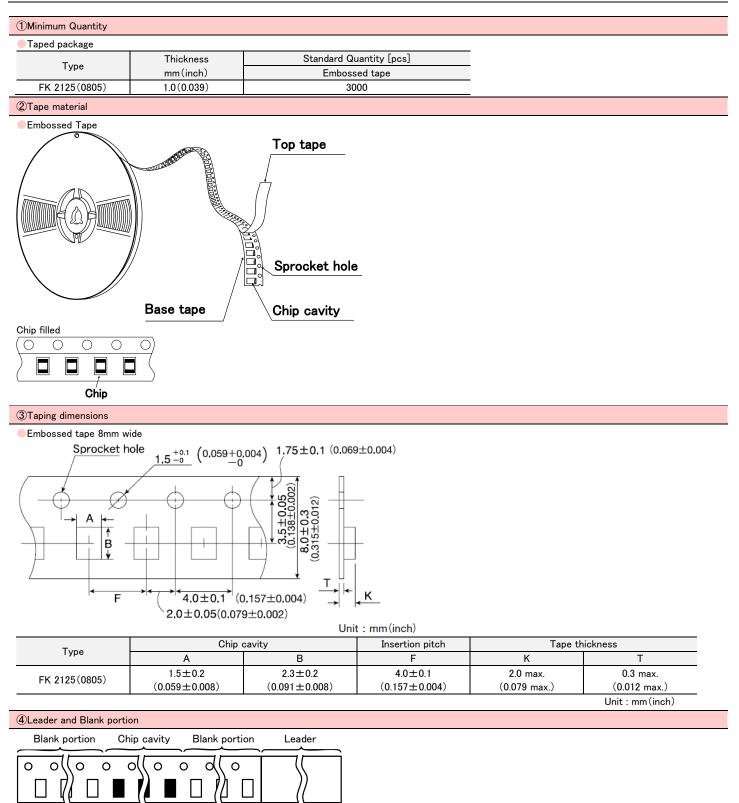
		21.10	Characteristic								Insulation			
		EHS frequency	insertion loss	attnuation				DC resistance [Ω](max.)	Rated voltage [V](DC)	Rated current [mA](DC)	resistance			
		[MHz]	[1MHz]	50MHz	100MHz	200MHz	350MHz	500MHz	600MHz	800MHz				[MΩ]
FK2125T 186AL-T	RoHS	18±3.6	≦1.0dB	≧20dB	≧20dB	-	-	\geq 20dB	-	-	2	10	100	≧30
FK2125T 256AL-T	RoHS	25±5	≦1.0dB	≧15dB	≧20dB	-	-	\geq 20dB	-	-	2	10	100	≧30
FK2125T 406AL-T	RoHS	40±10	≦1.0dB	-	≧15dB	\geq 20dB	-	\geq 20dB	-	-	2	10	100	≧30
FK2125T 107AL-T	RoHS	100 ± 20	≦1.0dB	-	-	\geq 20dB	-	\geq 20dB	-	-	3	10	100	≧30
FK2125T 167AL-T	RoHS	160 ± 30	≦1.0dB	-	-	-	≧20dB	\geq 20dB	-	-	2	10	100	≧30
FK2125T 207AL-T	RoHS	200±40	≦1.0dB	-	-	-	≧20dB	\geq 20dB	-	-	2	10	100	≧30
FK2125T 407AL-T	RoHS	400±80	≦1.0dB	-	-	-	-	-	≧20dB	≧20dB	2	10	100	≧30

ΤZ	Series

Parts number	EHS	Impedance (terminal1-3) [100MHz]	Capacitance(terminal1-2) [1MHz]	DC resistance [Ω](max.)	Rated voltage [V] (DC)	Rated current [mA] (DC)	Insulation resistance [MΩ]
FK2125TZ700C170T	RoHS	$70\Omega \pm 30\%$	17pF±20%	2	10	100	≧30
FK2125TZ700C500T	RoHS	$70\Omega\pm30\%$	50pF±20%	2	10	100	≧30
FK2125TZ700C850T	RoHS	$70\Omega \pm 30\%$	85pF±20%	2	10	100	≧30
FK2125TZ101C170T	RoHS	$100 \Omega \pm 30\%$	17pF±20%	2	10	100	≧30
FK2125TZ101C500T	RoHS	$100 \Omega \pm 30\%$	50pF±20%	2	10	100	≧30
FK2125TZ101C850T	RoHS	$100 \Omega \pm 30\%$	85pF±20%	2	10	100	≧30
FK2125TZ201C850T	RoHS	$200 \Omega \pm 30\%$	85pF±20%	2	10	100	≧30

MULTILAYER EMI SUPPRESSION FILTERS

PACKAGING



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100mm or more

(3.94inches or more)

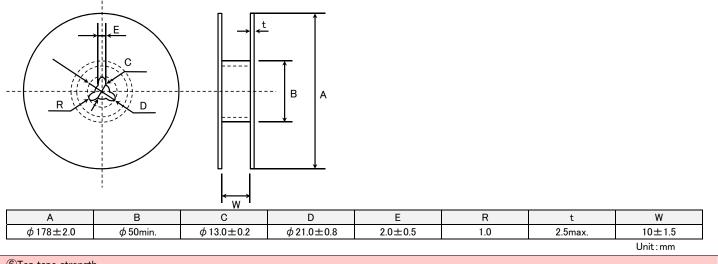
Direction of tape feed

400mm or more

(15.7inches or more)

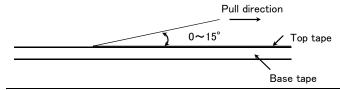
160mm or more

(6.3inches or more)



6 Top tape strength

The top tape requires a peel;-off force of $0.1 \sim 0.7 N$ in the direction of the arrow as illustrated below.



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MULTILAYER EMI SUPPRESSION FILTERS

RELIABILITY DATA

1. Operating Temperature Range				
Specified Value	-25~+85°C			

2. Storage Tempera	ture Range
Specified Value	-25~+85°C

3. Rated Voltage	
Specified Value	10V DC

4. Rated Current	
Specified Value	100mA DC

5. Cutoff frequency	(T Series)	
Specified Value	18MHz±3.6MHz, 25MHz±5MI 160MHz±30MHz, 200MHz±40	Hz, 40MHz±10MHz, 100MHz±20MHz, 0MHz, 400MHz±80MHz
Test Methods and Remarks	Measuring equipment Measuring source Input-Output impedance	: 8753D (or its equivalent) : 0dBm : 50 Ω

6. Impedance (TZ S	eries)	
Specified Value	$70 \Omega \pm 30\%$, $100 \Omega \pm 30\%$	6, 200 $\Omega \pm 30\%$
Test Methods and Remarks	Measuring frequency Measuring equipment Measuring jig Measuring source	: 100MHz : 4291A (or its equivalent) : 16192A : —20dBm

7. Capacitance (TZ	Series)			
Specified Value	7pF±20%, 50pF±20%, 85pF±20%			
Test Methods and Remarks	Measuring equipment Measuring voltage Measuring frequency Capacitance measuremer	: 4194A (or its equivalent) : 0.5V : 1MHz nt between Terminals 1 and 2.		

8. DC Resistance	
Specified Value	2Ω max., 3Ω max. (FK2125T107AL)
Test Methods and Remarks	Conduct measurement between Terminals 1 and 3.

9. Insulation Resistance				
Specified Value	DMΩ min.			
Test Methods and Remarks	Conduct measurement between Terminals 1 and 2. Applied voltage : 10VDC			

10. Resistance to Flexure of Substrate					
Specified Value	No mechanical damage.				
Test Methods and Remarks	Warp : 2mm Testing board : glass epoxy-resin substrate Thickness : 0.8mm Board Unit : mm)				

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11. Solderability	11. Solderability					
Specified Value	At least 75% of terminal electrode is covered by new solder.					
Test Methods and Remarks	Solder temperature Duration Preheating temperature Preheating time Flux	: 230±5°C : 4±1 sec. : 150 to 180°C : 2 to 3 min. : Immersion into methanol solution with colophony for 3 to 5 sec.				

12. Resistance to Soldering					
Specified Value	No significant abnormality in appearance.				
Test Methods and Remarks	Solder temperature Duration Preheating temperature Preheating time Flux	: $260\pm5^{\circ}$ C : 10 ± 0.5 sec. : 150 to 180° C : 2 to 3 min. : Immersion into methanol solution with colophony for 3 to 5 sec.			

Specified Value	No mechani Insulation re	0	:20MΩ min.	
	DC resistance (between 1 and 3)		: 2Ω max.	
	: 3Ω max. (FK2125T107AL)			
	Conditions f	for 1 cycle		
	Step	Temperature (°C)		Duration (min)
	1	Minimum operating temperature $+0/-3$		30 ± 3
Test Methods and	2	Room temperature		2 to 3
Remarks	3	Maximum operating temperature $+3/-0$		30 ± 3
	4	Room temperature		2 to 3

14. Damp Heat stea	idy state			
Specified Value	No mechanical damage. Insulation resistance (between 1 and 2) DC resistance (between 1 and 3)		: 20MΩ min. : 2Ω max. : 3Ω max. (FK2125T107AL)	
Test Methods and Remarks	Temperature Humidity Duration Recovery	: 40±2°C : 90 to 95%RH : 500±12 hrs : 2 to 3 hrs of recovery unde	er the standard condition after the removal from test chamber.	

15. Loading under Damp Heat					
Specified Value	No mechanical damage. Insulation resistance (between 1 and 2) DC resistance (between 1 and 3)		: 20MΩ min. : 2Ω max. : 3Ω max. (FK2125T107AL)		
Test Methods and Remarks	Temperature Humidity Applied voltage Applied current Duration Recovery	: 40±2°C : 90 to 95%RH : Rated voltage (between : Rated current (between : 500±12 hrs : 2 to 3 hrs of recovery u			

16. Loading at High	Temperature			
Specified Value	No mechanical damage.Insulation resistance (between 1 and 2): $20M \Omega$ min.DC resistance (between 1 and 3): 2Ω max.: 3Ω max. (FK2125T107AL)			
Test Methods and Remarks	Temperature Applied voltage Applied current Duration Recovery	: 85±2°C : Rated voltage (betweer : Rated current (betweer : 500±12 hrs : 2 to 3 hrs of recovery u		

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Note on standard condition :

"standard condition" referred to herein is defined as follows :

5 to 35°C of temperature, 45 to 85% relative humidity and 86 to 106kPa of air pressure.

When there are questions concerning measurement results:

In order to provide correlation data, the test shall be conducted under condition of $20\pm2^\circ$ C of temperature, 60 to 70% relative humidity and 86 to 106kPa of air pressure.

Unless otherwise specified, all the tests are conducted under the "standard condition."

☆Circuit diagram

10 03 IN OUT 11111 GND

Since neither 1 nor 3 is directional, either could be served as the IN terminal.

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