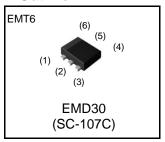


PNP + NPN Complex Digital Transistors (Bias Resistor Built-in Transistors)

<For DTr1(PNP)>

Parameter	Value
V _{CC}	-30V
I _{C(MAX.)}	–200mA
R ₁	1kΩ
R_2	10kΩ

Outline



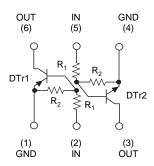
<For DTr2(NPN)>

Parameter	Value
V_{CC}	50V
I _{C(MAX.)}	100mA
R ₁	10kΩ
R_2	10kΩ

Features

- 1) Both the DTB713Z chip and DTC114E chip in one package.
- 2) Built-in bias resistors enable the configuration of an inverter circuit without connecting external input resistors (see inner circuit).
- 3) The bias resistors consist of thin-film resistors with complete isolation to allow negative biasing of the input. They also have the advantage of completely eliminating parasitic effects.
- Only the on/off conditions need to be set for operation, making the circuit design easy.
- 5) Lead Free/RoHS Compliant.

•Inner circuit



Application

Inverter circuit, Interface circuit, Driver circuit

Packaging specifications

Part No.	Package	Package size (mm)	Taping code	Reel size (mm)	Tape width (mm)	Basic ordering unit (pcs)	Marking
EMD30	EMT6	1616	T2R	180	8	8,000	D30

● Absolute maximum ratings (Ta = 25°C)

Parameter	Symbol	DTr1(PNP)	DTr2(NPN)	Unit
Supply voltage	V _{CC}	-30	50	V
Input voltage	V _{IN}	-10 to +5	-10 to +40	V
Output current	Io	- 50		mA
Collector current	I _{C(MAX.)} *1	-200 100		mA
Power dissipation	P _D *2	150 (Total)*3		mW
Junction temperature	T _j	150		°C
Range of storage temperature	T _{stg}	-55 to +150		°C

●Electrical characteristics(Ta = 25°C) <For DTr1(PNP)>

Parameter	Symbol	Conditions	Min.	Тур.	Max.	Unit	
la must usalta ma	$V_{I(off)}$	$V_{CC} = -5V, I_{O} = -100\mu A$	ı	ı	-0.3	V	
Input voltage	$V_{I(on)}$	$V_0 = -0.3V, I_0 = -20mA$	-2.5	-	-	v	
Output voltage	$V_{O(on)}$	$I_{O}/I_{I} = -50 \text{mA}/-2.5 \text{mA}$	-	-0.07	-0.3	V	
Input current	I _I	$V_1 = -5V$	-	-	-6.4	mA	
Output current	$I_{O(off)}$	$V_{CC} = -30V, V_{I} = 0V$	-	-	-0.5	μΑ	
DC current gain	Gı	$V_0 = -2V, I_0 = -100 \text{mA}$	140	-	-	-	
Input resistance	R ₁	-	0.7	1	1.3	kΩ	
Resistance ratio	R ₂ /R ₁	-	8	10	12	-	
Transition frequency	f _T *1	$V_{CE} = -10V, I_{E} = 5mA$ f = 100MHz		260		MHz	

●Electrical characteristics(Ta = 25°C) <For DTr2(NPN)>

Parameter	Symbol	Conditions	Min.	Тур.	Max.	Unit
la must usalta ma	$V_{I(off)}$	$V_{CC} = 5V, I_{O} = 100 \mu A$	-	-	0.5	V
Input voltage	$V_{I(on)}$	$V_0 = 0.3V, I_0 = 2mA$	3.0	-	-	V
Output voltage	$V_{O(on)}$	$I_{O}/I_{I} = 10mA/0.5mA$	-	0.1	0.3	V
Input current	l ₁	V _I = 5V	-	-	880	μΑ
Output current	I _{O(off)}	$V_{CC} = 50V, V_I = 0V$	-	-	0.5	μΑ
DC current gain	G _I	$V_O = 5V$, $I_O = 5mA$	30	-	-	-
Input resistance	R ₁	-	7	10	13	kΩ
Resistance ratio	R ₂ /R ₁	-	0.8	1.0	1.2	-
Transition frequency	f _T *1	$V_{CE} = 10V, I_{E} = -5mA$ f = 100MHz	-	250	-	MHz

^{*1} Characteristics of built-in transistor

^{*2} Each terminal mounted on a reference footprint

^{*3 120}mW per element must not be exceeded.

●Electrical characteristic curves (Ta = 25°C) <For DTr1(NPN)>

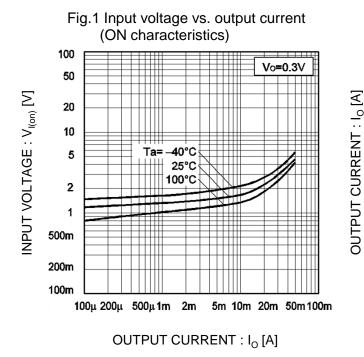


Fig.2 Output current vs. input voltage (OFF characteristics) 10m 5m 2m Ta=100°C 1m 25°C 500μ 40°C 200μ 100μ 50μ 20μ 10_µ 5μ 2μ 1μ 0 0.5 1.0 1.5 2.0 2.5 3.0 INPUT VOLTAGE : $V_{I(off)}[V]$

 $I_1 = 260 \mu A$ 50 240μΑ OUTPUT CURRENT : Io [mA] 220μΑ 40 DC CURRENT GAIN: G 200μΑ 180μΑ 30 160μΑ 20 120μΑ 10 Ta=25°C 100μΑ 0 5 10 0

OUTPUT VOLTAGE: Vo [V]

Fig.3 Output current vs. output voltage

1k Vo= 5V 500 Ta= 100°C 25°C 200 40°C 100 50 20 10 5 2 100μ 200μ 500μ 1m 2m 5m 10m 20m 50m 100m

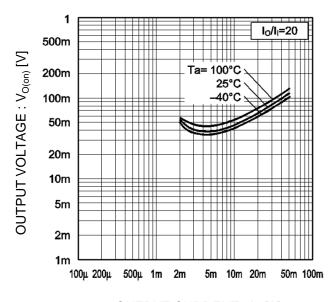
OUTPUT CURRENT : Io [A]

Fig.4 DC current gain vs. output current

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●Electrical characteristic curves (Ta = 25°C) <For DTr1(PNP)>

Fig.5 Output voltage vs. output current



OUTPUT CURRENT : Io [A]

●Electrical characteristic curves (Ta = 25°C) <For DTr2(PNP)>

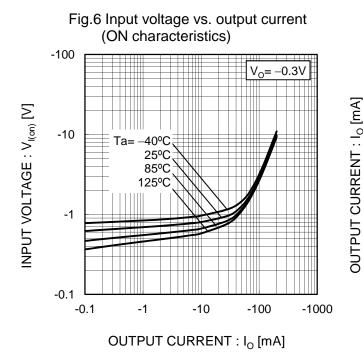


Fig.7 Output current vs. input voltage (OFF characteristics)

-100

-10

-10

Ta= 125°C

85°C

25°C

-40°C

INPUT VOLTAGE: V_{I(off)}[V]

●Electrical characteristic curves (Ta = 25°C) <For DTr2(NPN)>

Fig.8 Output current vs. output voltage

Fig.9 DC current gain vs. output current

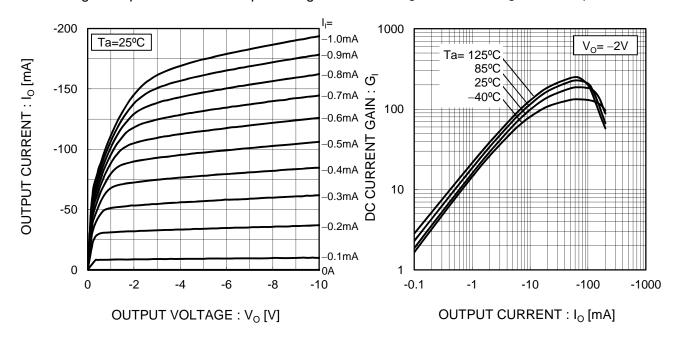
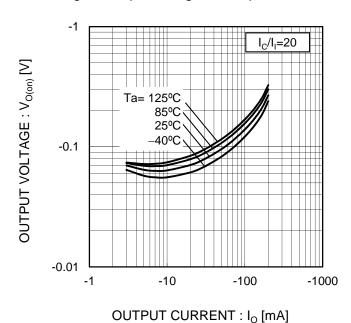
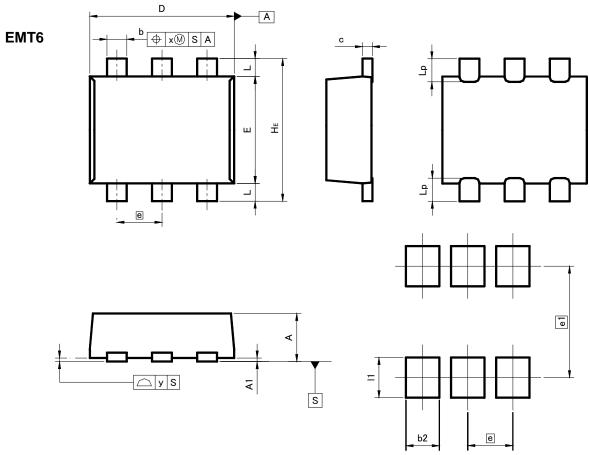


Fig.10 Output voltage vs. output current



●Dimensions (Unit : mm)



Pattern of terminal position areas [Not a recommended pattern of soldering pads]

DIM	MILIM	ETERS	INCHES		
DIM	MIN	MAX	MIN	MAX	
Α	0.45	0.55	0.018	0.022	
A1	0.00	0.10	0.000	0.004	
b	0.17	0.27	0.007	0.011	
С	0.08	0.18	0.003	0.007	
D	1.50	1.70	0.059	0.067	
E	1.10	1.30	0.043	0.051	
е	0.	50	0.020		
HE	1.50	1.70	0.059	0.067	
L	0.10	0.30	0.004	0.012	
Lp	_	0.35		0.014	
х	_	0.10	_	0.004	
у	_	0.10		0.004	

DIM	MILIM	ETERS	INCHES		
DIM MIN		MAX	MIN	MAX	
b2	- 0.37		ı	0.015	
e1	1.3	25	0.0	49	
11	_	0.45	-	0.018	

Dimension in mm / inches

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