

DTD743X series

NPN 200mA 30V Digital Transistors (Bias Resistor Built-in Transistors)

Parameter	Value
V _{CC}	30V
I _{C(MAX.)}	200mA
R ₁	4.7kΩ
R ₂	10kΩ

Features

- 1) Built-In Biasing Resistors
- Built-in bias resistors enable the configuration of an inverter circuit without connecting external input resistors (see inner circuit).
- The bias resistors consist of thin-film resistors with complete isolation to allow negative biasing of the input. They also have the advantage of completely eliminating parasitic effects.
- 4) Only the on/off conditions need to be set for operation, making the circuit design easy.
- 5) Complementary PNP Types :DTB743X series
- 6) Lead Free/RoHS Compliant.

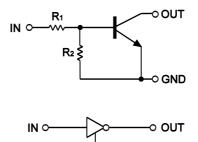
Application

Switching circuit, Inverter circuit, Interface circuit, Driver circuit

Outline

VMT3	EMT3
INGND	
DTD743XM (SC-105AA)	DTD743XE SOT-416 (SC-75A)

Inner circuit



GND

Packaging specif	ications						
Part No.	Package	Package size (mm)	Taping code	Reel size (mm)	Tape width (mm)	Basic ordering unit (pcs)	Marking
DTD743XM	VMT3	1212	T2L	180	8	8,000	M43
DTD743XE	EMT3	1616	TL	180	8	3,000	M43

●Absolute maximum ratings (Ta = 25°C)

Parameter	Symbol	Values	Unit
Supply voltage	V _{cc}	30	V
Input voltage	V _{IN}	-7 to +20	V
Collector current	^{*1} ا _{C(MAX.)}	200	mA
Power dissipation	P_{D}^{*2}	150	mW
Junction temperature	Тj	150	°C
Range of storage temperature	T _{stg}	-55 to +150	°C

•Electrical characteristics(Ta = 25°C)

Parameter	Symbol	Conditions	Min.	Тур.	Max.	Unit
Input voltage	V _{I(off)}	$V_{CC} = 5V, I_{O} = 100 \mu A$	-	-	0.3	v
Input voltage	V _{I(on)}	$V_0 = 0.3V, I_0 = 20mA$	2.5	-	-	
Output voltage	V _{O(on)}	I _O / I _I = 100mA / 5mA	-	0.07	0.3	V
Input current	I _I	$V_1 = 5V$	-	-	1.4	mA
Output current	I _{O(off)}	$V_{CC} = 30V, \ V_I = 0V$	-	-	0.5	μA
DC current gain	G _I	$V_0 = 2V, I_0 = 100mA$	140	-	-	-
Input resistance	R ₁	-	3.29	4.7	6.11	kΩ
Resistance ratio	R ₂ /R ₁	-	1.7	2.1	2.6	-
Transition frequency	f _T *1	V _{CE} = 10V, I _E = -5mA, f = 100MHz	-	260	-	MHz

*1 Characteristics of built-in transistor

*2 Each terminal mounted on a reference footprint

•Electrical characteristic curves(Ta = 25°C)

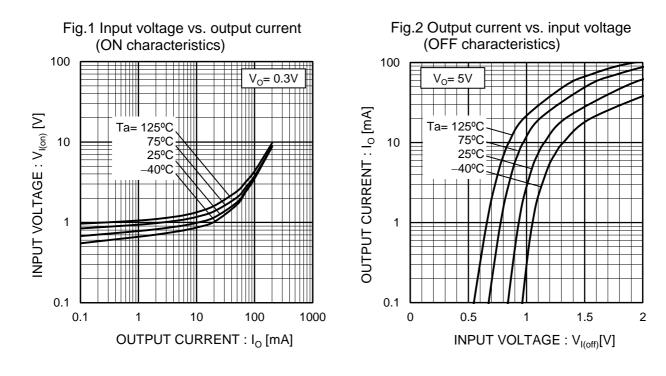
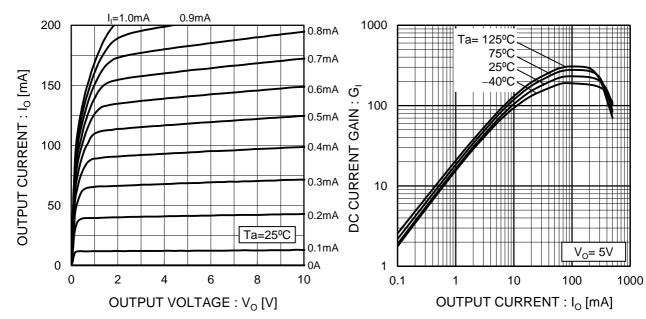


Fig.3 Output current vs. output voltage

Fig.4 DC current gain vs. output current



•Electrical characteristic curves(Ta = 25°C)

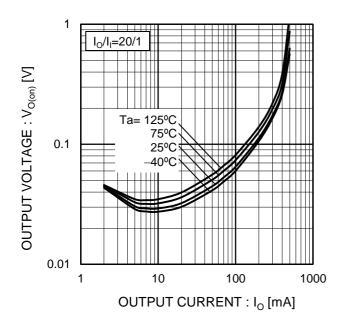
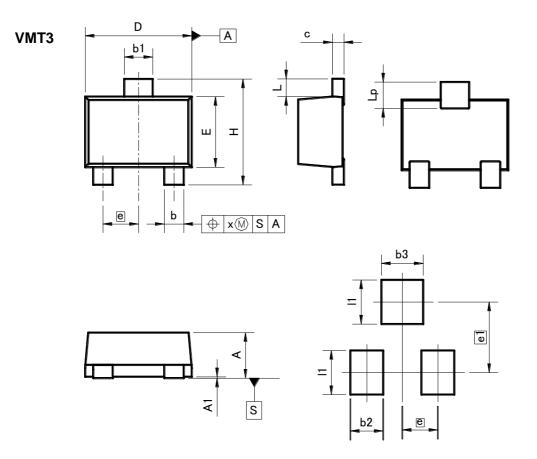


Fig.5 Output voltage vs. output current

•Dimensions (Unit : mm)



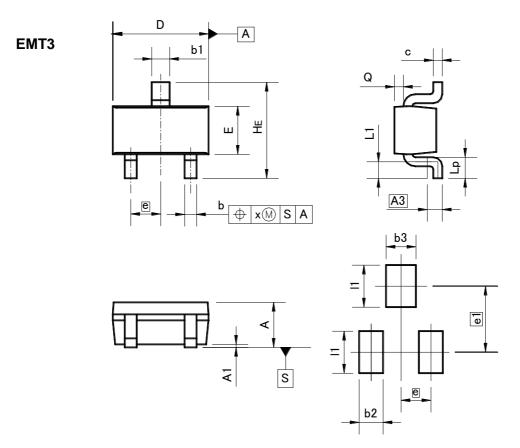
Patterm of terminal position areas

DIM	MILIM	ETERS	INC	HES
DIN	MIN	MAX	MIN	MAX
А	0.45	0.55	0.018	0.022
A1	0.00	0.10	0	0.004
b	0.17	0.27	0.007	0.011
b1	0.27	0.37	0.011	0.015
с	0.08	0.18	0.003	0.007
D	1.10	1.30	0.043	0.051
E	0.70	0.90	0.028	0.035
е	0.4	40	0.0	02
HE	1.10	1.30	0.043	0.051
L	0.10	0.30	0.004	-
Lp	0.20	0.40	0.008	-
х	_	0.10		0.004

DIM	MILIM	METERS		NCHES	
DIM	MIN	MAX	MIN	MAX	
e1	0.80		0.0	03	
b2	-	0.37	-	0.015	
b3	-	0.47	-	0.019	
1	-	0.50	-	0.02	

Dimension in mm/inches

•Dimensions (Unit : mm)



Patterm of terminal position areas

DIM	MILIM	ETERS	INC	HES
DIM	MIN	MAX	MIN	MAX
А	0.60	0.80	0.024	0.031
A1	0.00	0.10	0	0.004
A3	0.	25	0.0	01
b	0.15	0.30	0.006	0.012
b1	0.25	0.40	0.01	0.016
С	0.10	0.20	0.004	0.008
D	1.50	1.70	0.059	0.067
Е	0.70	0.90	0.028	0.035
е	0.	50	0.0	02
HE	1.40	1.80	0.055	0.071
L1	0.10	-	0.004	-
Lp	0.15	_	0.006	_
Q	0.05	0.25	0.002	0.01
х	_	0.10	_	0.004

DIM	MILIM	ETERS	INC	HES
DIM	MIN	MAX	MIN	MAX
e1	1.	1.10		04
b2	-	0.40	Ι	0.016
b3	-	0.50	Ι	0.02
1	-	0.70	_	0.028

Dimension in mm/inches

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