

# DTD543E series

NPN 500mA 12V Digital Transistors (Bias Resistor Built-in Transistors)

Parameter	Value
V <sub>CC</sub>	12V
I <sub>C(MAX.)</sub>	500mA
$R_1$	4.7kΩ
$R_2$	4.7kΩ

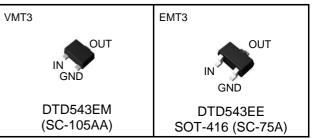
## Features

- 1) Built-In Biasing Resistors,  $R_1 = R_2 = 4.7 k\Omega$ .
- Built-in bias resistors enable the configuration of an inverter circuit without connecting external input resistors (see inner circuit).
- The bias resistors consist of thin-film resistors with complete isolation to allow negative biasing of the input. They also have the advantage of completely eliminating parasitic effects.
- 4) Only the on/off conditions need to be set for operation, making the circuit design easy.
- 5) Complementary PNP Types :DTB543E series
- 6) Lead Free/RoHS Compliant.

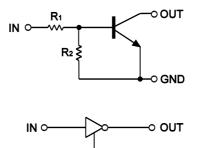
## Application

Switching circuit, Inverter circuit, Interface circuit, Driver circuit

#### Outline



#### Inner circuit



GND

Packaging specif	ications						
Part No.	Package	Package size (mm)	Taping code	Reel size (mm)	Tape width (mm)	Basic ordering unit (pcs)	Marking
DTD543EM	VMT3	1212	T2L	180	8	8,000	X23
DTD543EE	EMT3	1616	TL	180	8	3,000	X23

## ●Absolute maximum ratings (Ta = 25°C)

Parameter	Symbol	Values	Unit
Supply voltage	V <sub>cc</sub>	12	V
Input voltage	V <sub>IN</sub>	-10 to +12	V
Collector current	<sup>*1</sup> ا <sub>C(MAX.)</sub>	500	mA
Power dissipation	$P_D^{*2}$	150	mW
Junction temperature	Т <sub>ј</sub>	150	°C
Range of storage temperature	T <sub>stg</sub>	-55 to +150	°C

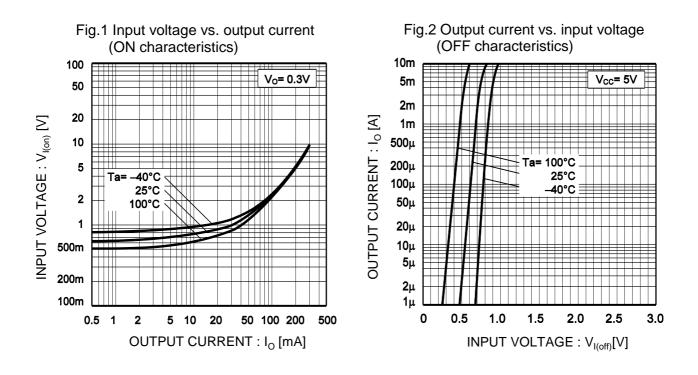
## •Electrical characteristics(Ta = 25°C)

Parameter	Symbol	Conditions	Min.	Тур.	Max.	Unit
	V <sub>I(off)</sub>	$V_{CC} = 5V, I_{O} = 100 \mu A$	-	-	0.5	V
Input voltage	V <sub>I(on)</sub>	$V_0 = 0.3V, I_0 = 20mA$	2.5	-	-	V
Output voltage	V <sub>O(on)</sub>	I <sub>O</sub> / I <sub>I</sub> = 100mA / 5mA	-	0.06	0.3	V
Input current	I <sub>I</sub>	$V_1 = 5V$	-	-	1.4	mA
Output current	I <sub>O(off)</sub>	$V_{CC} = 12V, \ V_I = 0V$	-	-	0.5	μA
DC current gain	G <sub>I</sub>	$V_0 = 2V, I_0 = 100mA$	115	-	-	-
Input resistance	R <sub>1</sub>	-	3.29	4.7	6.11	kΩ
Resistance ratio	R <sub>2</sub> /R <sub>1</sub>	-	0.8	1	1.2	-
Transition frequency	f <sub>T</sub> *1	V <sub>CE</sub> = 10V, I <sub>E</sub> = -5mA, f = 100MHz	-	260	-	MHz

\*1 Characteristics of built-in transistor

\*2 Each terminal mounted on a reference footprint

## •Electrical characteristic curves(Ta = 25°C)



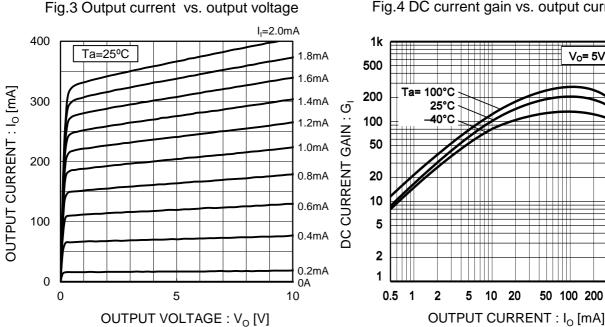


Fig.4 DC current gain vs. output current

Vo= 5V

500

## •Electrical characteristic curves(Ta = 25°C)

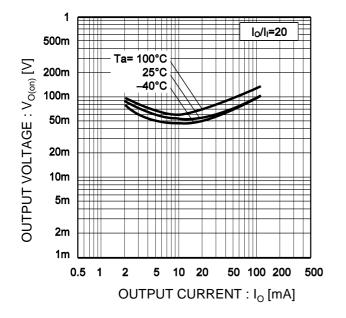
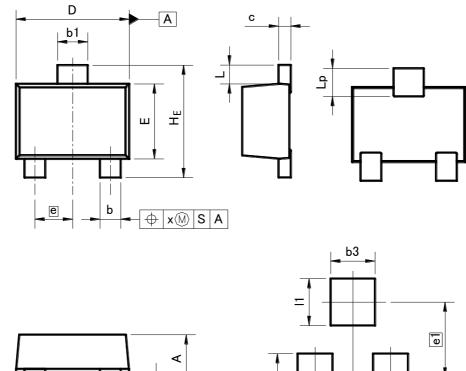
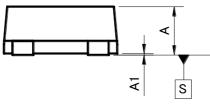


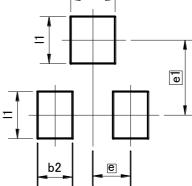
Fig.5 Output voltage vs. output current

### •Dimensions (Unit : mm)









Pattern of terminal position areas [Not a recommended pattern of soldering pads]

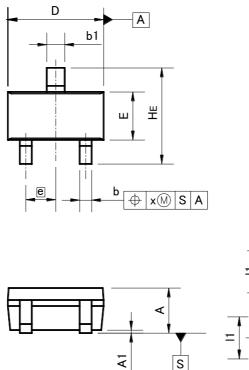
DIM	MILIM	ETERS	INC	HES
DIM	MIN	MAX	MIN	MAX
А	0.45	0.55	0.018	0.022
A1	0.00	0.10	0.000	0.004
b	0.17	0.27	0.007	0.011
b1	0.27	0.37	0.011	0.015
с	0.08	0.18	0.003	0.007
D	1.10	1.30	0.043	0.051
E	0.70	0.90	0.028	0.035
e	0.40		0.0	02
HE	1.10	1.30	0.043	0.051
L	0.10	0.30	0.004	0.012
Lp	0.20	0.40	0.008	0.016
х	-	0.10	_	0.004

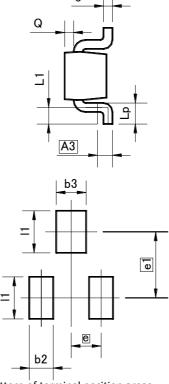
DIM	MILIM	MILIMETERS		HES
DIM	MIN	MAX	MIN	MAX
b2	-	0.37	-	0.015
b3	-	0.47	-	0.019
e1	0.80		0.0	31
1	_	0.50	_	0.020

Dimension in mm / inches

### •Dimensions (Unit : mm)

EMT3





Pattern of terminal position areas [Not a recommended pattern of soldering pads]

	MILIM	ETERS	INC	HES
DIM	MIN	MAX	MIN	MAX
А	0.60	0.80	0.024	0.031
A1	0.00	0.10	0.000	0.004
A3	0.:	25	0.0	10
b	0.15	0.30	0.006	0.012
b1	0.25	0.40	0.010	0.016
с	0.10	0.20	0.004	0.008
D	1.50	1.70	0.059	0.067
E	0.70	0.90	0.028	0.035
е	0.	50	0.0	20
HE	1.40	1.80	0.055	0.071
L1	0.10	-	0.004	-
Lp	0.15	_	0.006	-
Q	0.05	0.25	0.002	0.010
х	_	0.10	_	0.004

DIM	MILIM	ETERS	INCHES	
DIM	MIN	MAX	MIN	MAX
b2	_	0.40	-	0.016
b3	-	0.50	-	0.020
e1	1.10		0.0	43
1	_	0.70	-	0.028

Dimension in mm / inches

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