

DTD143TK

NPN 500mA 40V Digital Transistors (Bias Resistor Built-in Transistors)

Parameter	Value
V _{CEO}	40V
Ι _C	500mA
R	4.7kΩ

Features

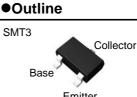
- 1) Built-In Biasing Resistors
- Built-in bias resistors enable the configuration of an inverter circuit without connecting external input resistors (see inner circuit).
- The bias resistors consist of thin-film resistors with complete isolation to allow negative biasing of the input. They also have the advantage of completely eliminating parasitic effects.
- 4) Only the on/off conditions need to be set for operation, making the circuit design easy.
- 5) Complementary PNP Types :DTB143TK
- 6) Lead Free/RoHS Compliant.

Application

Switching circuit, Inverter circuit, Interface circuit, Driver circuit

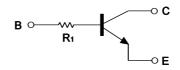
Packaging specifications

Part No.	Package	Package size (mm)	Taping code	Reel size (mm)	Tape width (mm)	Basic ordering unit (pcs)	Marking
DTD143TK	SMT3	2928	T146	180	8	3,000	F03





Inner circuit



●Absolute maximum ratings (Ta = 25°C)

Parameter	Symbol	Values	Unit
Collector-base voltage	V _{CBO}	50	V
Collector-emitter voltage	V _{CEO}	40	V
Emitter-base voltage	V _{EBO}	5	V
Collector current	I _C	500	mA
Collector Power dissipation	P _C ^{*2}	200	mW
Junction temperature	Тj	150	°C
Range of storage temperature	T _{stg}	-55 to +150	°C

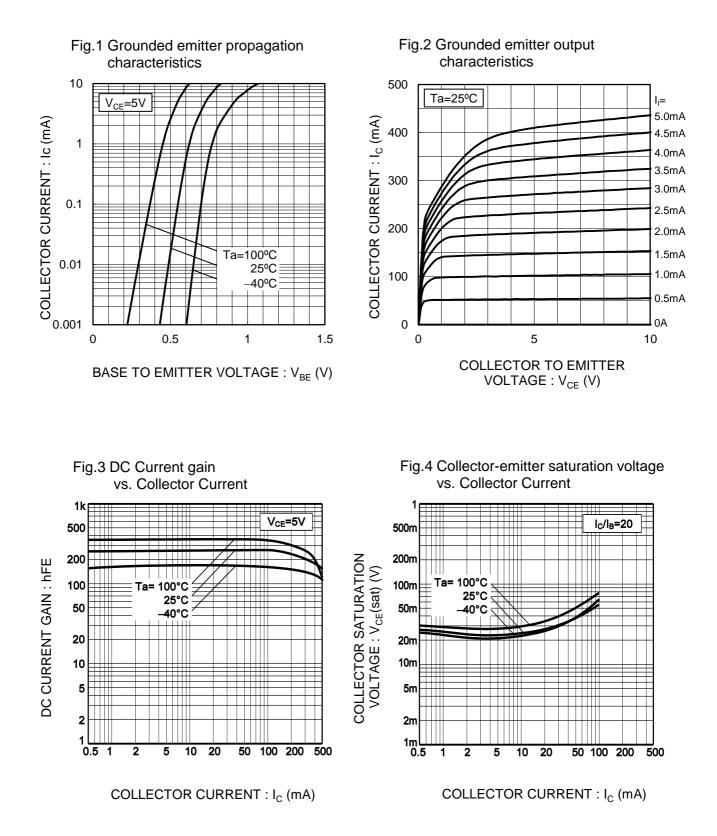
•Electrical characteristics(Ta = 25°C)

Parameter	Symbol	Conditions	Min.	Тур.	Max.	Unit
Collector-base breakdown voltage	BV_{CBO}	I _C = 50μA	50	-	-	V
Collector-emitter breakdown voltage	BV _{CEO}	I _C = 1mA	40	-	-	V
Emitter-base breakdown voltage	BV_{EBO}	I _E = 50μA	5	-	-	V
Collector cut-off current	I _{CBO}	V _{CB} = 50V	-	-	0.5	μA
Emitter cut-off current	I _{EBO}	$V_{EB} = 4V$	-	-	0.5	μA
Collector-emitter saturation voltage	V _{CE(sat)}	I _C / I _B = 50mA / 2.5mA	-	-	0.3	V
DC current gain	h _{FE}	V_{CE} = 5V , I _C = 50mA	100	250	600	-
Emitter-base resistance	R	-	3.29	4.7	6.11	kΩ
Transition frequency	f _T *1	V _{CE} = 10V, I _E = -50mA, f = 100MHz	-	200	-	MHz

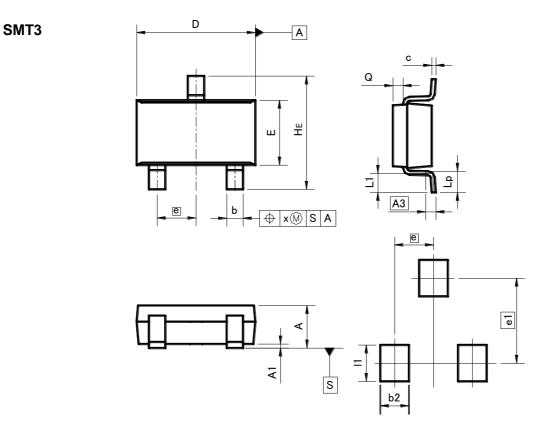
*1 Characteristics of built-in transistor

*2 Each terminal mounted on a reference footprint

•Electrical characteristic curves(Ta = 25°C)



•Dimensions (Unit : mm)



Patterm of terminal position areas

DIM	MILIM	ETERS	INC	HES
DIM	MIN	MAX	MIN	MAX
А	1.00	1.30	-	0.051
A1	0.00	0.10	0	0.004
A3	0.3	25	0.0	01
b	0.35	0.50	0.014	0.02
с	0.09	0.25	0.004	0.01
D	2.80	3.00	0.11	0.118
Ш	1.50	1.80	0.059	0.071
е	0.9	95	0.0	04
HE	2.60	3.00	0.102	0.118
L1	0.30	0.60	0.012	0.024
Lp	0.40	0.70	0.016	0.028
Q	0.20	0.30	0.008	0.012
х	-	0.10	_	0.004
У	_	0.10	_	0.004

DIM	MILIMETERS		INCHES	
DIM	MIN	MAX	MIN	MAX
e1	2.10		0.08	
b2		0.60	-	0.024
1	-	0.90	-	0.035

Dimension in mm/inches

	Notes
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