

Datasheet

Applications

Inverter, Interface, Driver

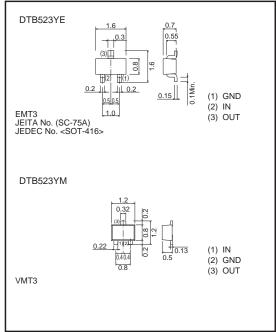
Feature

- 1) VCE (sat) is lower than conventional products.
- 2) Built-in bias resistors enable the configuration of an inverter circuit without connecting external input resistors (see equivalent circuit).
- 3) The bias resistors consist of thin-film resistors with complete isolation to allow positive biasing of the input. They also have the advantage of almost completely eliminating parasitic effects.
- 4) Only the on / off conditions need to be set for operation, making the device design easy.

Structure

PNP epitaxial plannar silicon transistor (Resistor built-in type)

Dimensions (Unit : mm)



Packaging specifications

	Package	EMT3	VMT3
	Packaging type	Taping	Taping
	Code	TL	T2L
Part No.	Basic ordering unit (pieces)	3000	8000
DTB523YE		0	_
DTB523YM		-	0

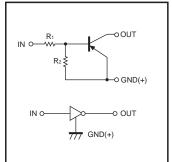
Absolute maximum ratings (Ta=25°C)

Parameter	Symbol	Limits	Unit
Falameter	Symbol	DTB523YE DTB523YM	
Supply voltage	Vcc	-12	V
Input voltage	Vin	-12 to +5	V
Collector current *1	IC (max)	-500	mA
Power dissipation *2	Po	150	mW
Junction temperature	Tj	150	Ĵ
Storage temperature	Tstg	-55 to +150	Ĵ

*1 Characteristics of built-in transistor.

*2 Each terminal mounted on a recommended land.

Inner circuit



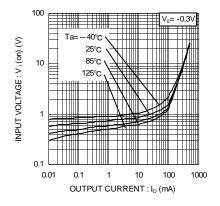
R1=2.2kΩ / R2=10kΩ

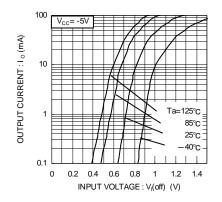
●Electrical characteristics (Ta=25°C)

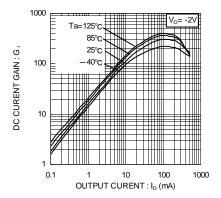
Parameter	Symbol	Min.	Тур.	Max.	Unit	Conditions
Input voltage	VI(off)	-	-	-0.3	V	Vcc=-5V, Io=-100µA
Input voltage	VI(on)	-2.5	-	-		Vo=-0.3V, Io=-20mA
Output voltage	VO(on)	-	-60	-300	mV	lo/l=-100mA / -5mA
Input current	h	-	-	-3.0	mA	VI=-5V
Output current	IO(off)	-	-	-0.5	μΑ	Vcc= -12V, VI=0V
DC current gain	Gi	140	-	-	-	Vo= -2V, Io=-100mA
Transition frequency *	fт	-	260	-	MHz	Vce=-10V, Ie=5mA, f=100MHz
Input resistance	R1	1.54	2.2	2.86	kΩ	_
Resistance ratio	R2/R1	3.6	4.5	5.5	-	_

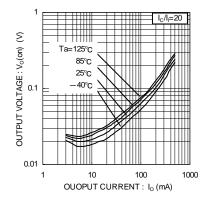
* Characteristics of built-in transistor.

•Electrical characteristics curves









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