

ZXMS6005DT8

# 60V DUAL N-CHANNEL SELF PROTECTED ENHANCEMENT MODE INTELLIFET® MOSFET

#### **Product Summary**

Continuos drain source voltage
 On-state resistance
 Nominal load current (V<sub>IN</sub> = 5V)
 Clamping Energy
 60V
 200mΩ
 1.8A
 210mJ

#### **Description**

The ZXMS6005DT8 is a dual self protected low side MOSFET with logic level input. It integrates over-temperature, over-current, over-voltage (active clamp) and ESD protected logic level functionality. The ZXMS6005DT8 is ideal as a general purpose switch driven from 3.3V or 5V microcontrollers in harsh environments where standard MOSFETs are not rugged enough.

### **Applications**

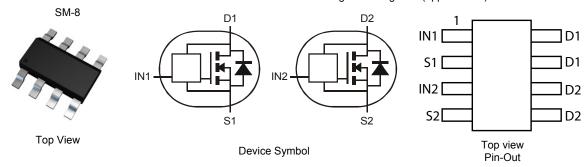
- Lamp Driver
- Motor Driver
- Relay Driver
- Solenoid Driver

#### **Features and Benefits**

- Compact Dual Package
- Low Input Current
- Logic Level Input (3.3V and 5V)
- Short Circuit Protection with Auto Restart
- Over Voltage Protection (active clamp)
- Thermal Shutdown with Auto Restart
- Over-Current Protection
- Input Protection (ESD)
- High Continuous Current Rating
- Lead-Free Finish; RoHS compliant (Note 1 & 2)
- Halogen and Antimony Free. "Green" Device (Note 3)
- Qualified to AEC-Q101 Standards for High Reliability

#### **Mechanical Data**

- Case: SM-8
- Case Material: Molded Plastic, "Green" Molding Compound.
   UL Flammability Classification Rating 94V-0
- Moisture Sensitivity: Level 1 per J-STD-020
- Terminals: Matte Tin Finish
- Weight: 0.117 grams (approximate)



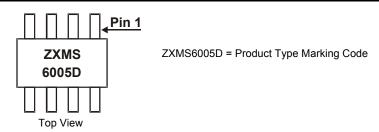
#### Ordering Information (Note 4)

Product	Marking	Reel size (inches)	Tape width (mm)	Quantity per reel
ZXMS6005DT8TA	ZXMS6005D	7	12	1,000

Notes:

- 1. No purposely added lead. Fully EU Directive 2002/95/EC (RoHS) & 2011/65/EU (RoHS 2) compliant.
- See http://www.diodes.com/quality/lead\_free.html for more information about Diodes Incorporated's definitions of Halogen- and Antimony-free, "Green" and Lead-free.
- 3. Halogen- and Antimony-free "Green" products are defined as those which contain <900ppm bromine, <900ppm chlorine (<1500ppm total Br + Cl) and <1000ppm antimony compounds.
- 4. For packaging details, go to our website at http"//www.diodes.com/products/packages.html.

#### **Marking Information**

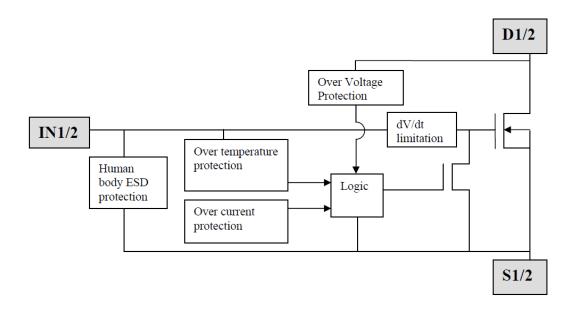


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### **Functional Block Diagram**



### **Application Information**

- Two completely isolated independent channels
- Especially suited for loads with a high in-rush current such as lamps and motors.
- All types of resistive, inductive and capacitive loads in switching applications.
- μC compatible power switch for 12V DC applications.
- Automotive rated.
- Replaces electromechanical relays and discrete circuits.
- Linear Mode capability the current-limiting protection circuitry is designed to de-activate at low V<sub>DS</sub> to minimise on state power dissipation. The maximum DC operating current is therefore determined by the thermal capability of the package/board combination, rather than by the protection circuitry. This does not compromise the product's ability to self-protect at low V<sub>DS</sub>.



ZXMS6005DT8

### Maximum Ratings (@T<sub>A</sub> = +25°C, unless otherwise specified.)

Characteristic	Symbol	Value	Units
Continuous Drain-Source Voltage	$V_{DS}$	60	V
Drain-Source Voltage For Short Circuit Protection	V <sub>DS(SC)</sub>	24	V
Continuous Input Voltage	V <sub>IN</sub>	-0.5 +6	V
Continuous Input Current @ $-0.2V \le V_{IN} \le 6V$ Continuous Input Current @ $V_{IN} < -0.2V$ or $V_{IN} > 6V$	lın	No limit   I <sub>IN</sub>   ≤2	mA
Pulsed Drain Current @V <sub>IN</sub> = 3.3V (Note 7)	I <sub>DM</sub>	5	Α
Pulsed Drain Current @V <sub>IN</sub> = 5V ( Note 7)	I <sub>DM</sub>	6	Α
Continuous Source Current (Body Diode) (Note 5)	Is	2.5	Α
Pulsed Source Current (Body Diode)	I <sub>SM</sub>	10	Α
Unclamped Single Pulse Inductive Energy, T <sub>J</sub> = +25°C, I <sub>D</sub> = 0.5A, V <sub>DD</sub> = 24V	Eas	210	mJ
Electrostatic Discharge (Human Body Model)	V <sub>ESD</sub>	4000	V
Charged Device Model	V <sub>CDM</sub>	1000	V

# Thermal Characteristics (@T<sub>A</sub> = +25°C, unless otherwise specified.)

Characteristic	Symbol	Value	Units
Power Dissipation at T <sub>A</sub> = +25°C (Notes 5 & 8) Linear Derating Factor	P <sub>D</sub>	1.16 9.28	W mW/°C
Power Dissipation at T <sub>A</sub> = +25°C (Notes 5 & 9) Linear Derating Factor	P <sub>D</sub>	1.67 13.3	W mW/°C
Power Dissipation at T <sub>A</sub> = +25°C (Notes 6 & 8) Linear Derating Factor	P <sub>D</sub>	2.13 17	W mW/°C
Thermal Resistance, Junction to Ambient (Notes 5 & 8)	$R_{\theta JA}$	108	°C/W
Thermal Resistance, Junction to Ambient (Notes 5 & 9)	R <sub>θJA</sub>	75	°C/W
Thermal Resistance, Junction to Case (Notes 6 & 8)	$R_{ heta JC}$	58.7	°C/W
Thermal Resistance, Junction to Case (Note 10)	$R_{ heta JC}$	26.5	°C/W
Operating Temperature Range	TJ	-40 to +150	°C
Storage Temperature Range	T <sub>STG</sub>	-55 to +150	°C

- 5. For a dual device surface mounted on a 25mm x 25mm single sided 1oz weight copper split down the middle on 1.6mm FR4 board, in still air conditions.
- 6. For a dual device surface mounted on FR4 PCB measured at t≤ 10sec
- 7. Repetitive rating25mm x 25mm FR4 PCB, D = 0.02, Pulse width = 300µs pulse width limited by junction temperature. Refer to transient thermal impedance graph.
- 8. For a dual device with one active die.
- 9. For a dual device with 2 active die running at equal power.

  10. Thermal resistance from junction to the mounting surface of the drain pin.

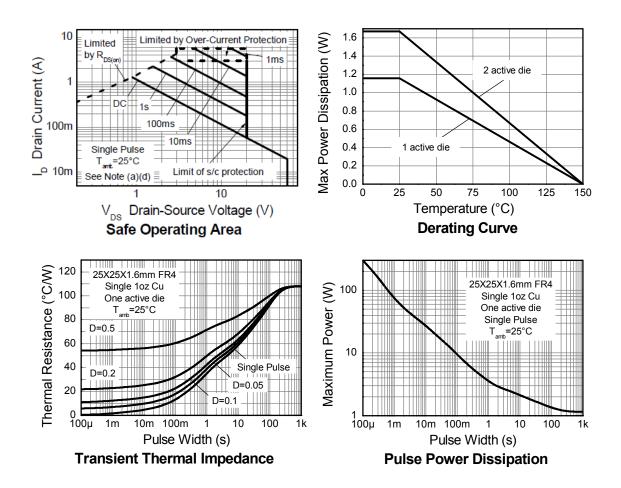
### **Recommended Operating Conditions**

The ZXMS6005DT8 is optimized for use with  $\mu C$  operating from 3.3V and 5V supplies.

Characteristic	Symbol	Min	Max	Unit
Input Voltage Range	V <sub>IN</sub>	0	5.5	V
Ambient Temperature Range	T <sub>A</sub>	-40	125	°C
High Level Input Voltage for MOSFET to be on	$V_{IH}$	3	5.5	V
Low Level Input Voltage for MOSFET to be off	$V_{IL}$	0	0.7	V
Peripheral Supply Voltage (voltage to which load is referred)	V <sub>P</sub>	0	24	V



#### **Thermal Characteristics**







**ZXMS6005DT8** 

### Electrical Characteristics (@T<sub>A</sub> = +25°C, unless otherwise specified.)

Characteristic	Symbol	Min	Тур	Max	Unit	Test Condition
Static Characteristics						
Drain-Source Clamp Voltage	$V_{DS(AZ)}$	60	65	70	V	$I_D = 10mA$
Off State Drain Current	I <sub>DSS</sub>	_	_	1	μA	$V_{DS} = 12V, V_{IN} = 0V$
Oil State Drain Current		_	_	2		$V_{DS} = 36V, V_{IN} = 0V$
Input Threshold Voltage	V <sub>IN(th)</sub>	0.7	1	1.5	V	$V_{DS} = V_{GS}$ , $I_D = 1mA$
Innut Current		_	60	100	μΑ	V <sub>IN</sub> = +3V
Input Current	I <sub>IN</sub>	_	120	200		V <sub>IN</sub> = +5V
Input Current while Over Temperature Active	_	_	_	300	μA	V <sub>IN</sub> = +5V
Static Drain-Source On-State Resistance	R <sub>DS(on)</sub>	_	170	250	mΩ	V <sub>IN</sub> = +3V, I <sub>D</sub> = 1A
Static Drain-Source On-State Resistance		_	150	200		V <sub>IN</sub> = +5V, I <sub>D</sub> = 1A
Continuous Dunin Comment (Natas F 8 0)	I <sub>D</sub>	1.4	_	_	A	V <sub>IN</sub> = 3V; T <sub>A</sub> = +25°C
Continuous Drain Current (Notes 5 & 9)		1.6	_	_		V <sub>IN</sub> = 5V; T <sub>A</sub> = +25°C
Continuous Drain Current (Notes E 9 9)		1.7	_	_		V <sub>IN</sub> = 3V; T <sub>A</sub> = +25°C
Continuous Drain Current (Notes 5 & 8)		1.8	_	_		V <sub>IN</sub> = 5V; T <sub>A</sub> = +25°C
Current Limit (Note 11)	I <sub>D(LIM)</sub>	2.2	5	_	Α	$V_{IN} = +3V$
Current Limit (Note 11)		3.3	7	_		V <sub>IN</sub> = +5V
Dynamic Characteristics						
Turn On Delay Time	t <sub>d(on)</sub>	_	6	_	μs	
Rise Time	t <sub>r</sub>	_	14	_	μs	\\ -12\\   -14 \\ -5\\
Turn Off Delay Time	t <sub>d(off)</sub>	_	34	_	μs	$V_{DD} = 12V, I_D = 1A, V_{GS} = 5V$
Fall Time	f <sub>f</sub>	_	19	_	μs	
Over-Temperature Protection						
Thermal Overload Trip Temperature (Note 12)	$T_{JT}$	150	175	_	°C	
Thermal Hysteresis (Note 12)	f <sub>f</sub>	_	10	_	°C	_

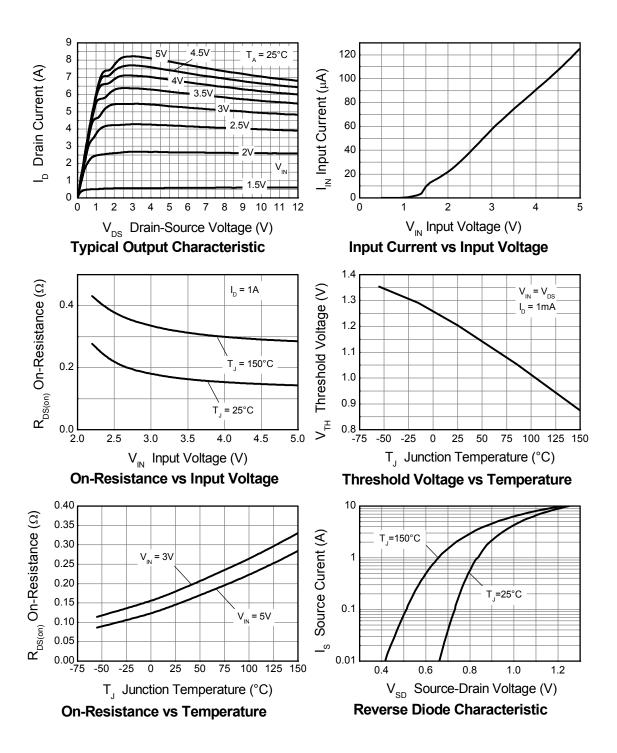
Notes:

<sup>11.</sup> The drain current is restricted only when the device is in saturation (see graph 'typical output characteristic'). This allows the device to be used in the fully on state without interference from the current limit. The device is fully protected at all drain currents, as the low power dissipation generated outside saturation makes current limit unnecessary.

<sup>12.</sup> Over-temperature protection is designed to prevent device destruction under fault conditions. Fault conditions are considered as "outside" normal operating range, so this part is not designed to withstand over-temperature for extended periods...

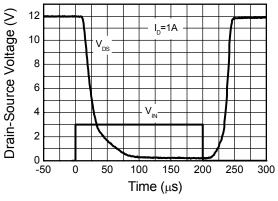


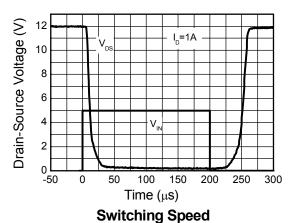
### **Typical Characteristics**



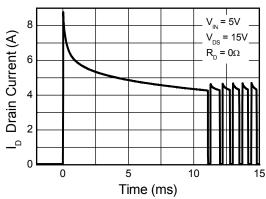


### **Typical Characteristics – (cont.)**





### **Switching Speed**

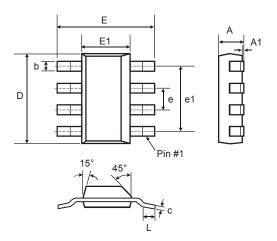


**Typical Short Circuit Protection** 



# **Package Outline Dimensions**

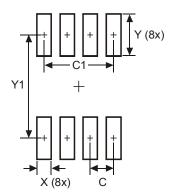
Please see AP02002 at http://www.diodes.com/datasheets/ap02002.pdf for latest version.



SM-8				
Dim	Min	Max	Тур	
Α	-	1.7	_	
A1	0.02	0.1	-	
b	_	0.7	-	
С	0.24	0.32	-	
D	6.3	6.7	-	
е	-	-	1.53	
e1	-	-	4.59	
Е	6.7	7.3	_	
E1	3.3	3.7	_	
L	0.9	_	_	
All Dimensions in mm				

### **Suggested Pad Layout**

Please see AP02001 at http://www.diodes.com/datasheets/ap02001.pdf for the latest version.



Dimensions	Value (in mm)		
С	1.52		
C1	4.6		
Х	0.95		
Y	2.80		
Y1	6.80		





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