

## Features

- Low On-Resistance
  - 54mΩ @  $V_{GS} = -4.5V$
  - 69mΩ @  $V_{GS} = -2.5V$
  - 90mΩ @  $V_{GS} = -1.8V$
- Low Input Capacitance
- Fast Switching Speed
- Low Input/Output Leakage
- Lead Free By Design/RoHS Compliant (Note 1)
- ESD Protected Up To 3kV
- "Green" Device, Halogen and Antimony Free (Note 2)
- Qualified to AEC-Q101 Standards for High Reliability

## Mechanical Data

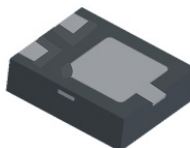
- Case: DFN2015H4-3
- Case Material: Molded Plastic, "Green" Molding Compound. UL Flammability Classification Rating 94V-0
- Moisture Sensitivity: Level 1 per J-STD-020
- Terminals: Finish — Matte Tin over Copper leadframe. Solderable per MIL-STD-202, Method 208
- Terminals Connections: See Diagram Below
- Marking Information: See Page 4
- Ordering Information: See Page 4
- Weight: 0.008 grams (approximate)



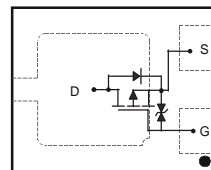
ESD PROTECTED TO 3kV



TOP VIEW



BOTTOM VIEW



Internal Schematic

## Maximum Ratings @ $T_A = 25^\circ C$ unless otherwise specified

Characteristic			Symbol	Value	Units
Drain-Source Voltage			$V_{DSS}$	-20	V
Gate-Source Voltage			$V_{GSS}$	±8	V
Continuous Drain Current (Note 3)	Steady State	$T_A = 25^{\circ}C$	$I_D$	-2.5	A
		$T_A = 70^{\circ}C$		-2.2	
Pulsed Drain Current (Note 4)			$I_{DM}$	-12	A

## Thermal Characteristics

Characteristic	Symbol	Value	Unit
Power Dissipation (Note 3)	$P_D$	0.53	W
Thermal Resistance, Junction to Ambient @ $T_A = 25^\circ C$	$R_{\theta JA}$	231	$^\circ C/W$
Operating and Storage Temperature Range	$T_J, T_{STG}$	-55 to +150	$^\circ C$

Notes:

- No purposefully added lead.
- Diodes Inc.'s "Green" policy can be found on our website at [http://www.diodes.com/products/lead\\_free/index.php](http://www.diodes.com/products/lead_free/index.php).
- Device mounted on FR-4 PCB with minimum recommended pad layout.
- Repetitive rating, pulse width limited by junction temperature.

**Electrical Characteristics** @T<sub>A</sub> = 25°C unless otherwise specified

Characteristic	Symbol	Min	Typ	Max	Unit	Test Condition
OFF CHARACTERISTICS (Note 5)						
Drain-Source Breakdown Voltage	BV <sub>DSS</sub>	-20	—	—	V	V <sub>GS</sub> = 0V, I <sub>D</sub> = -250μA
Zero Gate Voltage Drain Current T <sub>J</sub> = 25°C	I <sub>DSS</sub>	—	—	-1.0	μA	V <sub>DS</sub> = -20V, V <sub>GS</sub> = 0V
Gate-Source Leakage	I <sub>GSS</sub>	—	—	±10	μA	V <sub>GS</sub> = ±8V, V <sub>DS</sub> = 0V
ON CHARACTERISTICS (Note 5)						
Gate Threshold Voltage	V <sub>GS(th)</sub>	-0.3	-0.55	-1.0	V	V <sub>DS</sub> = V <sub>GS</sub> , I <sub>D</sub> = -250μA
Static Drain-Source On-Resistance	R <sub>DS (ON)</sub>	—	36	54	mΩ	V <sub>GS</sub> = -4.5V, I <sub>D</sub> = -2.5A
			46	69		V <sub>GS</sub> = -2.5V, I <sub>D</sub> = -2.2A
			60	90		V <sub>GS</sub> = -1.8V, I <sub>D</sub> = -2.0A
Forward Transfer Admittance	Y <sub>fs</sub>	—	8	—	S	V <sub>DS</sub> = -5V, I <sub>D</sub> = -2.5A
DYNAMIC CHARACTERISTICS (Note 6)						
Input Capacitance	C <sub>iss</sub>	—	214	—	pF	V <sub>DS</sub> = -10V, V <sub>GS</sub> = 0V f = 1.0MHz
Output Capacitance	C <sub>oss</sub>	—	104	—	pF	
Reverse Transfer Capacitance	C <sub>rss</sub>	—	25	—	pF	
Gate Resistnace	R <sub>g</sub>	—	250	—	Ω	V <sub>DS</sub> = 0V, V <sub>GS</sub> = 0V, f = 1.0MHz
SWITCHING CHARACTERISTICS (Note 6)						
Total Gate Charge	Q <sub>g</sub>	—	9.1	—	nC	V <sub>GS</sub> = -4.5V, V <sub>DS</sub> = -10V, I <sub>D</sub> = -4A
Gate-Source Charge	Q <sub>gs</sub>	—	1.5	—	nC	
Gate-Drain Charge	Q <sub>gd</sub>	—	1.7	—	nC	
Turn-On Delay Time	t <sub>D(on)</sub>	—	80.4	—	ns	V <sub>DS</sub> = -10V, V <sub>GS</sub> = -4.5V, R <sub>D</sub> = 2.5Ω, R <sub>G</sub> = 3.0Ω
Turn-On Rise Time	t <sub>r</sub>	—	155.1	—	ns	
Turn-Off Delay Time	t <sub>D(off)</sub>	—	688.1	—	ns	
Turn-Off Fall Time	t <sub>f</sub>	—	423.8	—	ns	

Notes: 5. Short duration pulse test used to minimize self-heating effect.  
6. Guaranteed by design. Not subject to production testing.

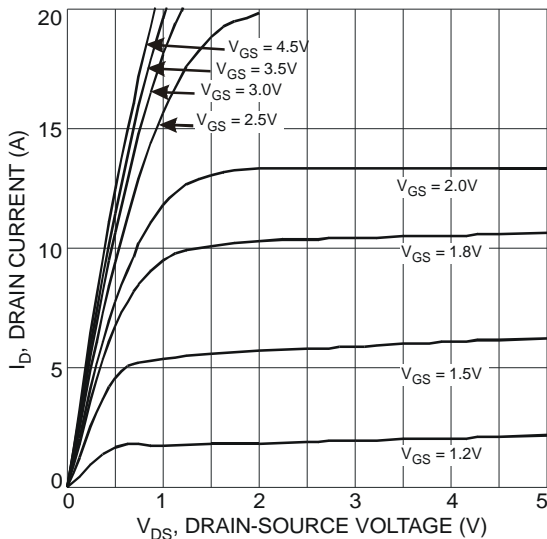


Fig. 1 Typical Output Characteristic

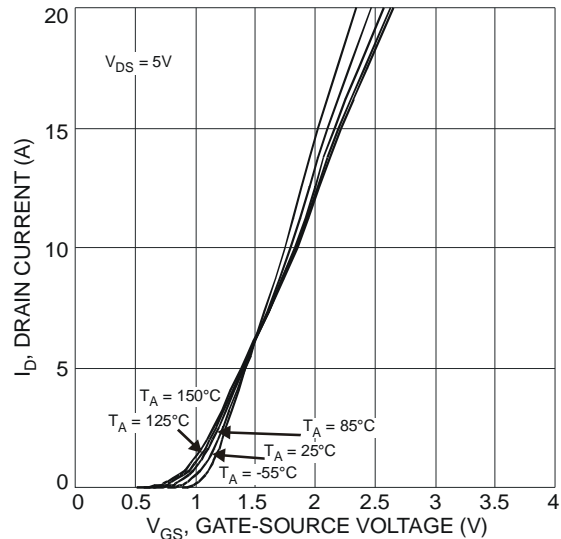


Fig. 2 Typical Transfer Characteristic

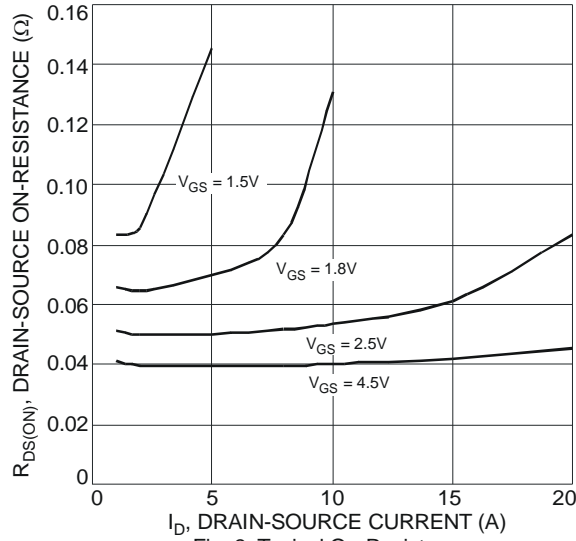


Fig. 3 Typical On-Resistance vs. Drain Current and Gate Voltage

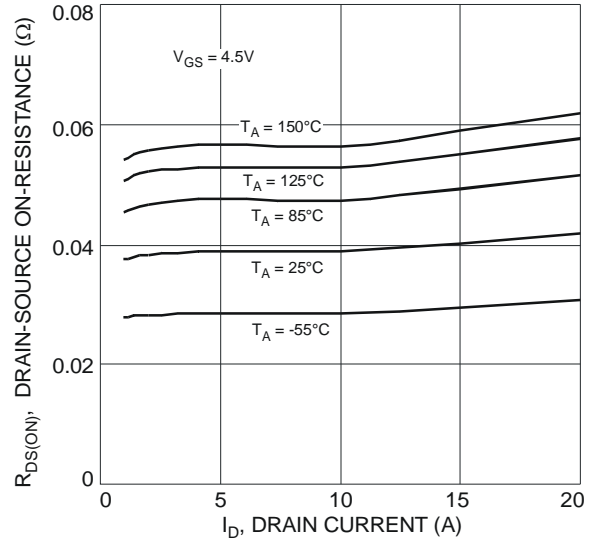


Fig. 4 Typical On-Resistance vs. Drain Current and Temperature

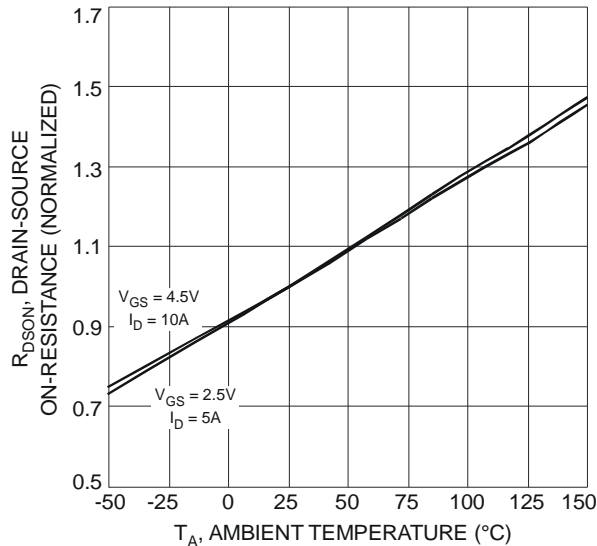


Fig. 5 On-Resistance Variation with Temperature

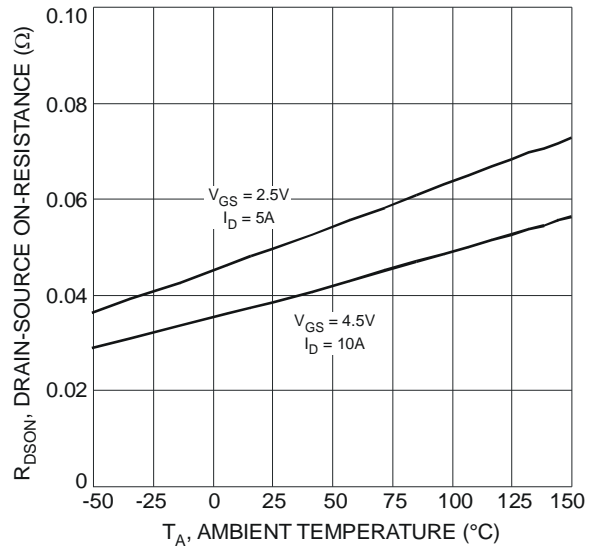


Fig. 6 On-Resistance Variation with Temperature

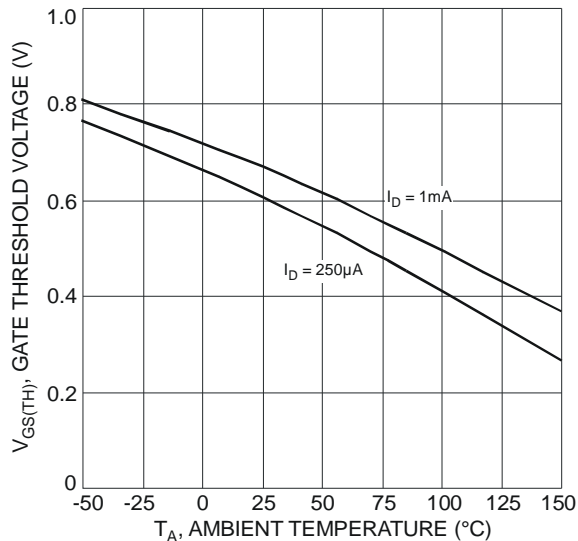


Fig. 7 Gate Threshold Variation vs. Ambient Temperature

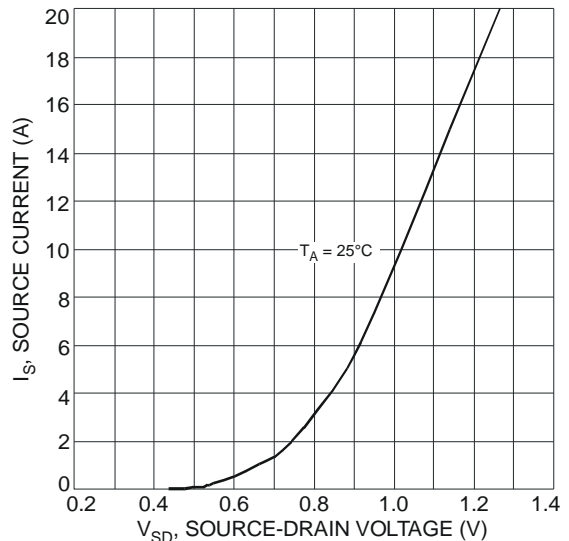


Fig. 8 Diode Forward Voltage vs. Current

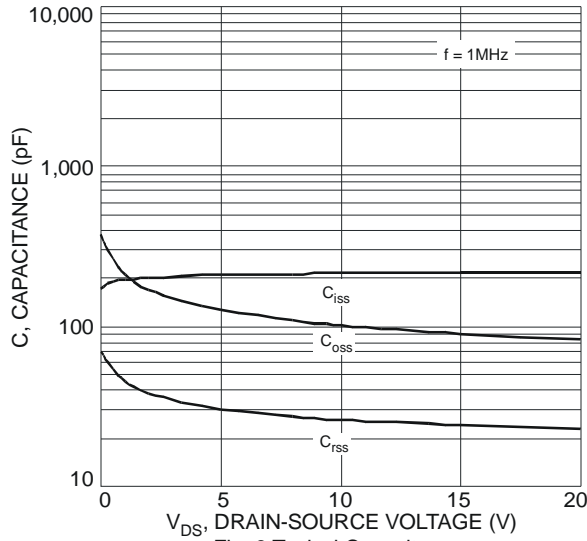


Fig. 9 Typical Capacitance

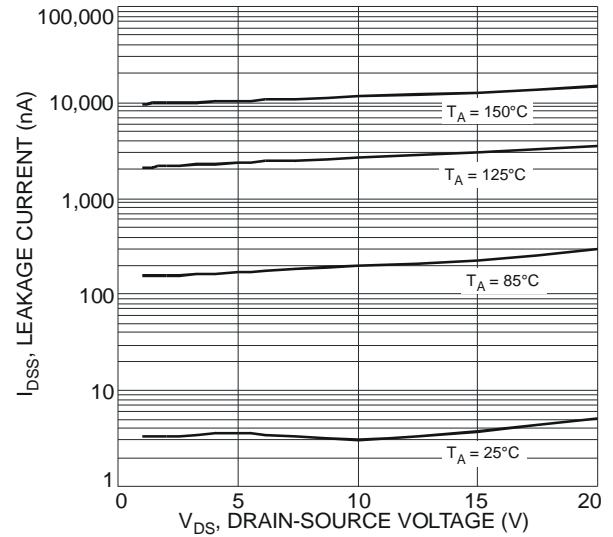


Fig. 10 Typical Leakage Current vs. Drain-Source Voltage

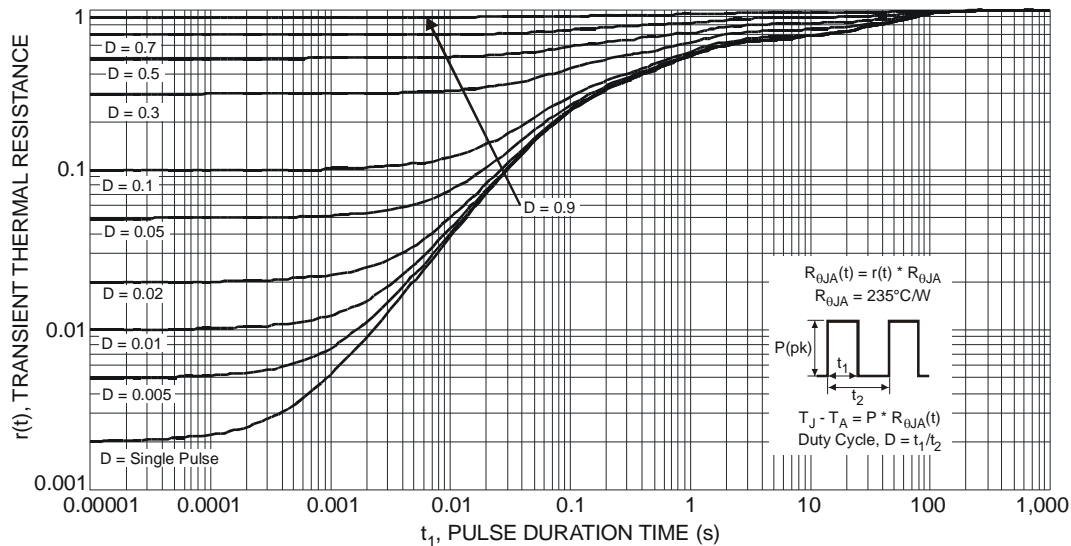


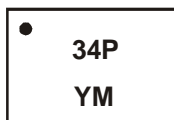
Fig. 11 Transient Thermal Response

## Ordering Information (Note 7)

Part Number	Case	Packaging
DMP2069UFY4-7	DFN2015H4-3	3000/Tape & Reel

Notes: 7. For packaging details, go to our website at <http://www.diodes.com/datasheets/ap02007.pdf>.

## Marking Information



34P = Marking Code  
YM = Date Code Marking  
Y = Year (ex: W = 2009)  
M = Month (ex: 9 = September)

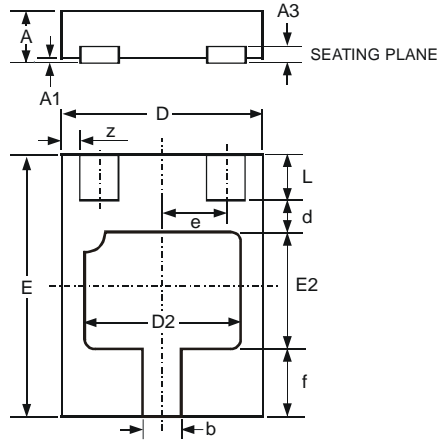
### Date Code Key

Date Code Key

Year	2009	2010	2011	2012	2013	2014	2015
Code	W	X	Y	Z	A	B	C

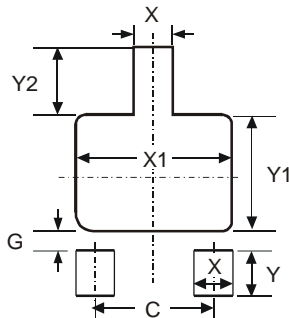
Month	Jan	Feb	Mar	Apr	May	Jun	Jul	Aug	Sep	Oct	Nov	Dec
Code	1	2	3	4	5	6	7	8	9	O	N	D

## Package Outline Dimensions



DFN2015H4-3			
Dim	Min	Max	Typ
A	—	0.40	—
A1	0	0.05	0.02
A3	—	—	0.13
b	0.20	0.30	0.25
d	—	—	0.30
D	1.45	1.575	1.50
D2	1.00	1.20	1.10
e	—	—	0.50
E	1.95	2.075	2.00
E2	0.70	0.90	0.80
f	—	—	0.60
L	0.25	0.35	0.30
z	—	—	0.125
All Dimensions in mm			

## Suggested Pad Layout



Dimensions	Value (in mm)
C	1.00
G	0.15
X	0.31
X1	1.30
Y	0.50
Y1	1.00
Y2	0.65

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