

## Product Summary

Device	$V_{(BR)DSS}$	$R_{DS(ON)}$	$I_D$ $T_A = 25^\circ\text{C}$
Q1	20V	$0.45\Omega @ V_{GS} = 4.5\text{V}$	1066mA
Q2		$0.75\Omega @ V_{GS} = -4.5\text{V}$	-845mA

## Features and Benefits

- Low On-Resistance
- Low Gate Threshold Voltage
- Low Input Capacitance
- Fast Switching Speed
- Low Input/Output Leakage
- Complementary Pair MOSFET
- Ultra-Small Surface Mount Package
- ESD Protected Up to 2.5kV
- **Totally Lead-Free & Fully RoHS Compliant (Notes 1 & 2)**
- **Halogen and Antimony Free. "Green" Device (Note 3)**
- **Qualified to AEC-Q101 standards for High Reliability**

## Description and Applications

This new generation MOSFET has been designed to minimize the on-state resistance ( $R_{DS(on)}$ ) and yet maintain superior switching performance, making it ideal for high efficiency power management applications.

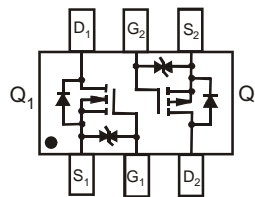
- Battery Operated Systems and Solid-State Relays
- Drivers: Relays, Solenoids, Lamps, Hammers, Displays, Memories, Transistors, etc.
- Power Supply Converter Circuits

## Mechanical Data

- Case: SOT-363
- Case Material: Molded Plastic, "Green" Molding Compound. UL Flammability Classification Rating 94V-0
- Moisture Sensitivity: Level 1 per J-STD-020
- Terminals: Matte Tin Finish annealed over Alloy 42 leadframe (Lead Free Plating). Solderable per MIL-STD-202, Method 208
- Terminal Connections: See Diagram
- Marking Information: See Page 1
- Ordering Information: See Page 1
- Weight: 0.006 grams (approximate)



TOP VIEW



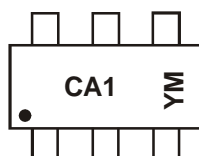
TOP VIEW  
Internal Schematic

## Ordering Information (Note 4)

Part Number	Qualification	Case	Packaging
DMG1016UDW-7	Commercial	SOT-363	3000/Tape & Reel
DMG1016UDWQ-7	Automotive	SOT-363	3000/Tape & Reel

- Notes:
1. No purposely added lead. Fully EU Directive 2002/95/EC (RoHS) & 2011/65/EU (RoHS 2) compliant.
  2. See <http://www.diodes.com> for more information about Diodes Incorporated's definitions of Halogen- and Antimony-free, "Green" and Lead-free.
  3. Halogen- and Antimony-free "Green" products are defined as those which contain <900ppm bromine, <900ppm chlorine (<1500ppm total Br + Cl) and <1000ppm antimony compounds.
  4. For packaging details, go to our website at <http://www.diodes.com>.

## Marking Information



CA1 = Product Type Marking Code  
 YM = Date Code Marking  
 Y = Year (ex: W = 2009)  
 M = Month (ex: 9 = September)

**Thermal Characteristics** @T<sub>A</sub> = 25°C unless otherwise specified

Characteristic	Symbol	Value	Units
Total Power Dissipation (Note 5)	P <sub>D</sub>	330	mW
Thermal Resistance, Junction to Ambient (Note 5)	R <sub>θJA</sub>	379	°C/W
Operating and Storage Temperature Range	T <sub>J</sub> , T <sub>STG</sub>	-55 to +150	°C

**Maximum Ratings N-CHANNEL – Q1** @T<sub>A</sub> = 25°C unless otherwise specified

Characteristic	Symbol	Value	Units
Drain-Source Voltage	V <sub>DSS</sub>	20	V
Gate-Source Voltage	V <sub>GSS</sub>	±6	V
Continuous Drain Current (Note 5)	I <sub>D</sub>	1066 690	mA

**Maximum Ratings P-CHANNEL – Q2** @T<sub>A</sub> = 25°C unless otherwise specified

Characteristic	Symbol	Value	Units
Drain-Source Voltage	V <sub>DSS</sub>	-20	V
Gate-Source Voltage	V <sub>GSS</sub>	±6	V
Continuous Drain Current (Note 5)	I <sub>D</sub>	-845 -548	mA

Notes: 5. Device mounted on FR-4 PCB with minimum recommended pad layout.

**Electrical Characteristics N-CHANNEL – Q1** @T<sub>A</sub> = 25°C unless otherwise specified

Characteristic	Symbol	Min	Typ	Max	Unit	Test Condition
<b>OFF CHARACTERISTICS (Note 6)</b>						
Drain-Source Breakdown Voltage	BV <sub>DSS</sub>	20	-	-	V	V <sub>GS</sub> = 0V, I <sub>D</sub> = 250μA
Zero Gate Voltage Drain Current @T <sub>c</sub> = 25°C	I <sub>DSS</sub>	-	-	100	nA	V <sub>DS</sub> = 20V, V <sub>GS</sub> = 0V
Gate-Source Leakage	I <sub>GSS</sub>	-	-	±1.0	μA	V <sub>GS</sub> = ±4.5V, V <sub>DS</sub> = 0V
<b>ON CHARACTERISTICS (Note 6)</b>						
Gate Threshold Voltage	V <sub>GS(th)</sub>	0.5	-	1.0	V	V <sub>DS</sub> = V <sub>GS</sub> , I <sub>D</sub> = 250μA
Static Drain-Source On-Resistance	R <sub>DS(on)</sub>	-	0.3	0.45	Ω	V <sub>GS</sub> = 4.5V, I <sub>D</sub> = 600mA
			0.4	0.6		V <sub>GS</sub> = 2.5V, I <sub>D</sub> = 500mA
			0.5	0.75		V <sub>GS</sub> = 1.8V, I <sub>D</sub> = 350mA
Forward Transfer Admittance	Y <sub>fs</sub>	-	1.4	-	S	V <sub>DS</sub> = 10V, I <sub>D</sub> = 400mA
Diode Forward Voltage (Note 6)	V <sub>SD</sub>	-	0.7	1.2	V	V <sub>GS</sub> = 0V, I <sub>S</sub> = 150mA
<b>DYNAMIC CHARACTERISTICS (Note 7)</b>						
Input Capacitance	C <sub>iss</sub>	-	60.67	-	pF	V <sub>DS</sub> = 10V, V <sub>GS</sub> = 0V, f = 1.0MHz
Output Capacitance	C <sub>oss</sub>	-	9.68	-	pF	
Reverse Transfer Capacitance	C <sub>rss</sub>	-	5.37	-	pF	
Total Gate Charge (4.5V)	Q <sub>g</sub>	-	736.6	-	nC	V <sub>GS</sub> = 4.5V, V <sub>DS</sub> = 10V, I <sub>D</sub> = 250mA
Gate-Source Charge	Q <sub>gs</sub>	-	93.6	-	nC	
Gate-Drain Charge	Q <sub>gd</sub>	-	116.6	-	nC	
Turn-On Delay Time	t <sub>D(on)</sub>	-	5.1	-	ns	V <sub>DD</sub> = 10V, V <sub>GS</sub> = 4.5V, R <sub>L</sub> = 47•, R <sub>G</sub> = 10•,
Turn-On Rise Time	t <sub>r</sub>	-	7.4	-	ns	
Turn-Off Delay Time	t <sub>D(off)</sub>	-	26.7	-	ns	
Turn-Off Fall Time	t <sub>f</sub>	-	12.3	-	ns	

Notes: 6. Short duration pulse test used to minimize self-heating effect.  
7. Guaranteed by design. Not subject to production testing.

# N-CHANNEL – Q1

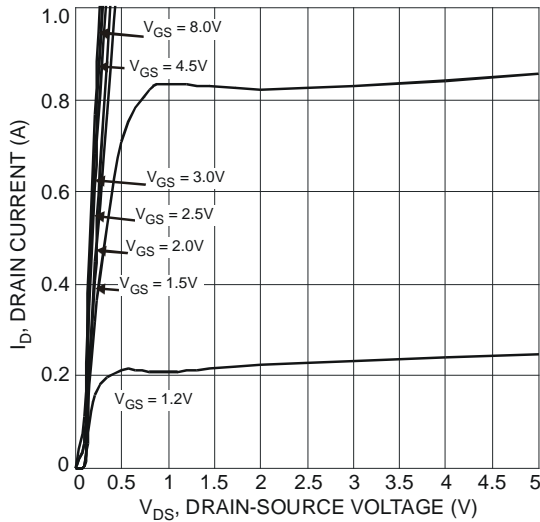


Fig. 1 Typical Output Characteristic

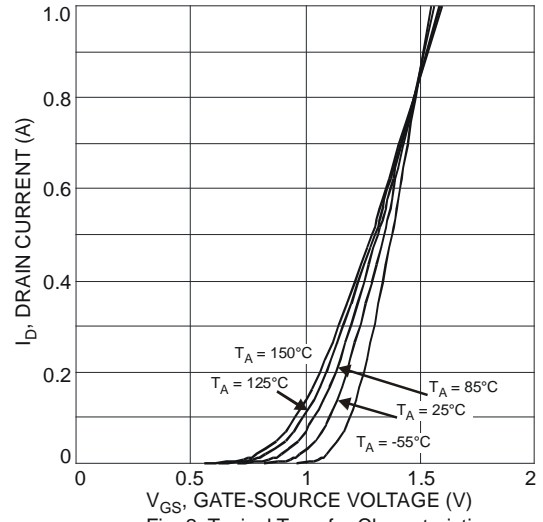


Fig. 2 Typical Transfer Characteristic

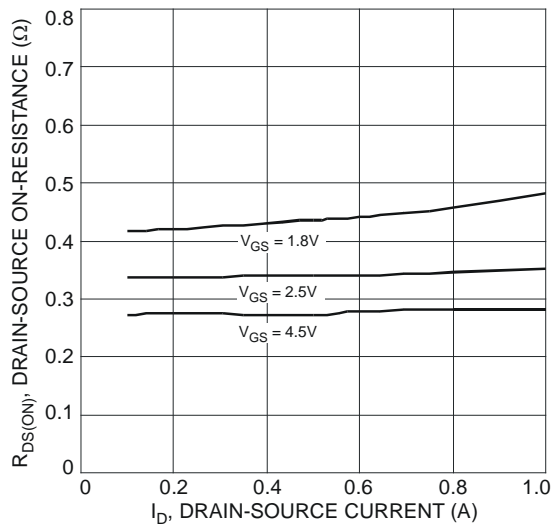


Fig. 3 Typical On-Resistance vs. Drain Current and Gate Voltage

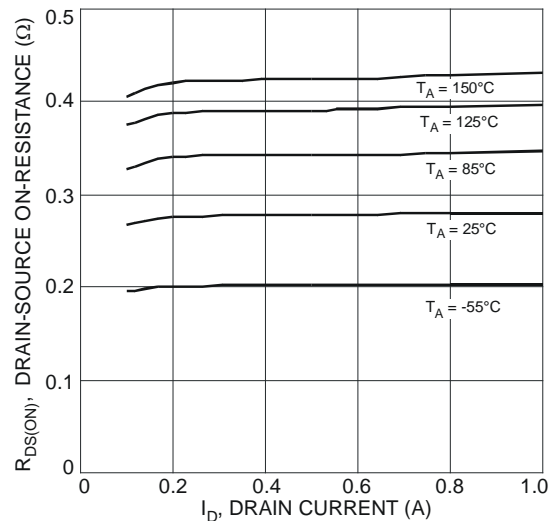


Fig. 4 Typical On-Resistance vs. Drain Current and Temperature

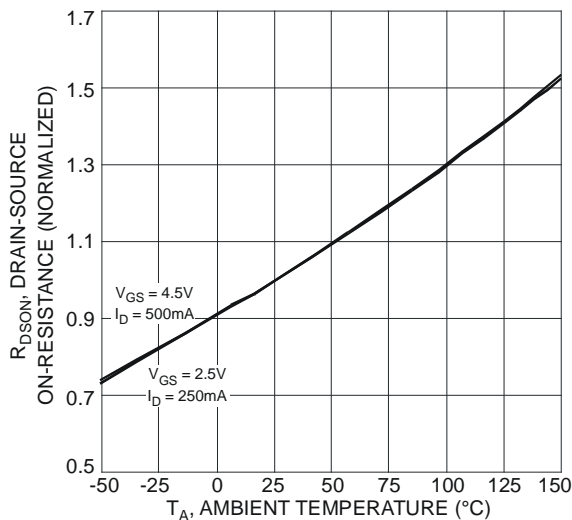


Fig. 5 On-Resistance Variation with Temperature

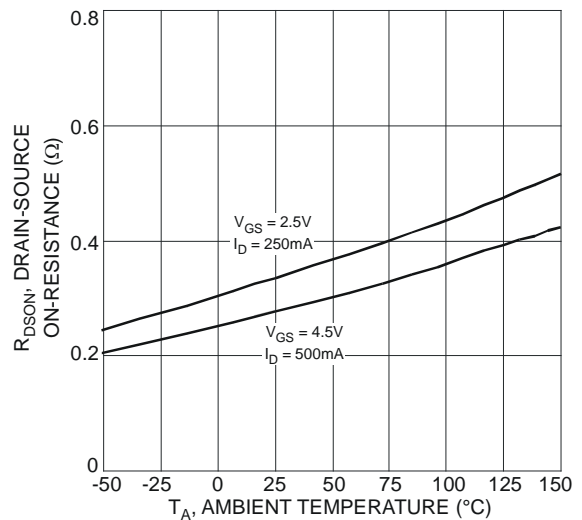


Fig. 6 On-Resistance Variation with Temperature

**N-CHANNEL – Q1 (continued)**

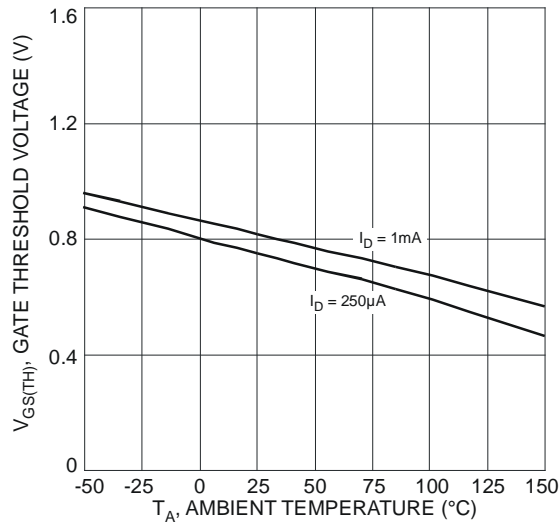


Fig. 7 Gate Threshold Variation vs. Ambient Temperature

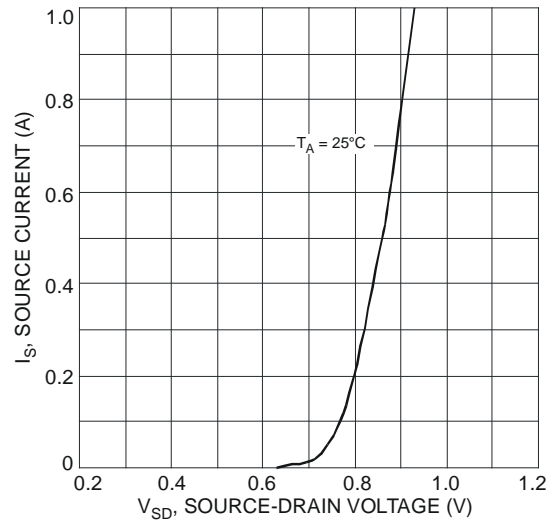


Fig. 8 Diode Forward Voltage vs. Current

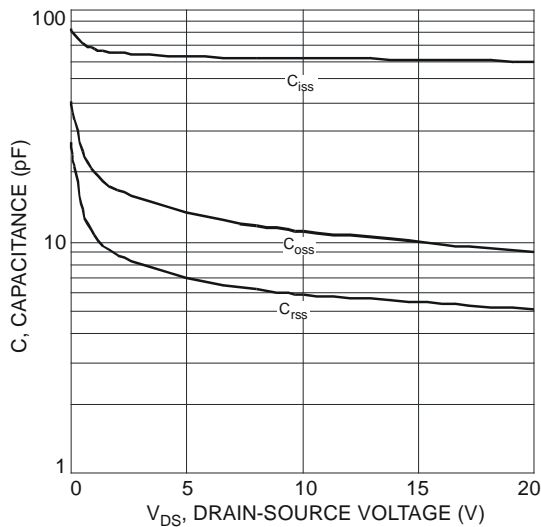


Fig. 9 Typical Total Capacitance

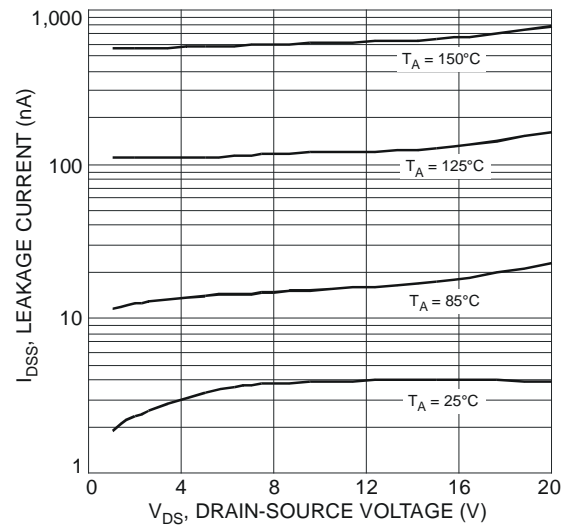


Fig. 10 Typical Leakage Current vs. Drain-Source Voltage

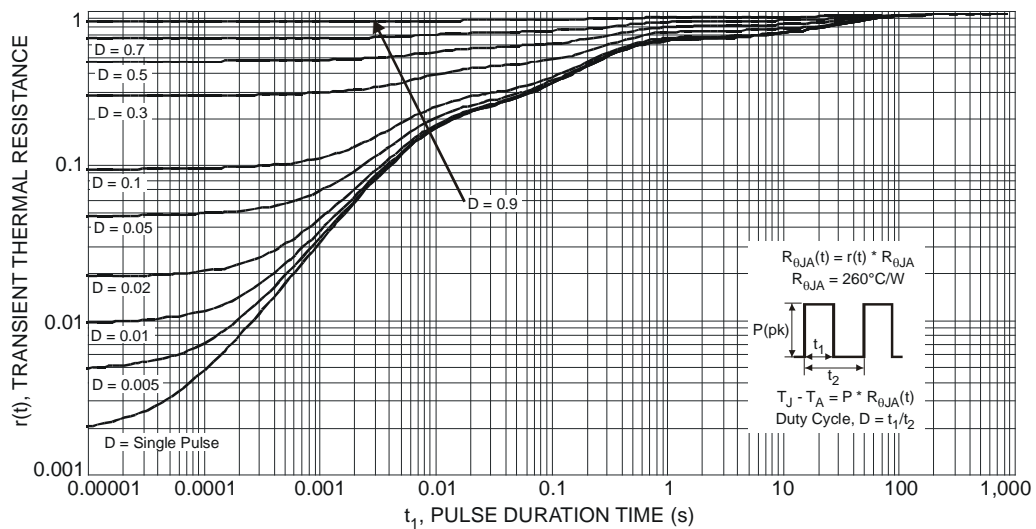


Fig. 11 Transient Thermal Response

**Electrical Characteristics P-CHANNEL – Q2** @T<sub>A</sub> = 25°C unless otherwise specified

Characteristic	Symbol	Min	Typ	Max	Unit	Test Condition
<b>OFF CHARACTERISTICS (Note 6)</b>						
Drain-Source Breakdown Voltage	BV <sub>DSS</sub>	-20	-	-	V	V <sub>GS</sub> = 0V, I <sub>D</sub> = -250μA
Zero Gate Voltage Drain Current @T <sub>c</sub> = 25°C	I <sub>DSS</sub>	-	-	-100	nA	V <sub>DS</sub> = -20V, V <sub>GS</sub> = 0V
Gate-Source Leakage	I <sub>GSS</sub>	-	-	±2.0	μA	V <sub>GS</sub> = ±4.5V, V <sub>DS</sub> = 0V
<b>ON CHARACTERISTICS (Note 6)</b>						
Gate Threshold Voltage	V <sub>GS(th)</sub>	-0.5	-	-1.0	V	V <sub>DS</sub> = V <sub>GS</sub> , I <sub>D</sub> = -250μA
Static Drain-Source On-Resistance	R <sub>DS(on)</sub>	-	0.5	0.75	Ω	V <sub>GS</sub> = -4.5V, I <sub>D</sub> = -430mA
			0.7	1.05		V <sub>GS</sub> = -2.5V, I <sub>D</sub> = -300mA
			1.0	1.5		V <sub>GS</sub> = -1.8V, I <sub>D</sub> = -150mA
Forward Transfer Admittance	Y <sub>fs</sub>	-	0.9	-	S	V <sub>DS</sub> = -10V, I <sub>D</sub> = -250mA
Diode Forward Voltage (Note 6)	V <sub>SD</sub>	-	-0.8	-1.2	V	V <sub>GS</sub> = 0V, I <sub>S</sub> = -150mA
<b>DYNAMIC CHARACTERISTICS (Note 7)</b>						
Input Capacitance	C <sub>iss</sub>	-	59.76	-	pF	V <sub>DS</sub> = -16V, V <sub>GS</sub> = 0V, f = 1.0MHz
Output Capacitance	C <sub>oss</sub>	-	12.07	-	pF	
Reverse Transfer Capacitance	C <sub>rss</sub>	-	6.36	-	pF	
Total Gate Charge (4.5V)	Q <sub>g</sub>	-	622.4	-	pC	V <sub>GS</sub> = -4.5V, V <sub>DS</sub> = -10V, I <sub>D</sub> = -250mA
Gate-Source Charge	Q <sub>gs</sub>	-	100.3	-	pC	
Gate-Drain Charge	Q <sub>gd</sub>	-	132.2	-	pC	
Turn-On Delay Time	t <sub>D(on)</sub>	-	5.1	-	ns	V <sub>DS</sub> = -10V, V <sub>GS</sub> = -4.5V, R <sub>G</sub> = 10•, R <sub>L</sub> = 47•
Turn-On Rise Time	t <sub>r</sub>	-	8.1	-	ns	
Turn-Off Delay Time	t <sub>D(off)</sub>	-	28.4	-	ns	
Turn-Off Fall Time	t <sub>f</sub>	-	20.72	-	ns	

Notes: 6. Short duration pulse test used to minimize self-heating effect.  
7. Guaranteed by design. Not subject to production testing

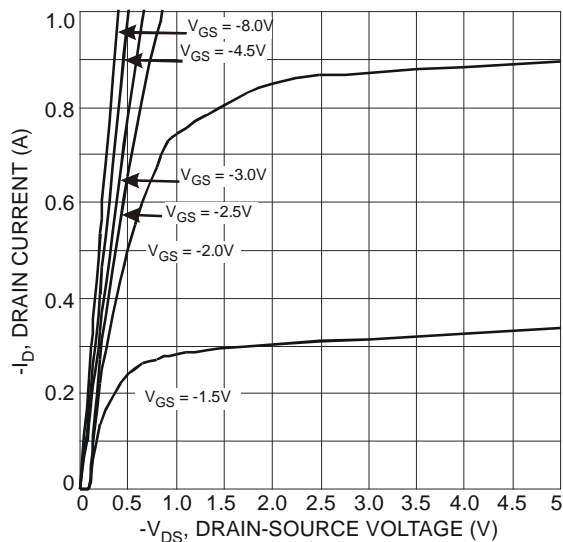
**P-CHANNEL – Q2**


Fig. 12 Typical Output Characteristic

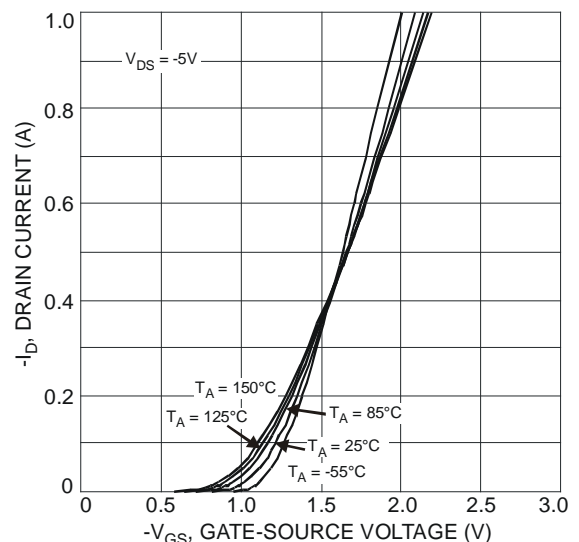


Fig. 13 Typical Transfer Characteristic

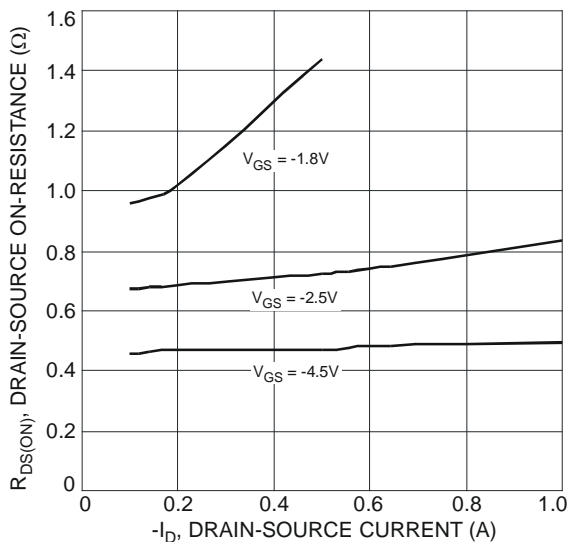


Fig. 14 Typical On-Resistance vs. Drain Current and Gate Voltage

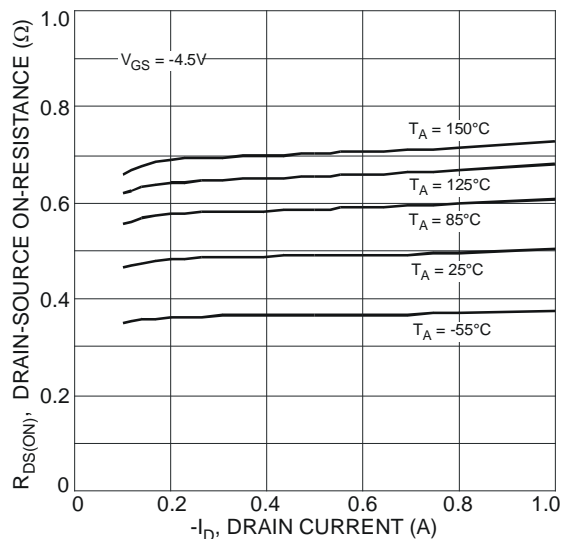


Fig. 15 Typical On-Resistance vs. Drain Current and Temperature

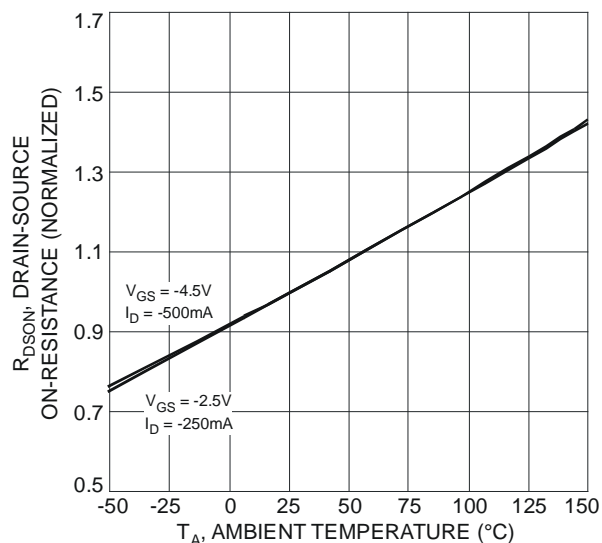


Fig. 16 On-Resistance Variation with Temperature

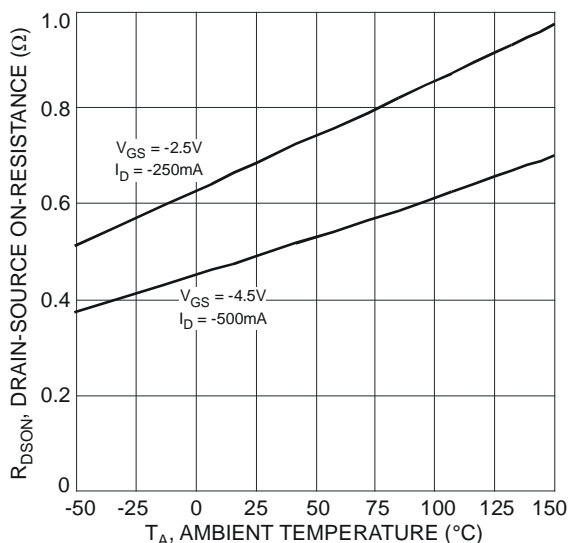


Fig. 17 On-Resistance Variation with Temperature

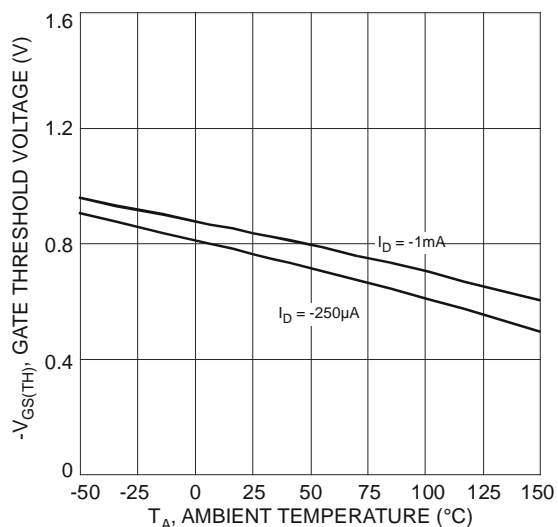


Fig. 18 Gate Threshold Variation vs. Ambient Temperature

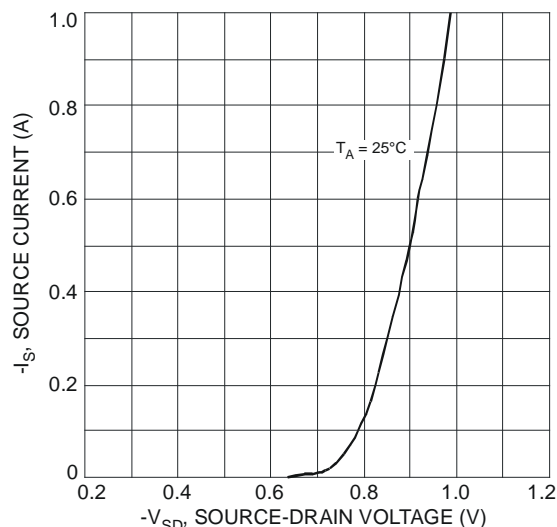


Fig. 19 Diode Forward Voltage vs. Current

**P-CHANNEL – Q2 (continued)**

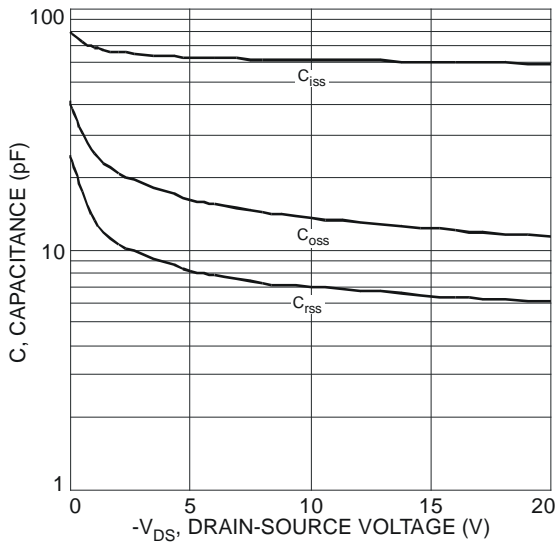


Fig. 20 Typical Total Capacitance

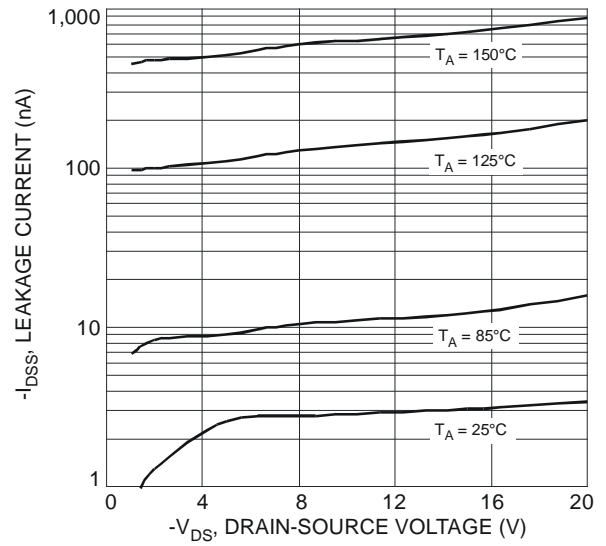


Fig. 21 Typical Leakage Current vs. Drain-Source Voltage

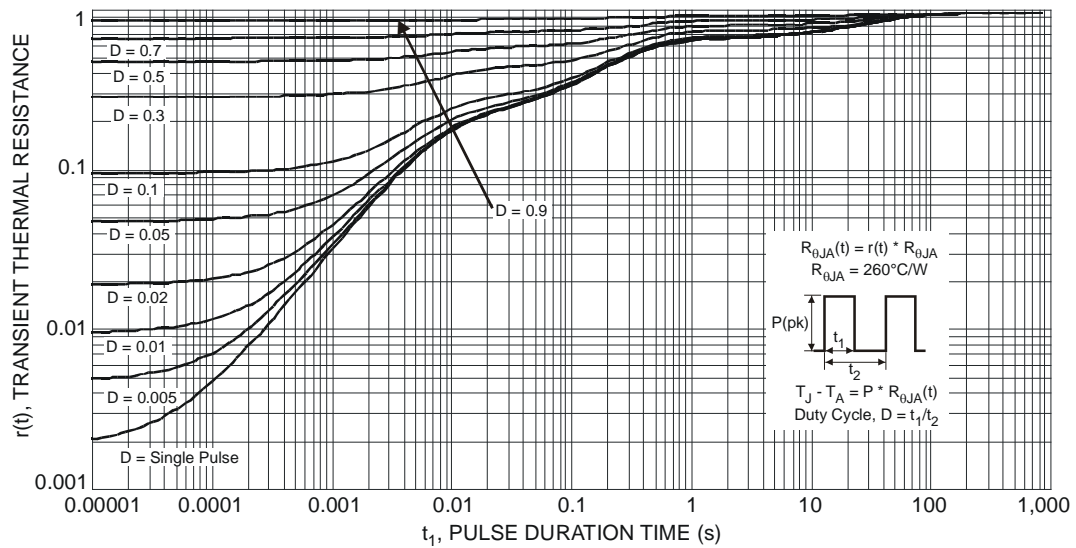
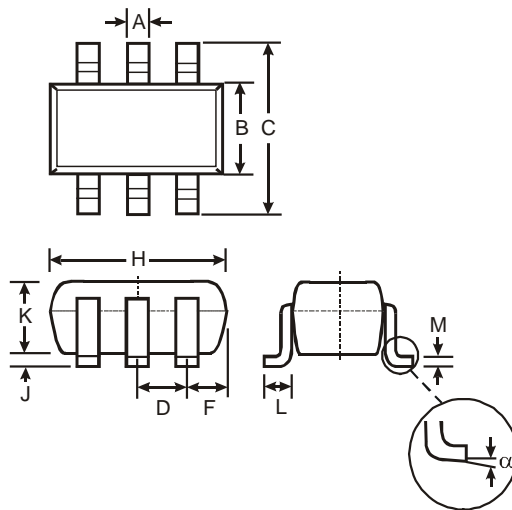


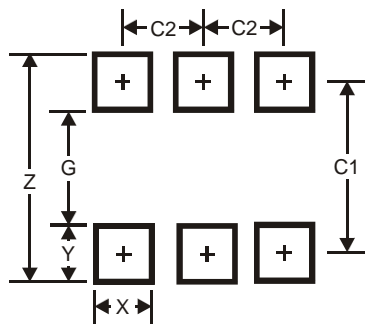
Fig. 22 Transient Thermal Response

## Package Outline Dimensions



SOT-363		
Dim	Min	Max
A	0.10	0.30
B	1.15	1.35
C	2.00	2.20
D	0.65 Typ	
F	0.40	0.45
H	1.80	2.20
J	0	0.10
K	0.90	1.00
L	0.25	0.40
M	0.10	0.22
$\alpha$	0°	8°
All Dimensions in mm		

## Suggested Pad Layout



Dimensions	Value (in mm)
Z	2.5
G	1.3
X	0.42
Y	0.6
C1	1.9
C2	0.65



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2. support or sustain life and whose failure to perform when properly used in accordance with instructions for use provided in the labeling can be reasonably expected to result in significant injury to the user.

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