

DUAL P-CHANNEL ENHANCEMENT MODE MOSFET

Features

- Low On-Resistance
 - $70m\Omega @V_{GS} = -4.5V$
 - 85mΩ @V_{GS} = -2.5V
 - 86m Ω (typ) @V_{GS} = -1.8V
 - Low Gate Threshold Voltage, -0.9V Max
- Fast Switching Speed
- Low Input/Output Leakage
- Low Profile, 0.5mm Max Height
- Totally Lead-Free & Fully RoHS Compliant (Notes 1 & 2)
- Halogen and Antimony Free. "Green" Device (Note 3)
- Qualified to AEC-Q101 Standards for High Reliability

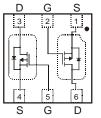
Mechanical Data

- Case: U-DFN2020-6 Type B
- Case Material: Molded Plastic, "Green" Molding Compound. UL Flammability Classification Rating 94V-0
- Moisture Sensitivity: Level 1 per J-STD-020
- Terminal Connections: See Diagram
- Terminals: Finish NiPdAu annealed over Copper leadframe.
 Solderable per MIL-STD-202, Method 208 ⁽⁴⁾
- Weight: 0.0065 grams (approximate)

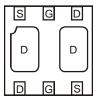
U-DFN2020-6 Type B



Bottom View



Top View Internal Schematic



Bottom View Pin Configuration

Ordering Information (Note 4)

Part Number	Case	Packaging
DMP2160UFDB-7	U-DFN2020-6 Type B	3000/Tape & Reel

Notes: 1. No purposely added lead. Fully EU Directive 2002/95/EC (RoHS) & 2011/65/EU (RoHS 2) compliant.

2. See http://www.diodes.com for more information about Diodes Incorporated's definitions of Halogen- and Antimony-free, "Green" and Lead-free.

3. Halogen- and Antimony-free "Green" products are defined as those which contain <900ppm bromine, <900ppm chlorine (<1500ppm total Br + Cl) and <1000ppm antimony compounds.

4. For packaging details, go to our website at http://www.diodes.com.

Marking Information



P2 = Marking Code YM = Date Marking Y = Year (ex: V = 2008) M = Month (ex: 9 = September) Dot denotes Pin 1

Date Code Key												
Year	2008	2009	2010	2011	201	2 20)13	2014	2015	2016	2017	2018
Code	V	W	Х	Y	Z		A	В	С	D	Е	F
Month	Jan	Feb	Mar	Apr	Мау	Jun	Jul	Aug	Sep	Oct	Nov	Dec
Code	1	2	3	4	5	6	7	8	9	0	N	D



Maximum Ratings (@T_A = +25°C, unless otherwise specified.)

Characteristic	Symbol	Value	Units
Drain-Source Voltage	V _{DSS}	-20	V
Gate-Source Voltage	V _{GSS}	±12	V
Drain Current (Note 5)	ID	-3.8	A
Pulsed Drain Current (Note 6)	I _{DM}	-13	А

Thermal Characteristics (@T_A = +25°C, unless otherwise specified.)

Characteristic	Symbol	Value	Unit
Power Dissipation (Note 5)	PD	1.4	W
Thermal Resistance, Junction to Ambient	$R_{ ext{ heta}JA}$	89	°C/W
Operating and Storage Temperature Range	T _J , T _{STG}	-55 to +150	°C

Electrical Characteristics (@T_A = +25°C, unless otherwise specified.)

Characteristic	Symbol	Min	Тур	Max	Unit	Test Condition
OFF CHARACTERISTICS (Note 7	Oymbol		тур	Max	Unit	
Drain-Source Breakdown Voltage	BV _{DSS}	-20			V	V _{GS} = 0V, I _D = -250μA
Zero Gate Voltage Drain Current	IDSS	_	_	-1	μA	$V_{DS} = -20V, V_{GS} = 0V$
		_		±100		$V_{GS} = \pm 8V, V_{DS} = 0V$
Gate-Source Leakage	I _{GSS}	_		±800	nA	$V_{GS} = \pm 12V$, $V_{DS} = 0V$
ON CHARACTERISTICS (Note 7)	-					·
Gate Threshold Voltage	V _{GS(th)}	-0.45		-0.9	V	$V_{DS} = V_{GS}, I_D = -250 \mu A$
		_	54	70		$V_{GS} = -4.5V, I_D = -2.8A$
Static Drain-Source On-Resistance	R _{DS (ON)}		68	85		V _{GS} = -2.5V, I _D = -2.0A
	- (-)	—	86	—		V _{GS} = -1.8V, I _D = -1.0A
Forward Transfer Admittance	Y _{fs}		8		S	V _{DS} = -5V, I _D = -2.8A
Diode Forward Voltage (Note 7)	V _{SD}		0.7	-1.2	V	V _{GS} = 0V, I _S = -1.6A
DYNAMIC CHARACTERISTICS				-	_	
Input Capacitance	Ciss		536		рF	
Output Capacitance	Coss	—	68		pF	V _{DS} = -10V, V _{GS} = 0V f = 1.0MHz
Reverse Transfer Capacitance	C _{rss}	—	59		pF	1 = 1.000112
Gate Resistance	Rg	-	8.72	-	Ω	$V_{DS} = 0V, V_{GS} = 0V, f = 1MHz$
Total Gate Charge	Qg	-	6.5	-	nC	
Gate-Source Charge	Q _{gs}	-	0.8	-	nC	V _{GS} = -4.5V, V _{DD} = -10V, I _D = -1.5A
Gate-Drain Charge	Q _{qd}	-	1.4	-	nC	$I_D = -1.5A$
Turn-On Delay Time	t _{D(on)}	-	11.51	-	ns	
Turn-On Rise Time	tr	-	12.09	-	ns	V _{GEN} = -4.5V, V _{DD} = -10V,
Turn-Off Delay Time	t _{D(off)}	-	55.34	-	ns	$R_L = 10\Omega, R_G = 6\Omega$
Turn-Off Fall Time	t _f	-	27.54	-	ns	

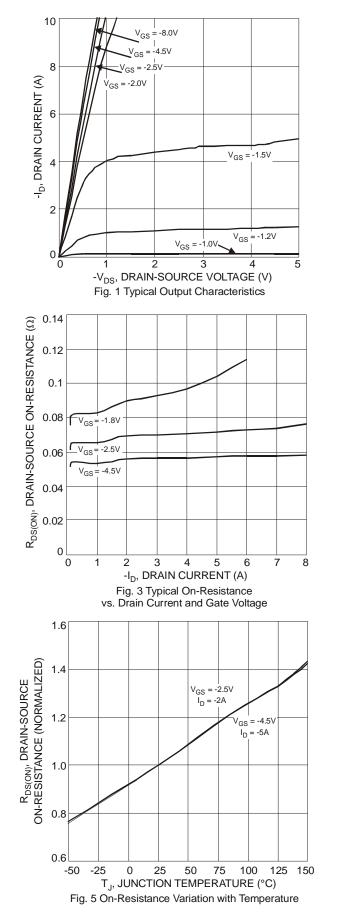
Notes:

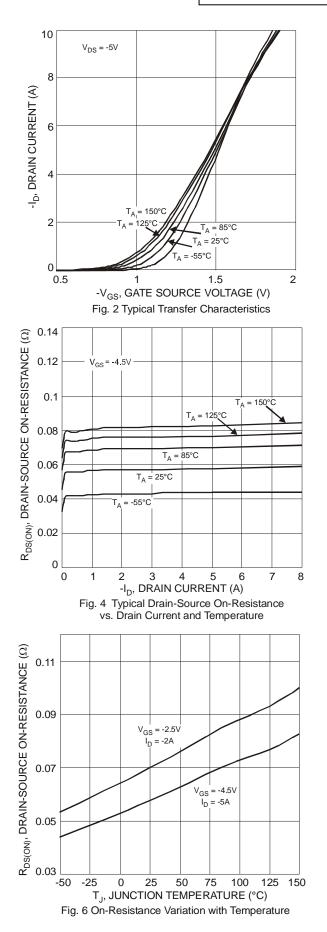
Device mounted on FR-4 PCB, on minimum recommended, 2oz Copper pad layout. 5.

Repetitive rating, pulse width limited by junction temperature. Short duration pulse test used to minimize self-heating effect. 6. 7.

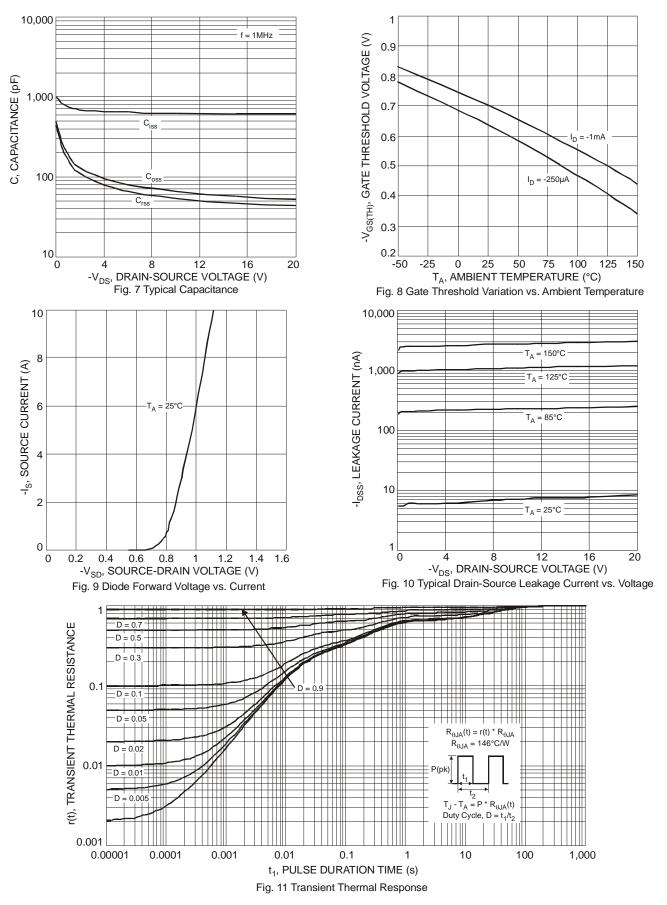


DMP2160UFDB





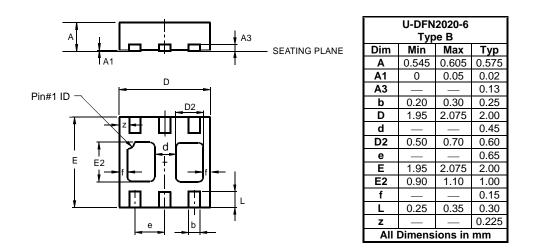






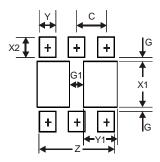
Package Outline Dimensions

Please see AP02002 at http://www.diodes.com/datasheets/ap02002.pdf for latest version.



Suggested Pad Layout

Please see AP02001 at http://www.diodes.com/datasheets/ap02001.pdf for the latest version.



Dimensions	Value (in mm)
Z	1.67
G	0.20
G1	0.40
X1	1.0
X2	0.45
Y	0.37
Y1	0.70
С	0.65



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