



P-CHANNEL ENHANCEMENT MODE MOSFET

Product Summary

V _{(BR)DSS}	R _{DS(on)}	Ι _D T _A = +25°C
-30V	122mΩ @ V _{GS} = -10V	-2.7A
-30 V	190mΩ @ V _{GS} = -4.5V	-2.0A

Description

This new generation MOSFET has been designed to minimize the onstate resistance ($R_{DS(ON)}$) and yet maintain superior switching performance, making it ideal for high efficiency power management applications.

SOT23

Top View

Applications

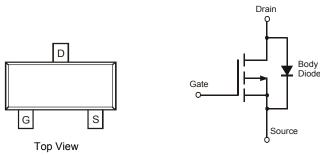
- DC-DC Converters
- Power Management Functions

Features

- Low On-Resistance
- Low Gate Threshold Voltage
- Low Input Capacitance
- Fast Switching Speed
- Low Input/Output Leakage
- Totally Lead-Free & Fully RoHS Compliant (Notes 1 & 2)
- Halogen and Antimony Free. "Green" Device (Note 3)
- Qualified to AEC-Q101 Standards for High Reliability
- PPAP Capable (Note 4)

Mechanical Data

- Case: SOT23
- Case Material: Molded Plastic, "Green" Molding Compound. UL Flammability Classification Rating 94V-0
- Moisture Sensitivity: Level 1 per J-STD-020
- Terminals: Finish Matte Tin annealed over Copper leadframe. Solderable per MIL-STD-202, Method 208 (3)
- Terminal Connections: See Diagram
- Weight: 0.008 grams (approximate)



Equivalent Circuit

Ordering Information (Note 4 & 5)

Part Number	Compliance	Case	Packaging
DMP3160L-7	Standard	SOT23	3000/Tape & Reel
DMP3160LQ-7	Automotive	SOT23	3000/Tape & Reel

Notes: 1. No purposely added lead. Fully EU Directive 2002/95/EC (RoHS) & 2011/65/EU (RoHS 2) compliant.

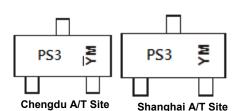
2. See http://www.diodes.com/quality/lead_free.html for more information about Diodes Incorporated's definitions of Halogen- and Antimony-free, "Green"

and Lead-free. 3. Halogen- and Antimony-free "Green" products are defined as those which contain <900ppm bromine, <900ppm chlorine (<1500ppm total Br + Cl) and <1000ppm antimony compounds.

4. For packaging details, go to our website at http://www.diodes.com/products/packages.html.

5. Automotive products are AEC-Q101 qualified and are PPAP capable. Automotive, AEC-Q101 and standard products are electrically and thermally the same, except where specified. For more information, please refer to http://www.diodes.com/quality/product_grade_definitions/.

Marking Information



PS3 = Product Type Marking Code YM = Date Code Marking for SAT (Shanghai Assembly/ Test site) $\overline{Y}M$ = Date Code Marking for CAT (Chengdu Assembly/ Test site) Y or \overline{Y} = Year (ex: A = 2013)

M = Month (ex: 9 = September)

Date Code Key 2015 Year 2007 2008 2009 2010 2011 2012 2013 2014 Code W х 7 B С U Α Month Jan Feb Mar Apr May Jun Jul Aug Sep Oct Nov Dec Code 2 3 4 5 6 8 9 0 Ν D

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Maximum Ratings (@T_A = +25°C, unless otherwise specified.)

Characteristic			Symbol	Value	Units
Drain-Source Voltage			V _{DSS}	-30	V
Gate-Source Voltage			V _{GSS}	±20	V
Drain Current (Note 6) V _{GS} = -10V	Steady State	T _A = +25°C T _A = +70°C	lD	-2.7 -2	A
Pulsed Drain Current (Note 7)			I _{DM}	-8	A

Thermal Characteristics

Characteristic	Symbol	Value	Units
Total Power Dissipation (Note 6)	PD	1.08	W
Thermal Resistance, Junction to Ambient @ T_A = +25°C (Note 6)	R _{θJA}	115	°C/W
Operating and Storage Temperature Range	T _{J,} T _{STG}	-55 to +150	°C

Electrical Characteristics (@T_A = +25°C, unless otherwise specified.)

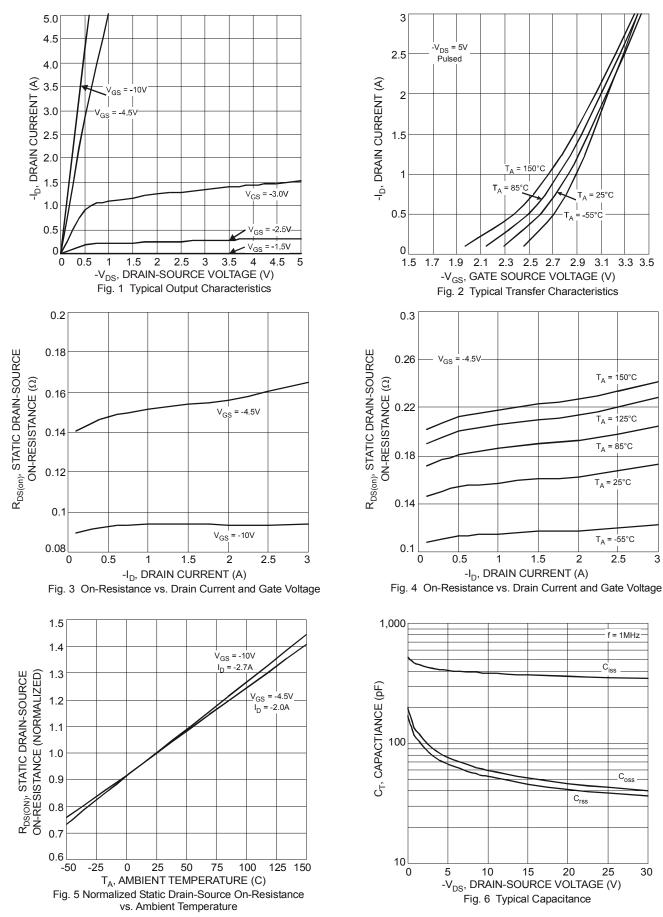
Characteristic	Symbol	Min	Тур	Max	Unit	Test Condition
OFF CHARACTERISTICS (Note 8)				•	•	
Drain-Source Breakdown Voltage	BV _{DSS}	-30	—		V	V_{GS} = 0V, I_{D} = -250 μ A
Zero Gate Voltage Drain Current	I _{DSS}	_	_	-800	nA	V _{DS} = -30V, V _{GS} = 0V
Gate-Source Leakage	I _{GSS}	_	_	±80 ±800	nA	$V_{GS} = \pm 12V, V_{DS} = 0V$ $V_{GS} = \pm 15V, V_{DS} = 0V$
ON CHARACTERISTICS (Note 8)						·
Gate Threshold Voltage	V _{GS(th)}	-1.3	-1.8	-2.1	V	V_{DS} = V_{GS} , I_D = -250 μ A
Static Drain-Source On-Resistance	P		97	122	mΩ	V_{GS} = -10V, I_{D} = -2.7A
	R _{DS(ON)}		165	190	11152	V_{GS} = -4.5V, I_{D} = -2.0A
Forward Transfer Admittance	Y _{fs}	_	5.9	_	S	V_{DS} = -5V, I_{D} = -2.7A
Diode Forward Voltage (Note 8)	V _{SD}	_	—	-1.26	V	V_{GS} = 0V, I_{S} = -2.7A
DYNAMIC CHARACTERISTICS(Note 9)						
Input Capacitance	C _{iss}	_	384.4	—	pF	
Output Capacitance	C _{oss}	_	59.4	_	pF	$V_{DS} = -10V, V_{GS} = 0V$ f = 1.0MHz
Reverse Transfer Capacitance	C _{rss}	_	52.8	_	pF	
Gate Resistance	R _G	_	17.1	_	Ω	$V_{GS} = 0V, V_{DS} = 0V,$ f = 1.0MHz
Total Gate Charge(V _{GS} = -4.5V)	Qg	_	4.0		nC	
Total Gate Charge(V _{GS} = -10V)	Qg	_	8.2	_	nC	V _{GS} = -10V/-4.5V,
Gate-Source Charge	Q _{gs}	_	0.9		nC	V _{DS} = -15V, I _D = -3A
Gate-Drain Charge	Q _{gd}	_	1.2		nC	
Turn-On Delay Time	t _{D(on)}		4.8	_	ns	
Turn-On Rise Time	tr	_	7.3		ns	V _{DS} = -15V, V _{GS} = -10V,
Turn-Off Delay Time	t _{D(off)}	_	22.5	_	ns	$R_{G} = 6\Omega, I_{D} = -1A$
Turn-Off Fall Time	t _f		13.4		ns	

Notes: 6. Device mounted on FR-4 PCB. t \leq 10 sec.

7. Pulse width $\leq 10\mu$ S, Duty Cycle $\leq 1\%$.

8. Short duration pulse test used to minimize self-heating effect.
9. Guaranteed by design. Not subject to product testing.





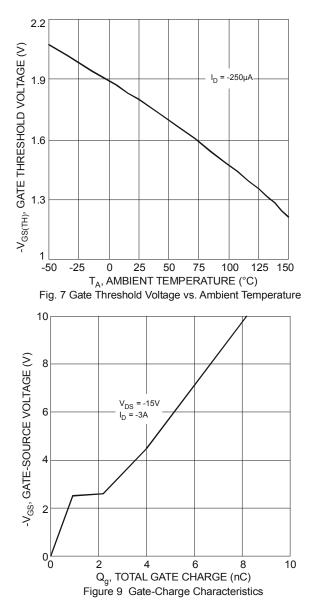
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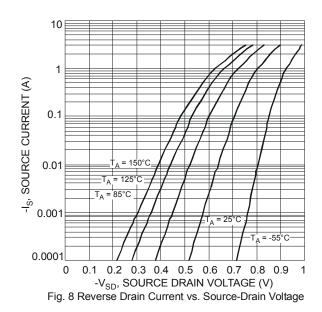
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Coss

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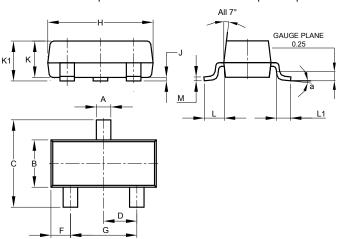






Package Outline Dimensions

Please see AP02002 at http://www.diodes.com/datasheets/ap02002.pdf for latest version.

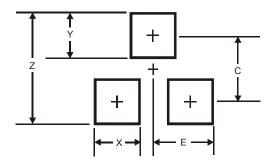


	SO.	Т23	
Dim	MMin	мМах	тЈур
Α	0.9.77	0.6.51	0.4040
в	1.2020	1.4040	1.3090
С	<u>2.</u> දිශි0	2.3550	2.4040
D	0.899	1.093	0.9.955
F	0.4545	0.660	0.9355
G	1.788	2.005	1.8383
н	2. දි @0	3.000	2. 8 090
J	0.0.013	0.000	0.005
κ	0.8990	1.000	0.0.275
K1	0.9093	1.100	1.0235
L	0.4545	0.6.61	0.\$55
L1	0.025	0.855	0.4040
М	0.085	0.9560	0.9.100
α	8°8°		
All Dimensions in mm			



Suggested Pad Layout

Please see AP02001 at http://www.diodes.com/datasheets/ap02001.pdf for the latest version.



Dimensions	Value (in mm)
Z	2.9
Х	0.8
Y	0.9
С	2.0
E	1.35

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