

NPN SMALL SIGNAL SURFACE MOUNT TRANSISTOR

Features

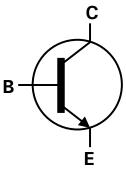
- Epitaxial Planar Die Construction
- Complementary PNP Type Available (MMBT2907A)
- Ideal for Low Power Amplification and Switching
- Totally Lead-Free & Fully RoHS compliant (Notes 1 & 2)
- Halogen and Antimony Free. "Green" Device (Note 3)
- Qualified to AEC-Q101 Standards for High Reliability
- PPAP capable (Note 4)

Mechanical Data

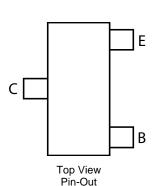
- Case: SOT23
- Case Material: Molded Plastic, "Green" Molding Compound, UL Flammability Classification Rating 94V-0
- Moisture Sensitivity: Level 1 per J-STD-020
- Terminals: Finish Matte Tin Leads; Solderable per MIL-STD-202, Method 208 63
- Weight: 0.008 grams (approximate)







Device Symbol



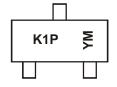
Ordering Information (Note 4 & 5)

Product	Compliance	Marking	Reel size (inches)	Tape width (mm)	Quantity per reel
MMBT2222A-7-F	AEC-Q101	K1P / C1P	7	8	3,000
MMBT2222A-13-F	AEC-Q101	K1P / C1P	13	8	10,000
MMBT2222AQ-7-F	Automotive	K1P	7	8	3,000

Notes:

- 1. No purposely added lead. Fully EU Directive 2002/95/EC (RoHS) & 2011/65/EU (RoHS 2) compliant.
- 2. See http://www.diodes.com for more information about Diodes Incorporated's definitions of Halogen and Antimony free, "Green" and Lead-Free.
- 3. Halogen and Antimony free "Green" products are defined as those which contain <900ppm bromine, <900ppm chlorine (<1500ppm total Br + Cl) and <1000ppm antimony compounds.
- 4. Automotive products are AEC-Q101 qualified and are PPAP capable. Automotive, AEC-Q101 and standard products are electrically and thermally the same, except where specified.
- 5. For packaging details, go to our website at http://www.diodes.com.

Marking Information



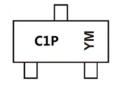
K = SAT (Shanghai Assembly / Test site)

1P = Product Type Marking Code

YM = Date Code Marking

Y = Year (ex: N = 2002)

M = Month (ex: 9 = September)



C = CAT (Chengdu Assembly / Test site)

1P = Product Type Marking Code

YM = Date Code Marking

Y = Year (ex: N = 2002)

M = Month (ex: 9 = September)

Date Code Key

Year	2010		2011	2012		2013	2014		2015	2016		2017
Code	X		Υ	Z		Α	В		С	D		E
Month	Jan	Feb	Mar	Apr	May	Jun	Jul	Aug	Sep	Oct	Nov	Dec
Code	1	2	3	4	5	6	7	8	9	0	N	D



Maximum Ratings (@T_A = +25°C unless otherwise specified)

Characteristic	Symbol	Value	Unit
Collector-Base Voltage	V _{CBO}	75	V
Collector-Emitter Voltage	V _{CEO}	40	V
Emitter-Base Voltage	V _{EBO}	6.0	V
Collector Current - Continuous	I _C	600	mA
Peak Collector Current	Icm	800	mA

Thermal Characteristics (@T_A = +25°C unless otherwise specified)

Characteristic	Symbol	Value	Unit		
Collector Power Dissipation	(Note 6)	C	310	mW	
Collector Power Dissipation	(Note 7)	P_{D}	350		
Thermal Resistance, Junction to Ambient	(Note 6)	D	403	°C/M	
Thermal Resistance, Junction to Ambient	(Note 7)	$R_{\theta JA}$	357	°C/W	
Thermal Resistance, Junction to Leads (Note 8)		$R_{ hetaJL}$	350	°C/W	
Operating and Storage Temperature Range	$T_{J,}T_{STG}$	-55 to +150	°C		

ESD Ratings (Note 9)

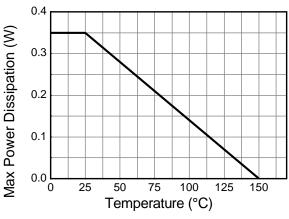
Characteristic	Symbol	Value	Unit	JEDEC Class
Electrostatic Discharge - Human Body Model	ESD HBM	≥ 8,000	V	3B
Electrostatic Discharge - Machine Model	ESD MM	≥ 400	V	С

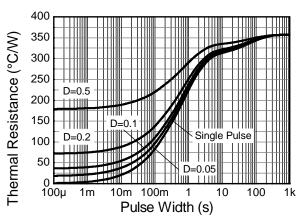
Notes:

- 6. For the device mounted on minimum recommended pad layout FR4 PCB with high coverage of single sided 1oz copper in still air condition; the device is measured when operating in a steady-state condition.
- 7. Same as Note 6, except the device is mounted on 15mm X 15mm X 1.6mm FR4 PCB
- 8. Thermal resistance from junction to solder-point (at the end of the collector lead).
- 9. Refer to JEDEC specification JESD22-A114 and JESD22-A115.



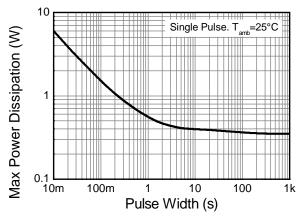
Thermal Characteristics





Derating Curve

Transient Thermal Impedance



Pulse Power Dissipation



Electrical Characteristics @T_A = 25°C unless otherwise specified

Characteristic (Note 9)	Symbol	Min	Max	Unit	Test Condition
OFF CHARACTERISTICS					
Collector-Base Breakdown Voltage	BV _{CBO}	75	_	V	$I_C = 100 \mu A, I_E = 0$
Collector-Emitter Breakdown Voltage	BV _{CEO}	40		V	$I_C = 10 \text{mA}, I_B = 0$
Emitter-Base Breakdown Voltage	BV _{EBO}	6.0	_	V	$I_E = 100 \mu A, I_C = 0$
Collector Cutoff Current		_	10	nA	$V_{CB} = 60V, I_{E} = 0$
Collector Cuton Current	Ісво			μΑ	$V_{CB} = 60V, I_E = 0, T_A = 150^{\circ}C$
Collector Cutoff Current	I _{CEX}	_	10	nA	$V_{CE} = 60V, V_{EB(OFF)} = 3.0V$
Emitter Cutoff Current	I _{EBO}	_	10	nA	$V_{EB} = 3.0V, I_C = 0$
Base Cutoff Current	I_{BL}	_	20	nA	$V_{CE} = 60V, V_{EB(OFF)} = 3.0V$
ON CHARACTERISTICS (Note 9)					
		35	_		$I_C = 100 \mu A, V_{CE} = 10 V$
		50	_		$I_C = 1.0 \text{mA}, V_{CE} = 10 \text{V}$
		75	_		$I_C = 10mA$, $V_{CE} = 10V$
DC Current Gain	h _{FE}	100	300	_	$I_C = 150 \text{mA}, V_{CE} = 10 \text{V}$
		40	_		$I_C = 500 \text{mA}, V_{CE} = 10 \text{V}$
		50	_		$I_C = 10 \text{mA}, V_{CE} = 10 \text{V}, T_A = -55 ^{\circ}\text{C}$
		35			$I_C = 150 \text{mA}, V_{CE} = 1.0 \text{V}$
Collector-Emitter Saturation Voltage	V _{CE(sat)}	_	0.3	V	$I_C = 150 \text{mA}, I_B = 15 \text{mA}$
Concetor Emitter Saturation Voltage	V CE(sat)		1.0	٧	$I_C = 500 \text{mA}, I_B = 50 \text{mA}$
Base-Emitter Saturation Voltage	V _{BE(sat)}	0.6	1.2	V	$I_C = 150 \text{mA}, I_B = 15 \text{mA}$
	VBE(sat)	—	2.0	V	$I_C = 500 \text{mA}, I_B = 50 \text{mA}$
SMALL SIGNAL CHARACTERISTICS					
Output Capacitance	C_{obo}	_	8	pF	$V_{CB} = 10V, f = 1.0MHz, I_E = 0$
Input Capacitance	C_{ibo}	_	25	pF	$V_{EB} = 0.5V$, $f = 1.0MHz$, $I_{C} = 0$
Current Gain-Bandwidth Product	f_{T}	300	_	MHz	V _{CE} = 20V, I _C = 20mA, f = 100MHz
Noise Figure	NF		4.0	dB	$V_{CE} = 10V, I_C = 100\mu A,$
<u> </u>					$R_S = 1.0k\Omega$, $f = 1.0kHz$
SWITCHING CHARACTERISTICS					T
Delay Time	t _d	_	10	ns	$V_{CC} = 30V, I_C = 150mA,$
,					$V_{BE(off)} = -0.5V, I_{B1} = 15mA$
Rise Time	t _r	_	25	ns	$V_{CC} = 3.0V$, $I_C = 150mA$, $I_{B1} = 15mA$,
	••				$V_{BE(OFF)} = 0.5V$
Storage Time	t _s	_	225	ns	$V_{CC} = 30V, I_{C} = 150mA,$
				-	$I_{B1} = I_{B2} = 15mA$
Fall Time	t _f	_	60	ns	$V_{CC} = 30V$, $I_C = 150mA$, $I_{B1} = I_{B2} = 15mA$

Notes: 10. Measured under pulsed conditions. Pulse width \leq 300 μ s. Duty cycle \leq 2%.



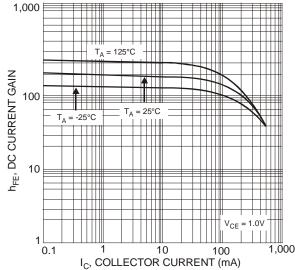
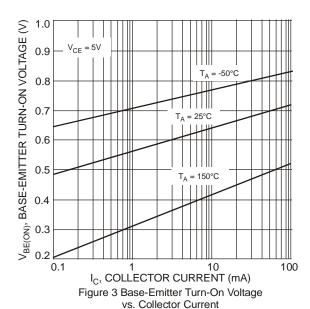
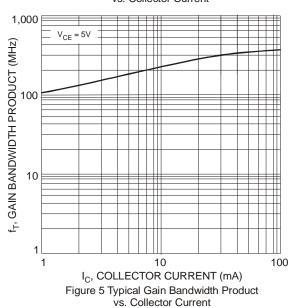


Figure 1 Typical DC Current Gain vs. Collector Current





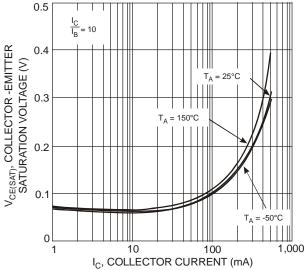
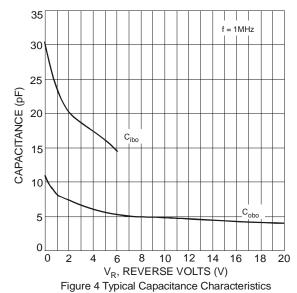


Figure 2 Typical Collector-Emitter Saturation Voltage vs. Collector Current



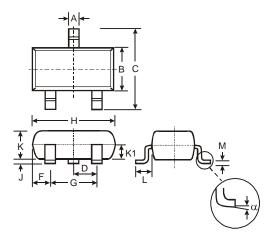
2.0 = 30mA V_{CE} , COLLECTOR-EMITTER VOLTAGE (V) 1.8 = 1mAI_C = 10mA 1.6 I_C = 100mA 1.4 = 300mA 1.2 1.0 8.0 0.6 0.4 0.2 0 0.001 0.01 I_B, BASE CURRENT (mA)

Figure 6 Typical Collector Saturation Region



Package Outline Dimensions

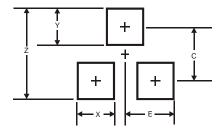
Please see AP02002 at http://www.diodes.com/datasheets/ap02002.pdf for latest version.



	SOT23						
Dim	Min	Max	Тур				
Α	0.37	0.51	0.40				
В	1.20	1.40	1.30				
С	2.30	2.50	2.40				
D	0.89	1.03	0.915				
F	0.45	0.60	0.535				
G	1.78	2.05	1.83				
Н	2.80	3.00	2.90				
J	0.013	0.10	0.05				
K	0.903	1.10	1.00				
K1	-	-	0.400				
L	0.45	0.61	0.55				
M	0.085	0.18	0.11				
α	0°	8°	-				
All	All Dimensions in mm						

Suggested Pad Layout

Please see AP02001 at http://www.diodes.com/datasheets/ap02001.pdf for the latest version.



Dimensions	Value (in mm)
Z	2.9
Х	0.8
Y	0.9
С	2.0
Ш	1.35

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