High Speed, Low Voltage, 3 Ω, Differential 4:1 CMOS Analog Multiplexer/Switch

DESCRIPTION

The DG2707 is a high speed, low voltage, 3 Ω, differential 4:1 multiplexer. It operates from a 1.65 V to 4.3 V single power supply. All channels guaranteed break before make switching. When powered with single 3.15 V supply, channel to channel ON Resistance matching is within 0.3 Ω.

All control logic input has 0.5 V to 1.65 V threshold. The EN pin enables cascading of the multiplexers. It features a 120 MHz - 3 dB bandwidth, - 90 dB crosstalk and - 70 dB off-isolation at 1 MHz.

The DG2707 comes in a small miniQFN-16 lead package (1.8 mm x 2.6 mm x 0.75 mm). As a committed partner to community and the environment, Vishay Siliconix manufactures this product with the lead (Pb)-free device terminations and is 100 % RoHS complicant.

FEATURES

- Low voltage operation (1.65 V to 4.3 V)
- Low on-resistance - R_{ON}: 2.8 Ω typ. at 3.15 V
- Low voltage logic threshold
- Low crosstalk: - 70 dB
- High off-isolation: - 90 dB
- Ultra small package: miniQFN16 of 1.8 mm x 2.6 mm

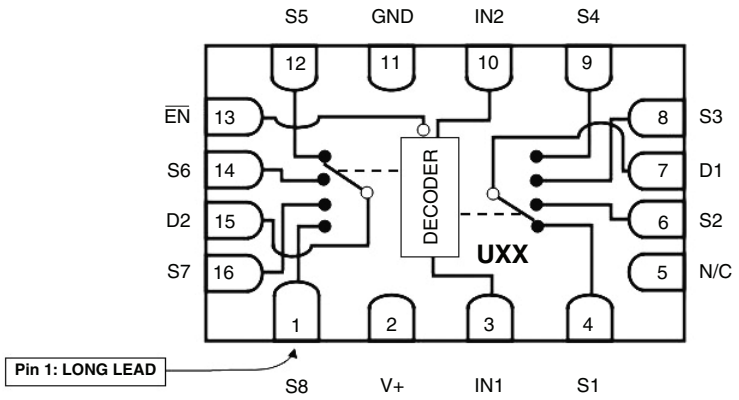
Pb-free
RoHS COMPLIANT

APPLICATIONS

- A/V and analog signal routing
- Battery operated devices
- Data acquisition systems
- Communications systems
- Medical and ATE equipments

FUNCTIONAL BLOCK DIAGRAM AND PIN CONFIGURATION

miniQFN-16L



Top View

Device Marking: UXX
Traceability Code:
U is DG2707DN
XX = Date/Lot

ORDERING INFORMATION		
Temp. Range	**Package**	**Part Number**
- 40 °C to 85 °C	miniQFN-16	DG2707DN-T1-E4

TRUTH TABLE DG2707 MULTIPLEXER, MINIQFN-16L				
Enable Input	**Select Input**		**On Switches (Pin)**	
EN (Pin 13)	**IN2 (Pin 10)**	**IN1 (Pin 3)**	**Description (Pin)**	**Common (Pin)**
0	0	0	S5 (Pin 12)	D2 (Pin 15)
0	0	1	S6 (Pin 14)	
0	1	0	S7 (Pin 16)	
0	1	1	S8 (Pin 1)	
0	0	0	S1 (Pin 4)	D1 (Pin 7)
0	0	1	S2 (Pin 6)	
0	1	0	S3 (Pin 8)	
0	1	1	S4 (Pin 9)	
1	X	X	All Switches are off	
Pin 5 N/C				

ABSOLUTE MAXIMUM RATINGS (T_A = 25 °C, unless otherwise noted)			
Parameter		**Limit**	**Unit**
Reference to GND	V+	- 0.3 to 5.0	V
	EN, IN, D_X, S_X[a]	- 0.3 to (V+ + 0.3)	
Current (Any terminal except S_X or D_X)		30	mA
Continuous Current (S_X or D_X)		± 300	
Peak Current (Pulsed at 1 ms, 10 % Duty Cycle)		± 500	
Storage Temperature (D Suffix)		- 65 to 150	°C
Thermal Resistance (Package)[b]	miniQFN-16	152	°C/W
Power Dissipation (Packages)[b]	miniQFN-16[c, d]	525	mW

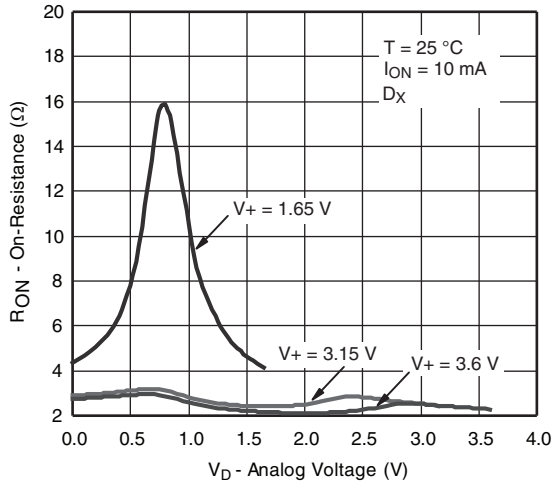
Notes:
a. Signals on S_X or D_X, or IN_X or EN exceeding V+ will be clamped by internal diodes. Limit forward diode current to maximum current ratings.
b. All leads welded or soldered to PC board.
c. Derate 6.6 mW/°C above 70 °C
d. Manual soldering with iron is not recommended for leadless components. The miniQFN-16 is a leadless package. The end of the lead terminal is exposed copper (not plated) as a result of the singulation process in manufacturing. A solder fillet at the exposed copper lip cannot be guaranteed and is not required to ensure adequate bottom side solder interconnection.

SPECIFICATIONS (V+ = 3.15 V)							
Parameter	**Symbol**	**Test Conditions Otherwise Unless Specified**	**Temp.[b]**	**Limits - 40 °C to 85 °C**			**Unit**
				Min.[d]	**Typ.[c]**	**Max.[d]**	
Analog Switch							
Analog Signal Range[e]	V_{analog}	$R_{DS(on)}$	Full	0		V+	V
On Resistance	$R_{DS(on)}$	V+ = 3.15 V, IS_X = 10 mA, VD_X = 1.0 V	Room		2.8	5.5	Ω
			Full			6	
R_{ON} Match	$\Delta R_{(on)}$	V+ = 3.15 V, IS_X = 10 mA, VD_X = 1.0 V	Room		0.3		
R_{ON} Resistance Flatness	$R_{(on)}$ Flatness	V+ = 3.15 V, IS_X = 10 mA, VD_X = 0.0 V, 1.0 V	Room		0.6		
Channel-Off Leakage Current	$I_{SX(off)}$	V+ = 3.6 V, VS_X = 0.5 V/3 V, VD_X = 3 V/0.5 V	Room	- 5		5	nA
	$I_{DX(off)}$		Full	- 10		10	
Channel-On Leakage Current	$I_{DX(on)}$	V+ = 3.6 V, VS_X, VD_X = 3 V/0.5 V	Room	- 10		10	
			Full	- 20		20	
Digital Control							
Input High Voltage	V_{INH}		Full	1.65			V
Input Low Voltage	V_{INL}					0.4	
Input Current	I_{INL} or I_{INH}	V_{IN} = 0 or V+		- 1		1	µA
Input Capacitance	C_{IN}	V+ = 3.15, f = 1 MHz			5.1		pF
Dynamic Characteristics							
Break-Before-Make Time	t_{BBM}	VS_X = 1.5 V, R_L = 50 Ω, C_L = 35 pF	Room		1		ns
			Full	5			
Enable Turn-On Time	$t_{ON(EN)}$		Room		20	45	
			Full			55	
Enable Turn-Off Time	$t_{OFF(EN)}$		Room		15	35	
			Full			45	
Transition Time	t_{TRANS}		Room		35	55	
			Full			65	
Charge Injection[d]	Q_{INJ}	C_L = 1 nF, R_{GEN} = 0 Ω, VS_X = 2 V	Room		- 14		pC
Off-Isolation[d]	OIRR	V+ = 3.15 V, f = 1 MHz, R_L = 50 Ω, C_L = 5 pF	Room		- 70		dB
Crosstalk[d, f]	X_{TALK}				- 90		
Bandwidth[d]	BW	V+ = 3.15 V, R_L = 50 Ω, C_L = 5 pF, - 3 dB	Room		120		MHz
Total Harmonic Distortion[d]	THD	V+ = 3.15 V, R_{load} = 600 Ω	Room		0.02		%
S_X, D_X Off Capacitance[d]	$C_{S(off)}$	V+ = 3.15 V, f = 1 MHz	Room		16		pF
	$CD_{X(off)}$				42		
Channel-On Capacitance[d]	$CD_{X(on)}$				49		
Power Supply							
Power Supply Range	V+			1.65		4.3	V
Power Supply Current	I+	V_{IN} = 0 V or V+	Full			1	µA

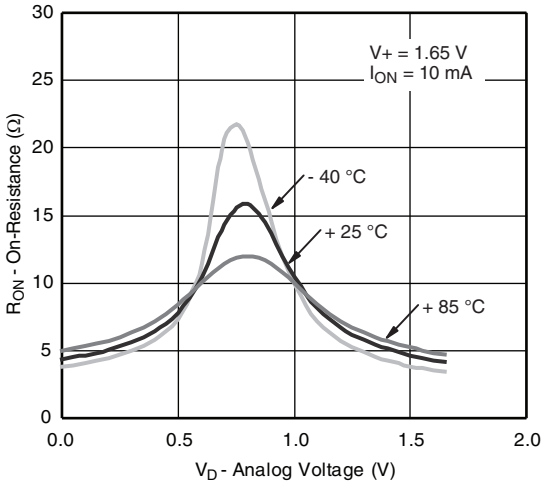
Notes:
a. Room = 25 °C, Full = as determined by the operating suffix.
b. The algebraic convention whereby the most negative value is a minimum and the most positive a maximum, is used in this data sheet.
c. Typical values are for design aid only, not guaranteed nor subject to production testing.
d. Guarantee by design, not subjected to production test.
e. V_{IN} = input voltage to perform proper function.
f. Crosstalk measured between channels.

Stresses beyond those listed under "Absolute Maximum Ratings" may cause permanent damage to the device. These are stress ratings only, and functional operation of the device at these or any other conditions beyond those indicated in the operational sections of the specifications is not implied. Exposure to absolute maximum rating conditions for extended periods may affect device reliability.

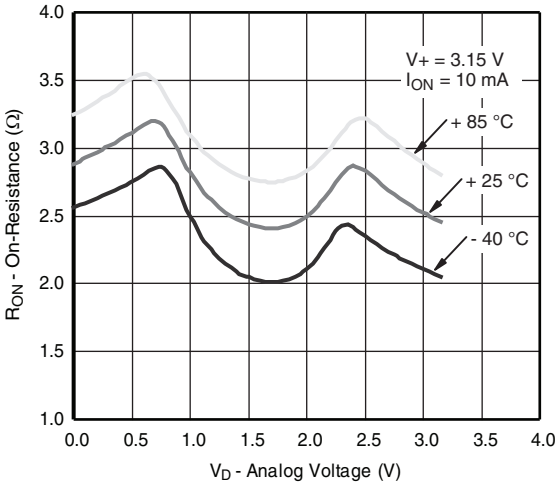
TYPICAL CHARACTERISTICS (T_A = 25 °C, unless otherwise noted)



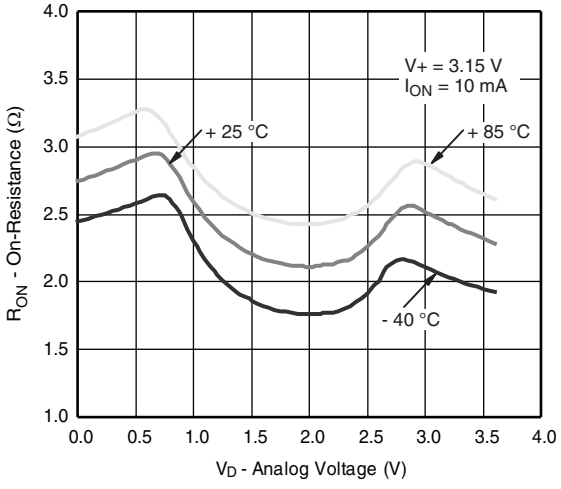
R_{ON} vs. V_D and Single Supply Voltage



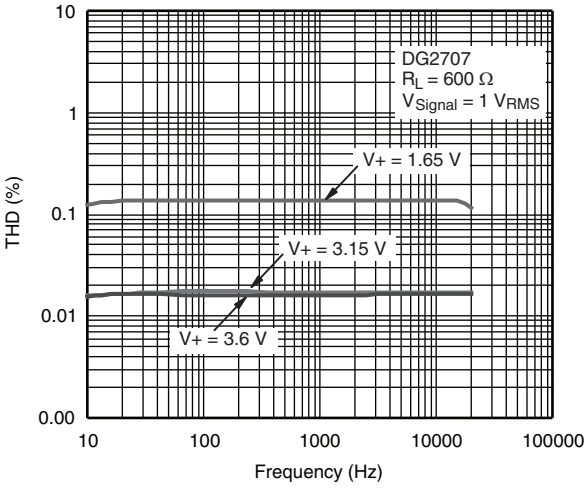
R_{ON} vs. Analog Voltage and Temperature



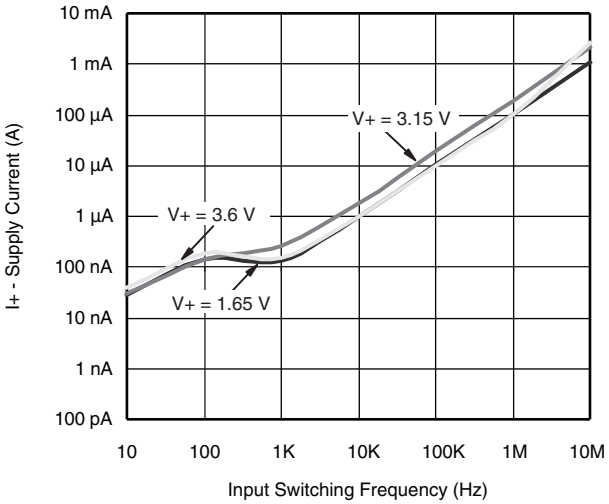
R_{ON} vs. Analog Voltage and Temperature



R_{ON} vs. Analog Voltage and Temperature

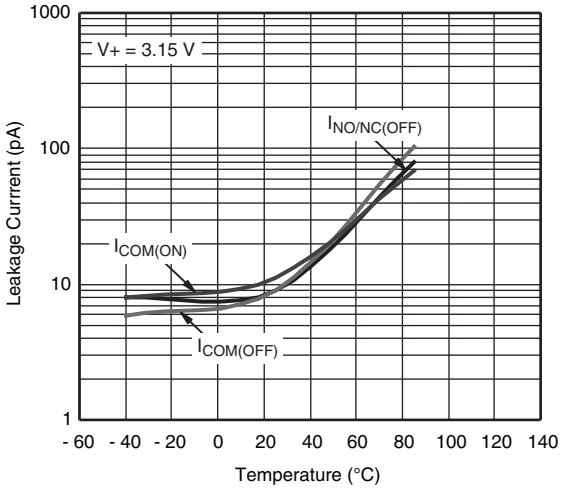


Switching Threshold vs. Supply Voltage

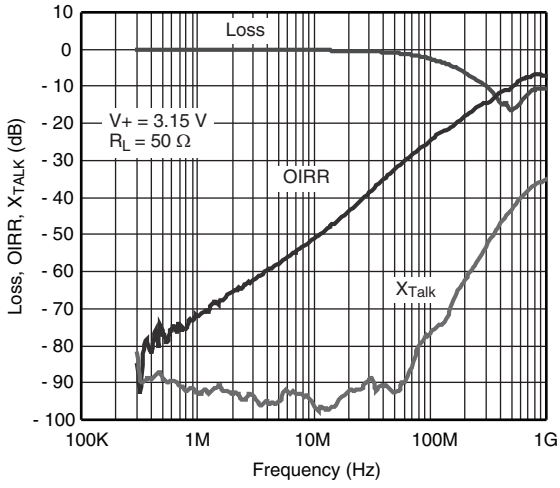


Supply Current vs. Input Switching Frequency

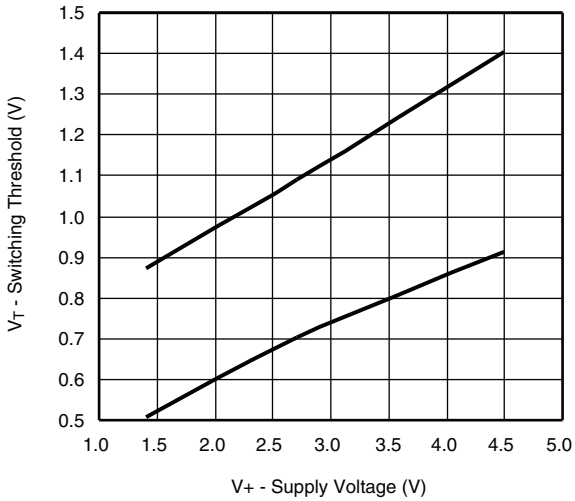
TYPICAL CHARACTERISTICS (T_A = 25 °C, unless otherwise noted)



Leakage Current vs. Temperature



Insertion Loss, Off-Isolation Crosstalk vs. Frequency



Switching Threshold vs. Supply Voltage

TEST CIRCUITS

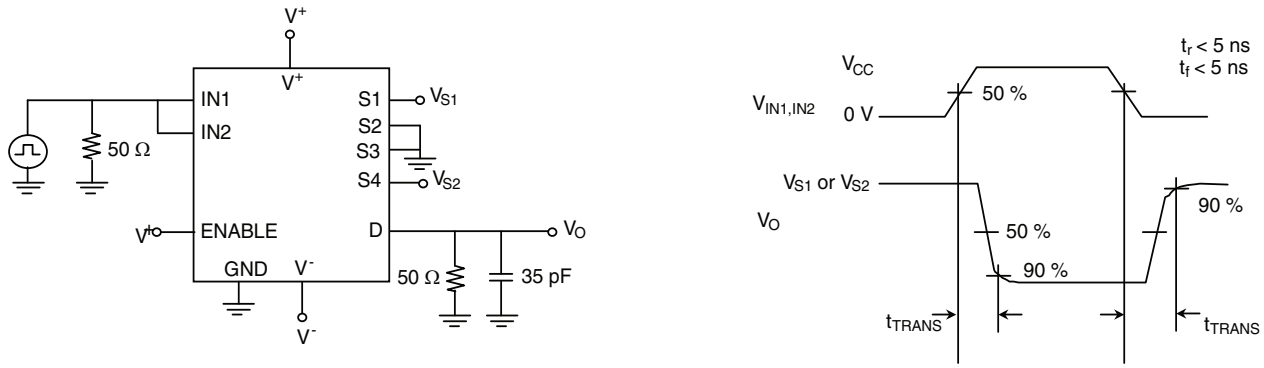


Figure 1. Transition Time

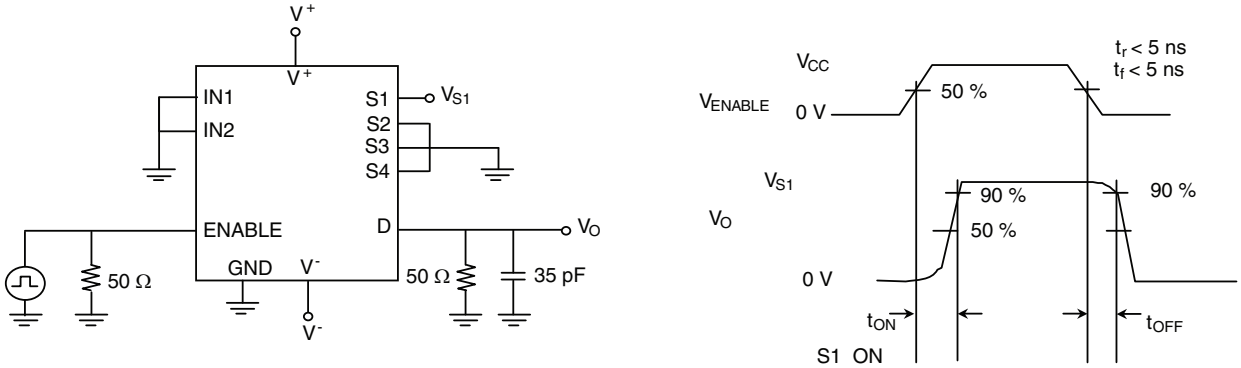


Figure 2. Enable Switching Time

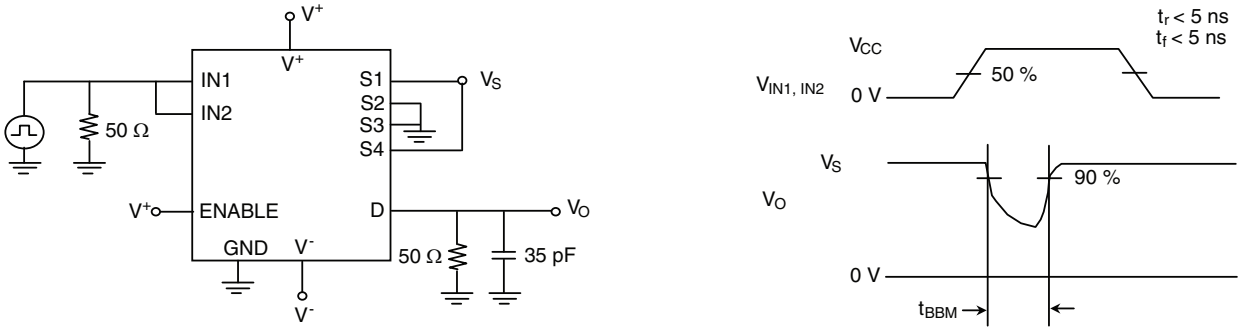


Figure 3. Break-Before Make

TEST CIRCUITS

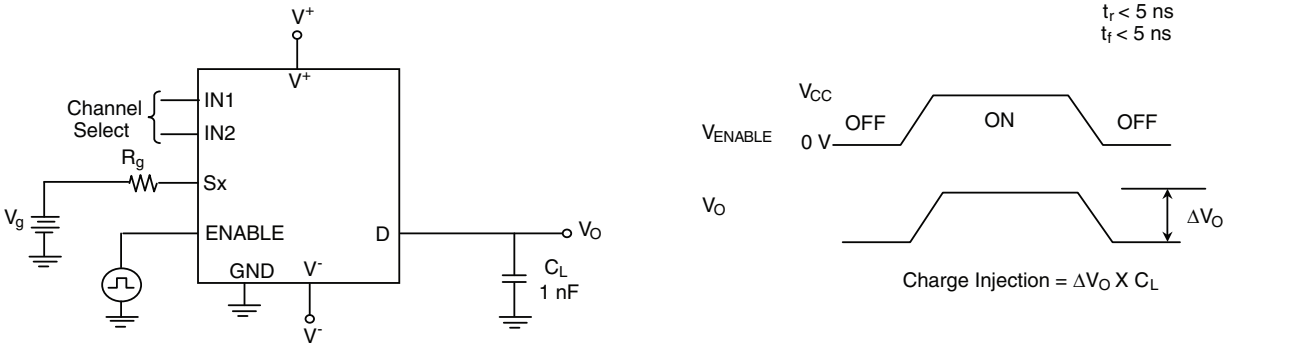


Figure 4. Charge Injection

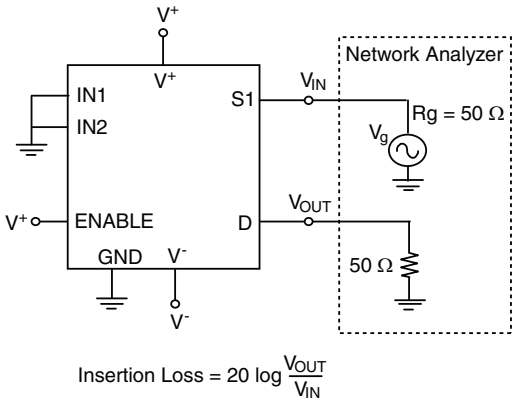


Figure 5. Insertion Loss

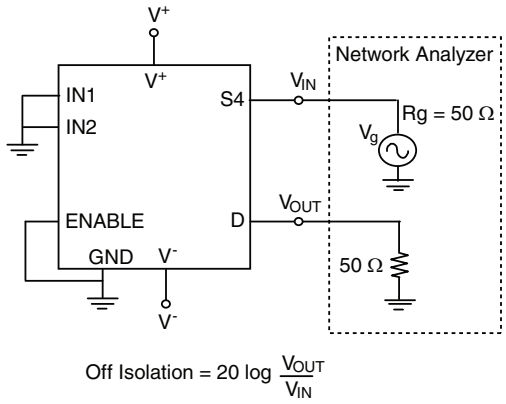


Figure 6. Off-Isolation

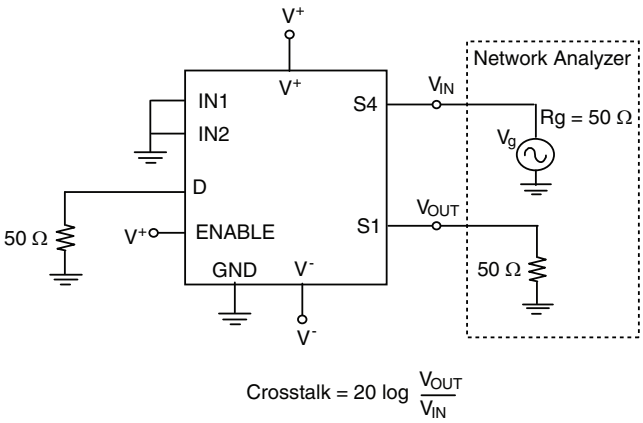


Figure 7. Crosstalk

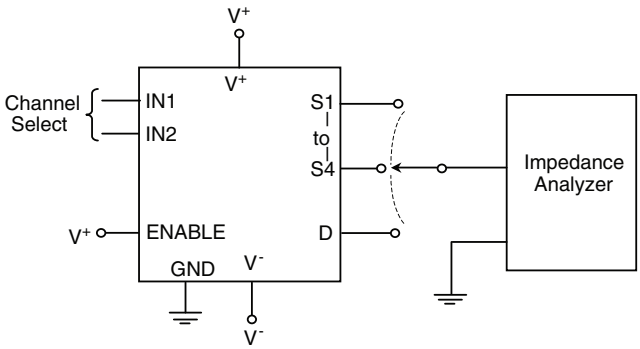
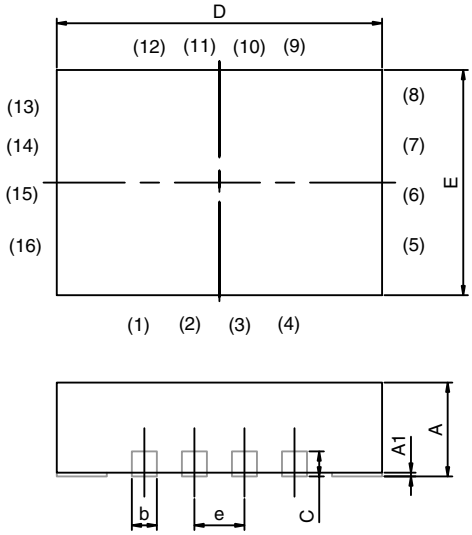
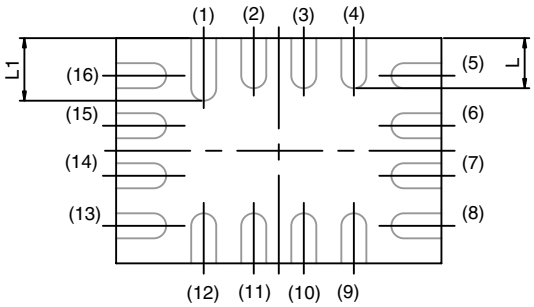


Figure 8. Source, Drain Capacitance

Vishay Siliconix maintains worldwide manufacturing capability. Products may be manufactured at one of several qualified locations. Reliability data for Silicon Technology and Package Reliability represent a composite of all qualified locations. For related documents such as package/tape drawings, part marking, and reliability data, see www.vishay.com/ppg?68397.

MINI QFN-16L



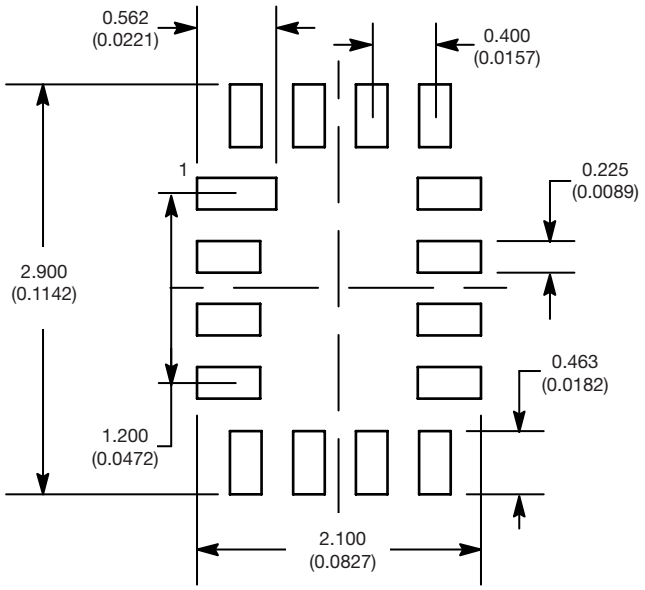


BACK SIDE VIEW

DIM	MILLIMETERS			INCHES		
	MIN.	NAM	MAX.	MIN.	NAM	MAX.
A	0.70	0.75	0.80	0.0275	0.0295	0.0315
A1	0	-	0.05	0	-	0.002
b	0.15	0.20	0.25	0.0059	0.0078	0.0098
C	0.15	0.20	0.25	0.0059	0.0078	0.0098
D	2.60 BSC			0.1023 BSC		
E	1.80 BSC			0.0708 BSC		
e	0.40 BSC			0.0157 BSC		
L	0.35	0.40	0.45	0.0137	0.0157	0.0177
L1	0.45	0.50	0.55	0.0177	0.0196	0.0216

ECN T-06380-Rev. A, 14-Aug-06
DWG: 5954

RECOMMENDED MINIMUM PADS FOR MINI QFN 16L



Mounting Footprint
Dimensions in mm (inch)

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