



A Product Line of Diodes Incorporated



**ZXTR2008K** 

#### 100V INPUT, 8.2V 50mA REGULATOR TRANSISTOR

## Description

The ZXTR2008K monolithically integrates a transistor, Zener diode and resistor to function as a high voltage linear regulator. The device regulates with an 8.2V nominal output at 15mA. It is designed for use in high voltage applications where standard linear regulators cannot be used. This function is fully integrated into a single TO252 package, minimizing PCB area and reducing number of components when compared with a multi-chip discrete solution.

# Applications

Supply voltage regulation in::

- Networking
- Telecom
- Power Over Ethernet (PoE)

TO252 (DPAK)

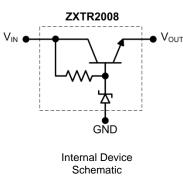
### **Features**

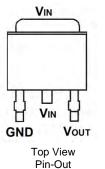
- Series Linear Regulator Using Emitter-Follower Stage
- Input Voltage = 12V to 100V
- Output Voltage = 8.2V ±10%
- Fully integrated into a single TO252 package
- Totally Lead-Free & Fully RoHS compliant (Notes 1 & 2)
- Halogen and Antimony Free. "Green" Device (Note 3)
- Qualified to AEC-Q101 Standards for High Reliability

### **Mechanical Data**

- Case: TO252 (DPAK)
- Case Material: Molded Plastic. "Green" Molding Compound.
- UL Flammability Rating 94V-0
- Moisture Sensitivity: Level 1 per J-STD-020
- Terminals: Finish Matte Tin Plated Leads, Solderable per MIL-STD-202, Method 208 3
- Weight: 0.34 grams (approximate)







Pin Name	Pin Function	
Vin	Input Supply	
GND	Power Ground	
Vout	Voltage Output	

### Ordering Information (Note 4)

Product	Package	Marking	Reel size (inches)	Tape width (mm)	Quantity per reel
ZXTR2008K-13	TO252 (DPAK)	ZXTR 2008	13	16	2,500

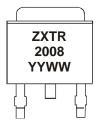
Notes: 1. No purposely added lead. Fully EU Directive 2002/95/EC (RoHS) & 2011/65/EU (RoHS 2) compliant.

2. See http://www.diodes.com/quality/lead\_free.html for more information about Diodes Incorporated's definitions of Halogen and Antimony free, "Green" and Lead-Free.

3. Halogen and Antimony free "Green" products are defined as those which contain <900ppm bromine, <900ppm chlorine (<1500ppm total Br + Cl) and <1000ppm antimony compounds.

4. For packaging details, go to our website at http://www.diodes.com/products/packages.html

# **Marking Information**

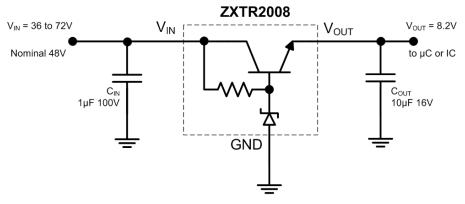


ZXTR2008 = Product Type Marking Code YYWW = Date Code Marking YY = Last Digit of Year, (ex: 13 = 2013) WW = Week Code 01 - 52





# **Typical Application Circuit**



Example of an 8.2V regulated supply from a nominal 48V for powering a Controller IC.

#### Absolute Maximum Ratings (Voltage relative to GND, @TA = +25°C, unless otherwise specified.)

Characteristic	Symbol	Value	Unit
Input Voltage	VIN	100	V
Continuous Input & Output Current	I <sub>IN,</sub> I <sub>OUT</sub>	550	mA

# Maximum Current at V<sub>IN</sub> = 48V (@T<sub>A</sub> = +25°C, unless otherwise specified.)

Characteristic		Symbol	Value	Unit
Continuous Output Current	(Note 7)	I <sub>OUT</sub>	55	mA
Dulaad Output Current	(Note 8)		900	
Pulsed Output Current	(Note 9)	Іом	190	mA

# **Thermal Characteristics**

Characteristic		Symbol	Value	Unit
Power Dissinction	(Note 5)	D	2.3	W
Power Dissipation	(Note 6)	P <sub>D</sub>	1.1	vv
Thermal Desistance Junction to Ambient	(Note 5)	D	44	
Thermal Resistance, Junction to Ambient	(Note 6)	R <sub>0JA</sub>	90	°C/W
Thermal Resistance, Junction to Lead (Note 10)		R <sub>θJL</sub>	8.39	
Recommended Operating Junction Temperature Range		TJ	-40 to +125	°C
Maximum Operating Junction and Storage Temperature Range		TJ, TSTG	-65 to +150	°C

#### ESD Ratings (Note 11)

Characteristics	Symbols	Value	Unit	JEDEC Class
Electrostatic Discharge – Human Body Model	ESD HBM	≥ 4000	V	ЗA
Electrostatic Discharge – Machine Model	ESD MM	≥ 300	V	В

Notes:

5. For a device mounted with the exposed V<sub>IN</sub> pad on 50mm x 50mm 1oz copper that is on a single-sided 1.6mm FR4 PCB; device is measured under still air conditions whilst operating in steady-state.

6. Same as note 5, except mounted on 15mm x 15mm 1oz copper.

7. Same as note 5, whilst operating at  $V_{IN}$ =48V. Refer to Safe Operating Area for other Input Voltages.

8. Same as note 5, except measured with a single pulse width = 100 $\mu$ s and V<sub>IN</sub>=48V.

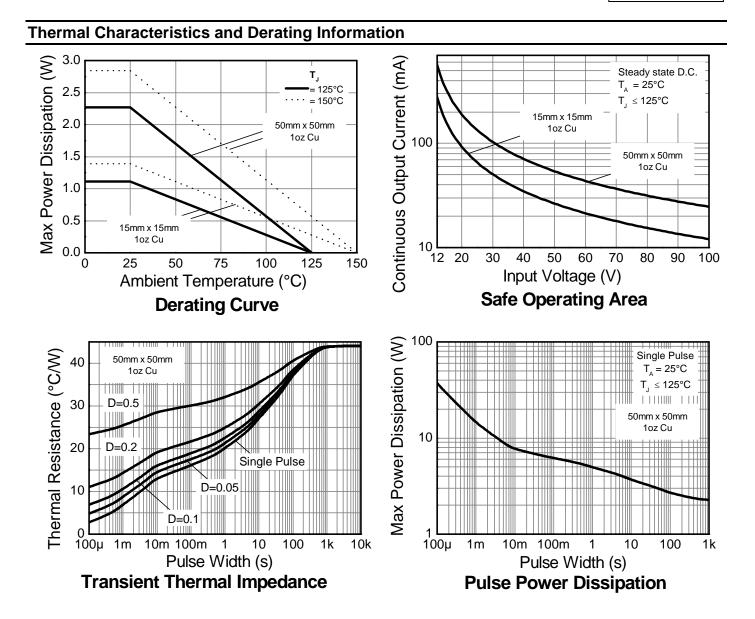
9. Same as note 5, except measured with a single pulse width = 10ms and  $V_{IN}$ =48V.

10. Thermal resistance from junction to solder-point (on the exposed V<sub>IN</sub> pad).

11. Refer to JEDEC specification JESD22-A114 and JESD22-A115.











# Electrical Characteristics (@T<sub>A</sub> = +25°C, unless otherwise specified.)

Characteristic	Symbol	Min	Тур	Max	Unit	Test Condition
Output Voltage (Note 12)	Vout	7.38	8.2	9.02	V	V <sub>IN</sub> = 48V, I <sub>OUT</sub> = 15mA
Line Regulation (Notes 12 & 13)	$\Delta V_{OUT}$		10	300	mV	$V_{IN} = 12$ to 100V, $I_{OUT} = 15$ mA
Temperature Coefficient	$\Delta V_{OUT} / \Delta T$	_	10	—	mV/°C	T <sub>J</sub> = -40°C to +125°C V <sub>IN</sub> = 48V, I <sub>OUT</sub> = 15mA
Load Regulation (Notes 12 & 14)	ΔVουτ	—	-180 -250	-300 -500	mV	$I_{OUT} = 0.1$ to 30mA, $V_{IN} = 48V$ $I_{OUT} = 0.1$ to 100mA, $V_{IN} = 48V$
Minimum Value of Input Voltage Required to Maintain Line Regulation	V <sub>IN(MIN)</sub>	12	_	_	V	-
Quiescent Current	ΙQ	_	275 650	500 900	μA	V <sub>IN</sub> = 48V, I <sub>OUT</sub> = 10μA V <sub>IN</sub> = 100V, I <sub>OUT</sub> = 10μA
Power Supply Rejection Ratio	$\Delta V_{IN} / \Delta V_{OUT}$	_	38	_	dB	C <sub>OUT</sub> = 100nF, I <sub>OUT</sub> = 15mA, V <sub>OUT</sub> = 8.2V, V <sub>IN</sub> =12 to 100V, f=100H

Notes: 12. Measured under pulsed conditions. Pulse width  $\leq$  300µs. Duty cycle  $\leq$  2%.

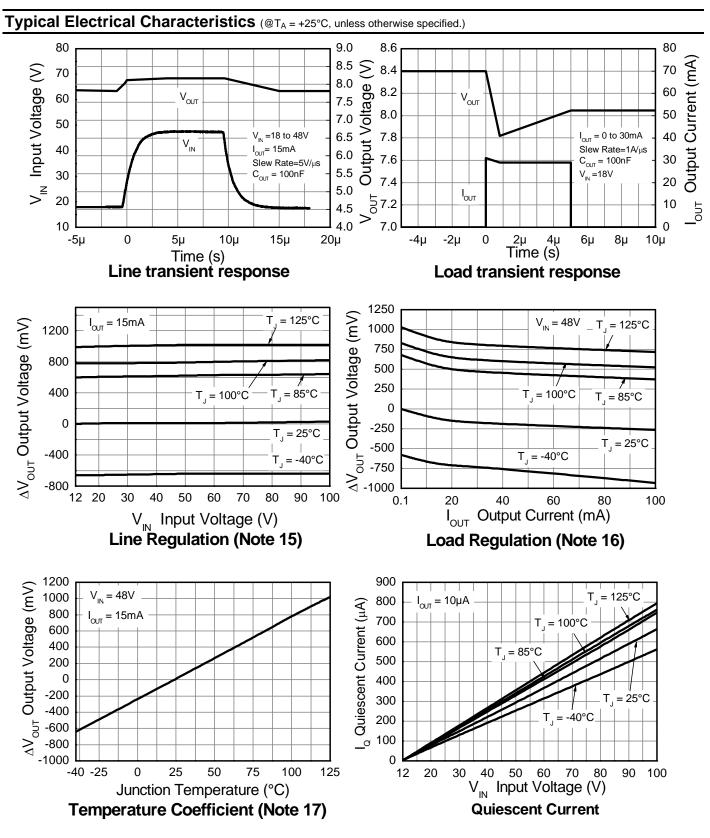
 $\Delta V_{OUT} = V_{OUT} (@V_{IN} = 100V) - V_{OUT} (@V_{IN} = 12V)$ 13. Line regulation

14. Load regulation  $\Delta V_{OUT} = V_{OUT} (@ I_{OUT} = 30 \text{mA}) - V_{OUT} (@ I_{OUT} = 0.1 \text{mA})$ 

 $\Delta V_{OUT} = V_{OUT} (@ I_{OUT} = 100 \text{mA}) - V_{OUT} (@ I_{OUT} = 0.1 \text{mA})$ 







Notes: 15. Line regulation  $\Delta V_{OUT} = V_{OUT} - V_{OUT} (@V_{IN} = 12V, I_{OUT} = 15mA, T_J = 25^{\circ}C)$ 16. Load regulation  $\Delta V_{OUT} = V_{OUT} - V_{OUT} (@V_{IN} = 48V, I_{OUT} = 0.1 \text{mA}, T_J = 25^{\circ}\text{C})$ 

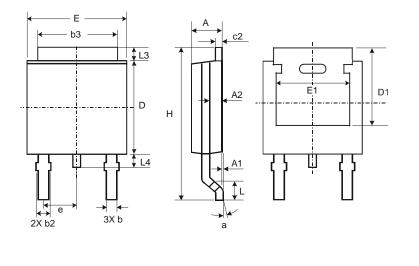
17. Temperature Coefficient  $\Delta V_{OUT} = V_{OUT} - V_{OUT} (@V_{IN} = 48V, I_{OUT} = 15mA, T_J = 25^{\circ}C)$ 





# **Package Outline Dimensions**

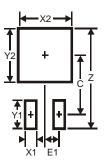
Please see AP02002 at http://www.diodes.com/datasheets/ap02002.pdf for latest version.



TO252						
Dim	Min	Max	Тур			
Α	2.19	2.39	2.29			
A1	0.00	0.13	0.08			
A2	0.97	1.17	1.07			
b	0.64	0.88	0.783			
b2	0.76	1.14	0.95			
b3	5.21	5.46	5.33			
c2	0.45	0.58	0.531			
D	6.00	6.20	6.10			
D1	5.21	_	-			
е	_	_	2.286			
Е	6.45	6.70	6.58			
E1	4.32	_	_			
Η	9.40	10.41	9.91			
L	1.40	1.78	1.59			
L3	0.88	1.27	1.08			
L4	0.64	1.02	0.83			
а	0°	10°	-			
All Dimensions in mm						

# Suggested Pad Layout

Please see AP02001 at http://www.diodes.com/datasheets/ap02001.pdf for the latest version.



Dimensions	Value (in mm)
Z	11.6
X1	1.5
X2	7.0
Y1	2.5
Y2	7.0
С	6.9
E1	2.3



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