

60V P-CHANNEL ENHANCEMENT MODE MOSFET

Product Summary

V _{(BR)DSS}	R _{DS(on)} Max	I _D Max T _A = 25°C (Note 7)	
60)/	125m Ω @ V _{GS} = -10V	-3.0 A	
-60V	190mΩ @ V _{GS} = -4.5V	-2.4 A	

Description and Applications

This MOSFET has been designed to minimize the on-state resistance and yet maintain superior switching performance, making it ideal for high efficiency power management applications.

- DC-DC Converters
- Power management functions
- · Disconnect switches
- Motor control

Features and Benefits

- Low on-resistance
- Fast switching speed
- Low threshold
- Low gate drive
- Low input capacitance
- Totally Lead-Free & Fully RoHS Compliant (Notes 1 & 2)
- Halogen and Antimony Free. "Green" Device (Note 3)
- Qualified to AEC-Q101 Standards for High Reliability
- PPAP Capable (Note 4)

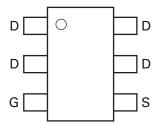
Mechanical Data

- Case: SOT-26
- Case Material: Molded Plastic, UL Flammability Classification Rating 94V-0
- Moisture Sensitivity: Level 1 per J-STD-020
- Terminals: Finish Matte Tin annealed over Copper lead frame.
 Solderable per MIL-STD-202, Method 208
- · Weight: 0.018 grams (approximate)

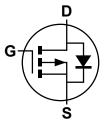




Top View



Pin Out - Top View



Equivalent Circuit

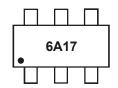
Ordering Information (Note 4 & 5)

Part Number	Compliance	Case	Quantity per reel
ZXMP6A17E6TA	Standard	SOT-26	3,000
ZXMP6A17E6QTA	Automotive	SOT-26	3.000

Note:

- 1. No purposely added lead. Fully EU Directive 2002/95/EC (RoHS) & 2011/65/EU (RoHS 2) compliant.
- See http://www.diodes.com/quality/lead_free.html for more information about Diodes Incorporated's definitions of Halogen- and Antimony-free, "Green" and Lead-free.
- 3. Halogen- and Antimony-free "Green" products are defined as those which contain <900ppm bromine, <900ppm chlorine (<1500ppm total Br + Cl) and <1000ppm antimony compounds.
- 4. Automotive products are AEC-Q101 qualified and are PPAP capable. Automotive, AEC-Q101 and standard products are electrically and thermally the same, except where specified. For more information, please refer to http://www.diodes.com/quality/product_grade_definitions/.
- 5. For packaging details, go to our website at http"//www.diodes.com/products/packages.html.

Marking Information



6A17 = Product Type Marking Code





Maximum Ratings (@T_A = +25°C, unless otherwise specified.)

Characteristic			Symbol	Value	Unit
Drain-Source voltage		V_{DSS}	-60	V	
Gate-Source voltage			V _{GS}	±20	V
		(Note 7)		-3.0	
Continuous Drain current	$V_{GS} = 10V$	$T_A = 70^{\circ}C \text{ (Note 7)}$	I_{D}	-2.4	Α
		(Note 6)		-2.3	
Pulsed Drain current	V _{GS} = 10V	(Note 8)	I _{DM}	-13.6	Α
Continuous Source current (Body diode) (Note 7)		(Note 7)	I _S	-2.5	Α
Pulsed Source current (Body diode) (Note 8)		I _{SM}	-13.6	Α	

Thermal Characteristics (@T_A = +25°C, unless otherwise specified.)

Characteristic		Symbol	Value	Unit	
Power dissipation	(Note 6)		1.1 8.8	W mW/°C	
Linear derating factor	(Note 7)	P _D	1.92 15.4		
Thermal Resistance, Junction to Ambient	(Note 6)	р	113	°C/W	
Thermal Resistance, sunction to Ambient	(Note 7)	$R_{ hetaJA}$	65		
Operating and storage temperature range		T _J , T _{STG}	-55 to 150	°C	

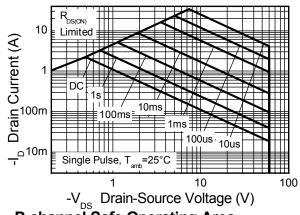
Notes:

- 6. For a device surface mounted on 25mm x 25mm x 1.6mm FR4 PCB with high coverage of single sided 1oz copper, in still air conditions; the device is measured when operating in a steady-state condition.
- 7. Same as note (6), except the device is measured at $t \le 5$ sec.
- 8. Same as note (6), except the device is pulsed with D = 0.02 and pulse width 300 μs. The pulse current is limited by the maximum junction temperature.

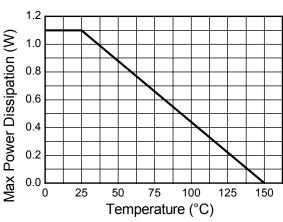




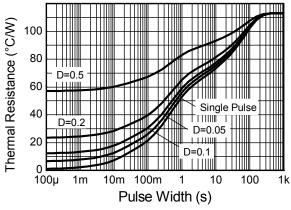
Thermal Characteristics



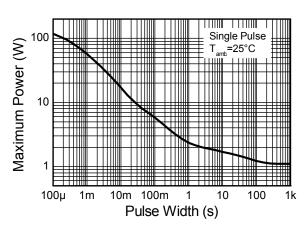
P-channel Safe Operating Area



Derating Curve



Transient Thermal Impedance



Pulse Power Dissipation





Electrical Characteristics (@TA = +25°C, unless otherwise specified.)

Characteristic	Symbol	Min	Тур	Max	Unit	Test Condition	
OFF CHARACTERISTICS							
Drain-Source Breakdown Voltage	BV_{DSS}	-60	_	_	V	$I_D = -250 \mu A$, $V_{GS} = 0 V$	
Zero Gate Voltage Drain Current	I _{DSS}	_	_	-1.0	μΑ	$V_{DS} = -60V, V_{GS} = 0V$	
Gate-Source Leakage	I _{GSS}	_	_	±100	nA	$V_{GS} = \pm 20V, V_{DS} = 0V$	
ON CHARACTERISTICS			•	•	•		
Gate Threshold Voltage	V _{GS(th)}	-1.0	_	-3.0	V	$I_D = -250 \mu A$, $V_{DS} = V_{GS}$	
Static Drain Source On Decistance (Note 0)	_		0.100	0.125	Ω	$V_{GS} = -10V, I_D = -2.3A$	
Static Drain-Source On-Resistance (Note 9)	R _{DS (ON)}		0.130	0.190	12	$V_{GS} = -4.5V, I_D = -1.9A$	
Forward Transconductance (Notes 9 & 10)	9 _{fs}	_	4.7	_	S	$V_{DS} = -15V, I_{D} = -2.3A$	
Diode Forward Voltage (Note 9)	V_{SD}	_	-0.85	-0.95	V	I _S = -2.0A, V _{GS} = 0V	
Reverse recovery time (Note 10)	t _{rr}		25.1	_	ns	I _F = -1.7A, di/dt = 100A/μs	
Reverse recovery charge (Note 10)	Q _{rr}	_	27.2	_	nC		
DYNAMIC CHARACTERISTICS (Note 10)			•	•	•		
Input Capacitance	C _{iss}	_	637	_	pF	.,	
Output Capacitance	Coss	_	70	_	pF	V _{DS} = -30V, V _{GS} = 0V f = 1.0MHz	
Reverse Transfer Capacitance	Crss	_	53	_	pF	1 - 1.0WH2	
Total Gate Charge (Note 11)	Qg	_	9.8	_	nC	V _{GS} = -5.0V	
Total Gate Charge (Note 11)	Qq	_	17.7	_	nC	V _{DS} = -30V	
Gate-Source Charge (Note 11)	Q _{gs}	_	1.6	_	nC	$V_{GS} = -10V$ $I_{D} = -2.3A$	
Gate-Drain Charge (Note 11)	Q_{gd}	_	4.4	_	nC		
Turn-On Delay Time (Note 11)	t _{D(on)}	_	2.6	_	ns	V_{DD} = -30V, V_{GS} = -10V I_{D} = -1.0A, $R_{G} \cong 6.0\Omega$	
Turn-On Rise Time (Note 11)	t _r	_	3.4	_	ns		
Turn-Off Delay Time (Note 11)	$t_{D(off)}$	_	26.2	_	ns		
Turn-Off Fall Time (Note 11)	t _f	_	11.3	_	ns		

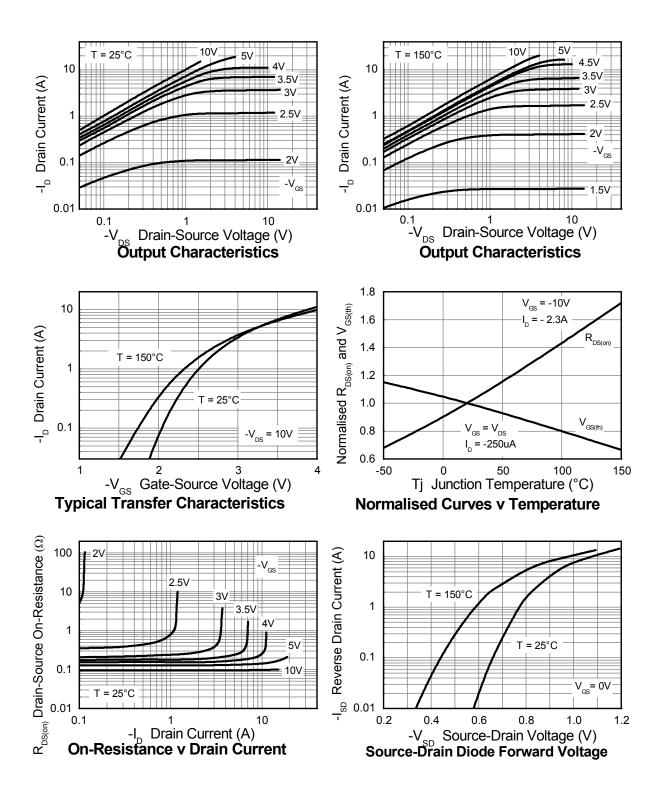
Notes:

- 9. Measured under pulsed conditions. Pulse width $\leq 300 \mu s$; duty cycle $\leq 2\%$
- 10. For design aid only, not subject to production testing.11. Switching characteristics are independent of operating junction temperatures.



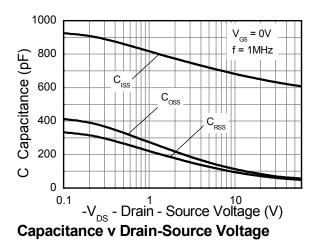


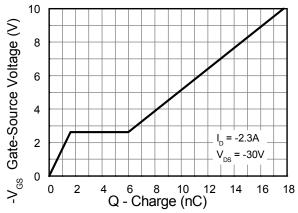
Typical Characteristics





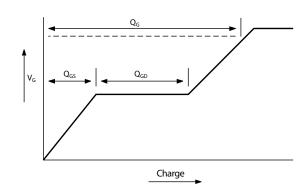
Typical Characteristics (cont.)





Gate-Source Voltage v Gate Charge

Test Circuits



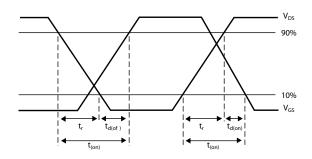
Current regulator

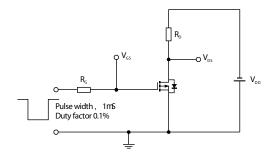
12V 0.2nF 50k Same as D.U.T

Vo.

Basic gate charge waveform

Gate charge test circuit





Switching time waveforms

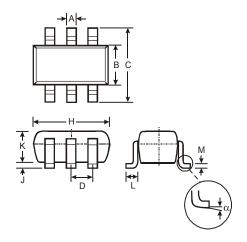
Switching time test circuit





Package Outline Dimensions

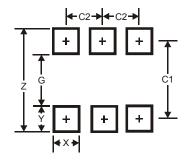
Please see AP02002 at http://www.diodes.com/datasheets/ap02002.pdf for latest version.



SOT26				
Dim	Min	Max	Тур	
Α	0.35	0.50	0.38	
В	1.50	1.70	1.60	
С	2.70	3.00	2.80	
D	_	_	0.95	
Н	2.90	3.10	3.00	
J	0.013	0.10	0.05	
K	1.00	1.30	1.10	
L	0.35	0.55	0.40	
М	0.10	0.20	0.15	
α	0°	8°		
All Dimensions in mm				

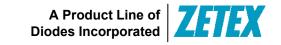
Suggested Pad Layout

Please see AP02001 at http://www.diodes.com/datasheets/ap02001.pdf for latest version.



Dimensions	Value (in mm)
Z	3.20
G	1.60
X	0.55
Υ	0.80
C1	2.40
C2	0.95





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