

**ZXMP10A13F**
**100V P-CHANNEL ENHANCEMENT MODE MOSFET**
**Product Summary**

$BV_{DS}$	Max $R_{DS(ON)}$	Package	Max $I_D$ $T_A = +25^\circ\text{C}$ Note 5
-100V	1.0 $\Omega$ @ $V_{GS} = -10\text{V}$	SOT23	-0.7A
	1.45 $\Omega$ @ $V_{GS} = -6.0\text{V}$		-0.5A

**Description**

This MOSFET utilizes a unique structure that combines the benefits of low on-resistance with fast switching speed, making it ideal for high-efficiency power management applications.

**Applications**

- DC - DC Converters
- Power Management Functions
- Disconnect Switches
- Motor Control

**Features**

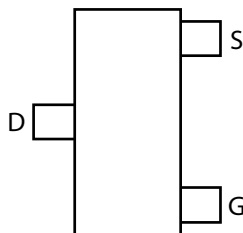
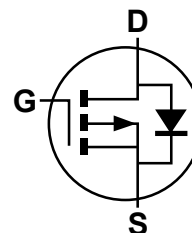
- Fast Switching Speed
- Low Input Capacitance
- Low Gate Charge
- Low Threshold
- **Totally Lead-Free & Fully RoHS Compliant (Notes 1 & 2)**
- **Halogen and Antimony Free. "Green" Device (Note 3)**
- **Qualified to AEC-Q101 Standards for High Reliability**
- **PPAP Capable (Note 4)**

**Mechanical Data**

- Case: SOT-23
- Case Material: Molded Plastic, UL Flammability Classification Rating 94V-0
- Moisture Sensitivity: Level 1 per J-STD-020
- Terminals: Matte Tin Finish annealed over Copper leadframe Solderable per MIL-STD-202, Method 208 (e3)
- Weight: 0.008 grams (approximate)



Top View

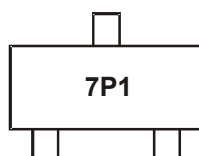

Top View  
Pin Out


Equivalent Circuit

**Ordering Information** (Note 4 & 5)

Part Number	Compliance	Case	Quantity per reel
ZXMP10A13FTA	Standard	SOT23	3,000
ZXMP10A13FQTA	Automotive	SOT23	3,000
ZXMP10A13FTC	Standard	SOT23	10,000
ZXMP10A13FQTC	Automotive	SOT23	10,000

- Notes:
1. No purposely added lead. Fully EU Directive 2002/95/EC (RoHS) & 2011/65/EU (RoHS 2) compliant.
  2. See [http://www.diodes.com/quality/lead\\_free.html](http://www.diodes.com/quality/lead_free.html) for more information about Diodes Incorporated's definitions of Halogen- and Antimony-free, "Green" and Lead-free.
  3. Halogen- and Antimony-free "Green" products are defined as those which contain <900ppm bromine, <900ppm chlorine (<1500ppm total Br + Cl) and <1000ppm antimony compounds.
  4. Automotive products are AEC-Q101 qualified and are PPAP capable. Automotive, AEC-Q101 and standard products are electrically and thermally the same, except where specified. For more information, please refer to [http://www.diodes.com/quality/product\\_grade\\_definitions/](http://www.diodes.com/quality/product_grade_definitions/).
  5. For packaging details, go to our website at <http://www.diodes.com/products/packages.html>.

**Marking Information**


7P1 = Product Type Marking Code

**Maximum Ratings** (@T<sub>A</sub> = +25°C, unless otherwise specified.)

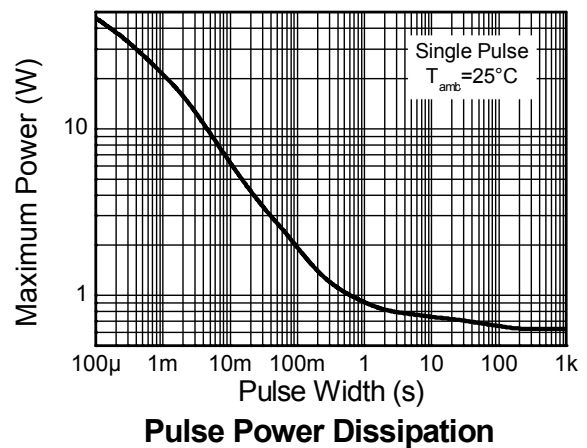
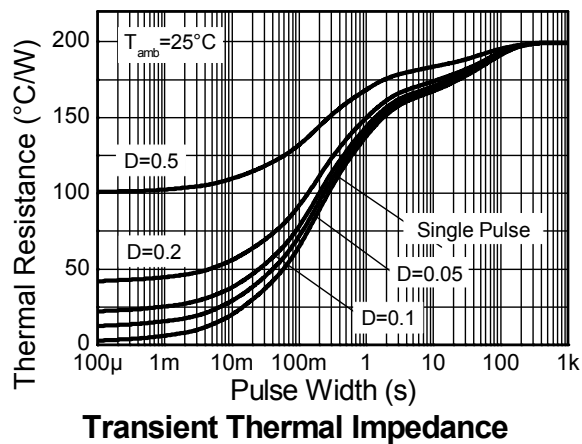
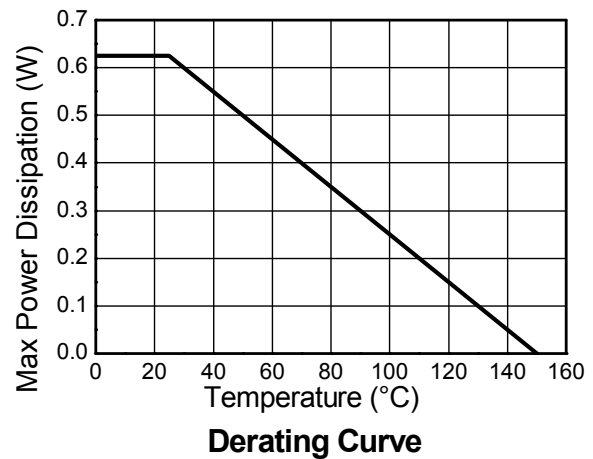
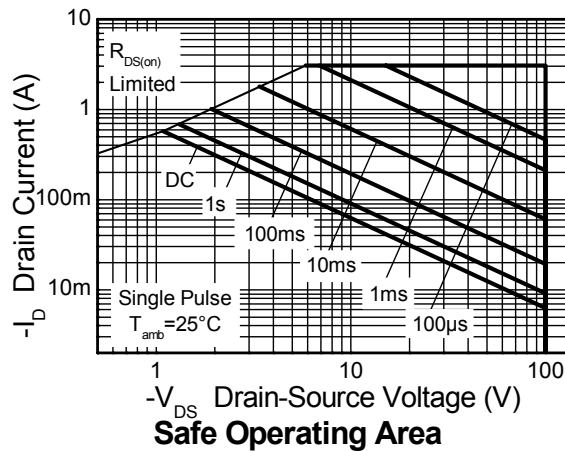
Characteristic			Symbol	Value	Units
Drain-Source Voltage			V <sub>DSS</sub>	-100	V
Gate-Source Voltage			V <sub>GS</sub>	±20	V
Continuous Drain Current	V <sub>GS</sub> = 10V	(Note 6) T <sub>A</sub> = +70°C (Note 6) (Note 5)	I <sub>D</sub>	-0.7 -0.5 -0.6	A
Pulsed Drain Current (Note 7)			I <sub>DM</sub>	-3.1	A
Continuous Source Current (Body Diode) (Note 6)			I <sub>S</sub>	-1.1	A
Pulsed Source Current (Body Diode) (Note 7)			I <sub>SM</sub>	-3.1	A

**Thermal Characteristics**

Characteristic	Symbol	Value	Unit
Power Dissipation (Note 5)	P <sub>D</sub>	625	mW
Linear Derating Factor		5	mW/°C
Power Dissipation (Note 6)	P <sub>D</sub>	806	mW
Linear Derating Factor		6.4	mW/°C
Thermal Resistance, Junction to Ambient (Note 5)	R <sub>θJA</sub>	200	°C/W
Thermal Resistance, Junction to Ambient (Note 6)	R <sub>θJA</sub>	155	°C/W
Thermal Resistance, Junction to Leads (Note 8)	R <sub>θJL</sub>	194	°C/W
Operating and Storage Temperature Range	T <sub>J</sub> , T <sub>STG</sub>	-55 to +150	°C

- Notes:
- For a device surface mounted on 25mm x 25mm FR4 PCB with high coverage of single sided 1oz copper, in still air conditions
  - For a device surface mounted on FR4 PCB measured at t ≤ 5 secs.
  - Repetitive rating 25mm x 25mm FR4 PCB, D=0.05 pulse width=10μs - pulse current limited by maximum junction temperature.
  - Thermal resistance from junction to solder-point (at the end of the drain lead).

## Thermal Characteristics

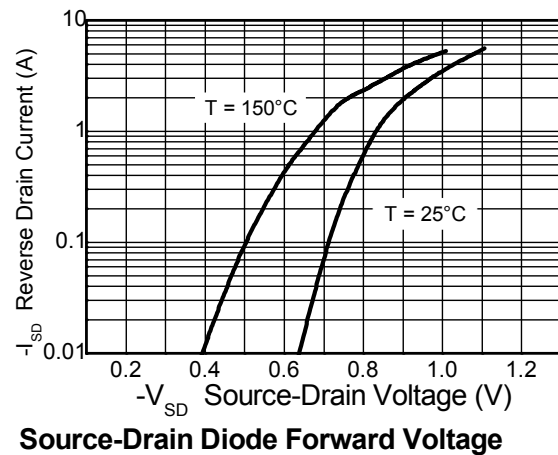
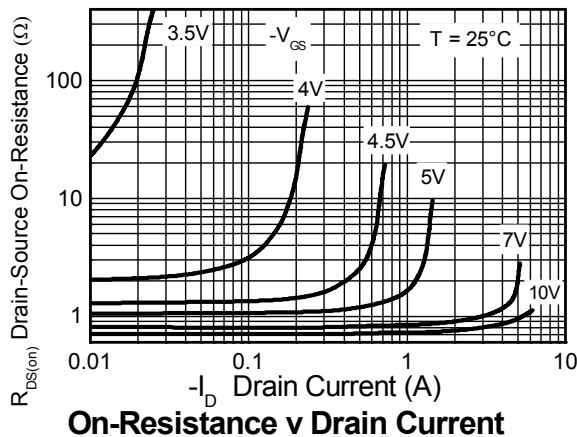
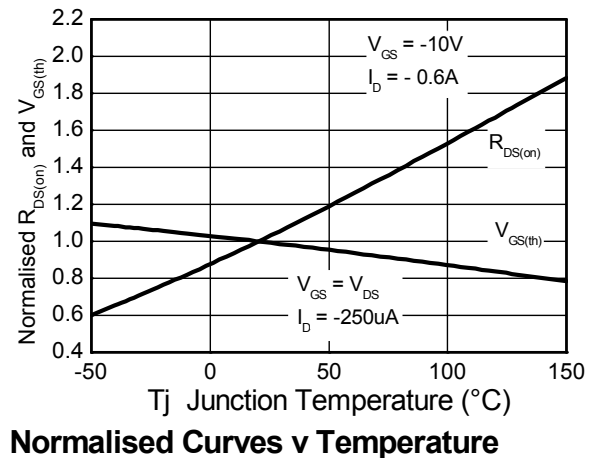
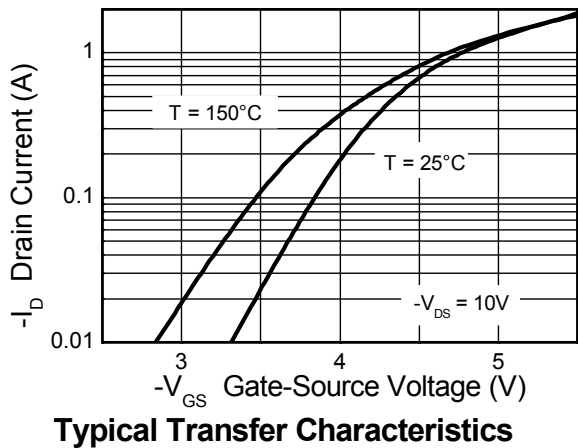
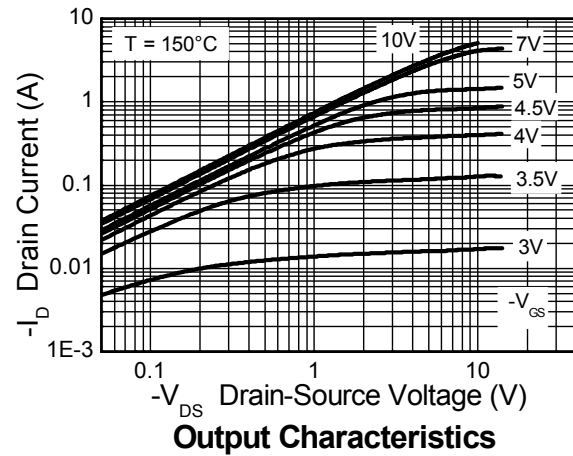
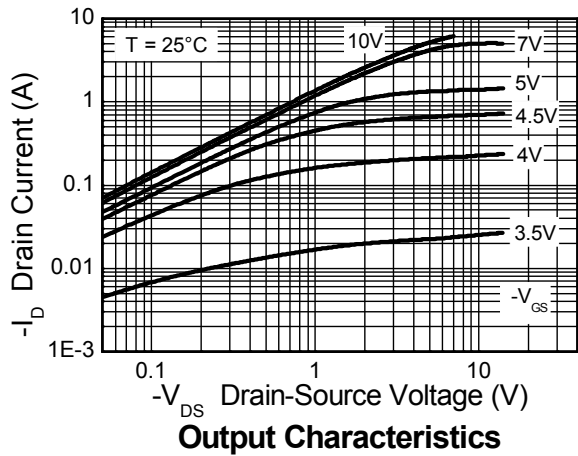


**ZXMP10A13F**
**Electrical Characteristics** (@T<sub>A</sub> = +25°C, unless otherwise specified.)

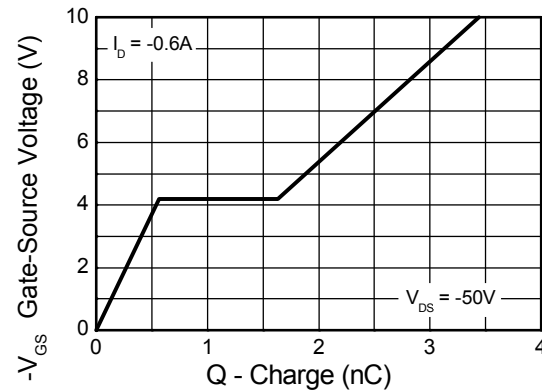
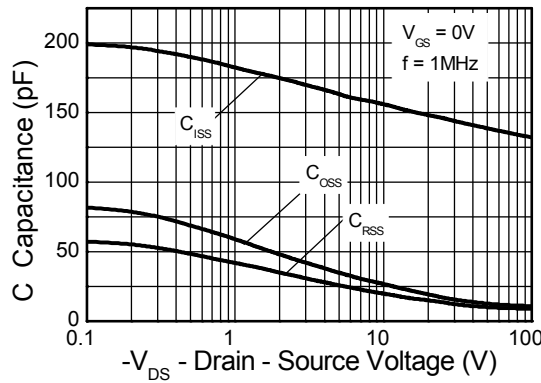
Characteristic	Symbol	Min	Typ	Max	Unit	Test Condition
OFF CHARACTERISTICS						
Drain-Source Breakdown Voltage	BV <sub>DSS</sub>	-100	—	—	V	I <sub>D</sub> = -250μA, V <sub>GS</sub> = 0V
Zero Gate Voltage Drain Current	I <sub>DSS</sub>	—	—	-1.0	μA	V <sub>DS</sub> = -100V, V <sub>GS</sub> = 0V
Gate-Source Leakage	I <sub>GSS</sub>	—	—	±100	nA	V <sub>GS</sub> = ±20V, V <sub>DS</sub> = 0V
ON CHARACTERISTICS						
Gate Threshold Voltage	V <sub>GS(th)</sub>	-2.0	—	-4.0	V	I <sub>D</sub> = -250μA, V <sub>DS</sub> = V <sub>GS</sub>
Static Drain-Source On-Resistance (Note 9)	R <sub>DS (on)</sub>	—	—	1.0	Ω	V <sub>GS</sub> = -10V, I <sub>D</sub> = -0.6A
				1.45		V <sub>GS</sub> = -6.0V, I <sub>D</sub> = -0.5A
Forward Transconductance (Notes 9 and 11)	g <sub>fs</sub>	—	1.2	—	S	V <sub>DS</sub> = -15V, I <sub>D</sub> = -0.6A
Diode Forward Voltage (Note 9)	V <sub>SD</sub>	—	-0.85	-0.95	V	T <sub>J</sub> = 25°C, I <sub>S</sub> = -0.75A, V <sub>GS</sub> = 0V
Reverse Recovery Time (Note 11)	t <sub>rr</sub>	—	29	—	ns	T <sub>J</sub> = 25°C, I <sub>F</sub> = -0.9A, di/dt = 100A/μs
Reverse Recovery Charge (Note 11)	Q <sub>rr</sub>	—	31	—	nC	
DYNAMIC CHARACTERISTICS (Note 11)						
Input Capacitance	C <sub>iss</sub>	—	141	—	pF	V <sub>DS</sub> = -50V, V <sub>GS</sub> = 0V f = 1.0MHz
Output Capacitance	C <sub>oss</sub>	—	13.1	—		
Reverse Transfer Capacitance	C <sub>rss</sub>	—	10.8	—		
Turn-On Delay Time (Note 10)	t <sub>D(on)</sub>	—	1.6	—	ns	V <sub>DD</sub> = -50V, I <sub>D</sub> = -1.0A, R <sub>G</sub> ≅ 6.0Ω, V <sub>GS</sub> = -10V
Turn-On Rise Time (Note 10)	t <sub>r</sub>	—	2.1	—		
Turn-Off Delay Time (Note 10)	t <sub>D(off)</sub>	—	5.9	—		
Turn-Off Fall Time (Note 10)	t <sub>f</sub>	—	3.3	—		
Total Gate Charge (Note 10)	Q <sub>g</sub>	—	1.8	—	nC	V <sub>DS</sub> = -50V, V <sub>GS</sub> = -5.0V, I <sub>D</sub> = -0.6A
Total Gate Charge (Note 10)	Q <sub>g</sub>	—	3.5	—	nC	V <sub>DS</sub> = -50V, V <sub>GS</sub> = -10V, I <sub>D</sub> = -0.6A
Gate-Source Charge (Note 10)	Q <sub>gs</sub>	—	0.6	—		
Gate-Drain Charge (Note 10)	Q <sub>gd</sub>	—	1.6	—		

Notes: 9. Measured under pulsed conditions. Pulse width = 300μs. Duty cycle ≤ 2%.  
10. Switching characteristics are independent of operating junction temperature.  
11. For design aid only, not subject to production testing.

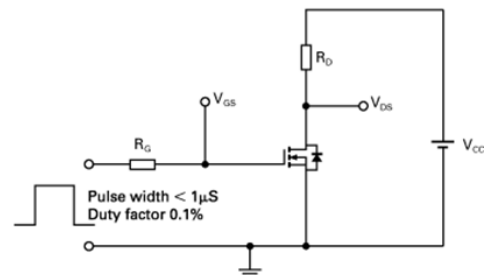
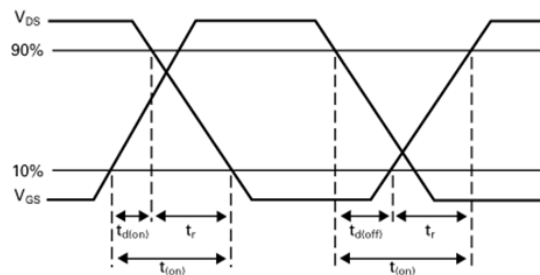
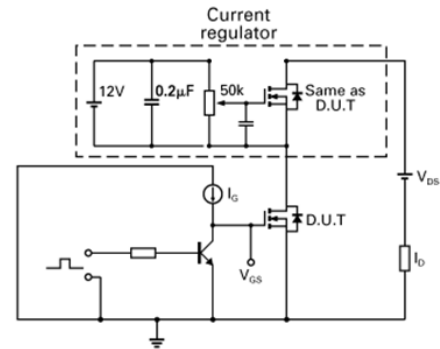
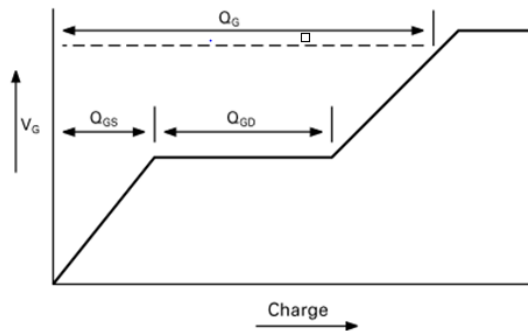
## Typical Characteristics



## Typical Characteristics (cont.)

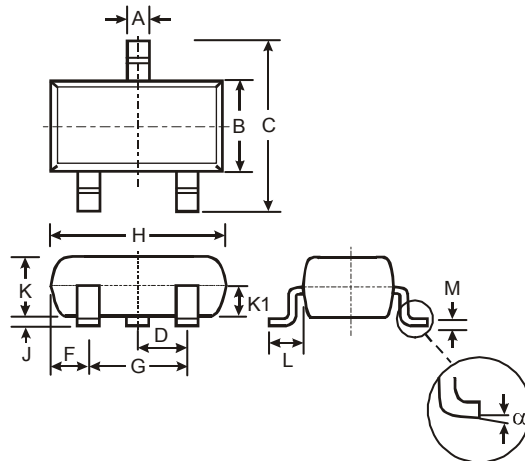


## Test Circuits



## Package Outline Dimensions

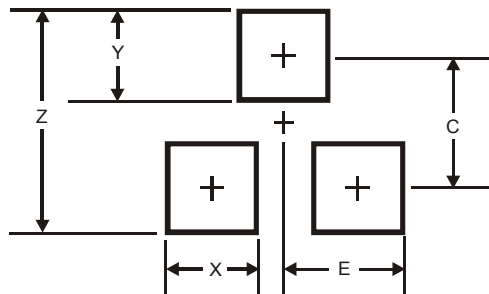
Please see AP02002 at <http://www.diodes.com/datasheets/ap02002.pdf> for latest version.



SOT23			
Dim	Min	Max	Typ
A	0.37	0.51	0.40
B	1.20	1.40	1.30
C	2.30	2.50	2.40
D	0.89	1.03	0.915
F	0.45	0.60	0.535
G	1.78	2.05	1.83
H	2.80	3.00	2.90
J	0.013	0.10	0.05
K	0.903	1.10	1.00
K1	-	-	0.400
L	0.45	0.61	0.55
M	0.085	0.18	0.11
α	0°	8°	-
All Dimensions in mm			

## Suggested Pad Layout

Please see AP02001 at <http://www.diodes.com/datasheets/ap02001.pdf> for the latest version.



Dimensions	Value (in mm)
Z	2.9
X	0.8
Y	0.9
C	2.0
E	1.35

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