Cogent Computer Systems, Inc.

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"ALWAYS COMPLETE"

CSB1880 - MPC8536E Computer On a Module (COM)

The CSB1880, designed, developed and manufactured by Cogent Computer Systems, Inc., is a high performance, network oriented, PowerPC based Computer on a Module (COM). The CSB1880 provides a small, powerful and flexible engine for embedded Linux based 10/100/1000 networking applications of all kinds.
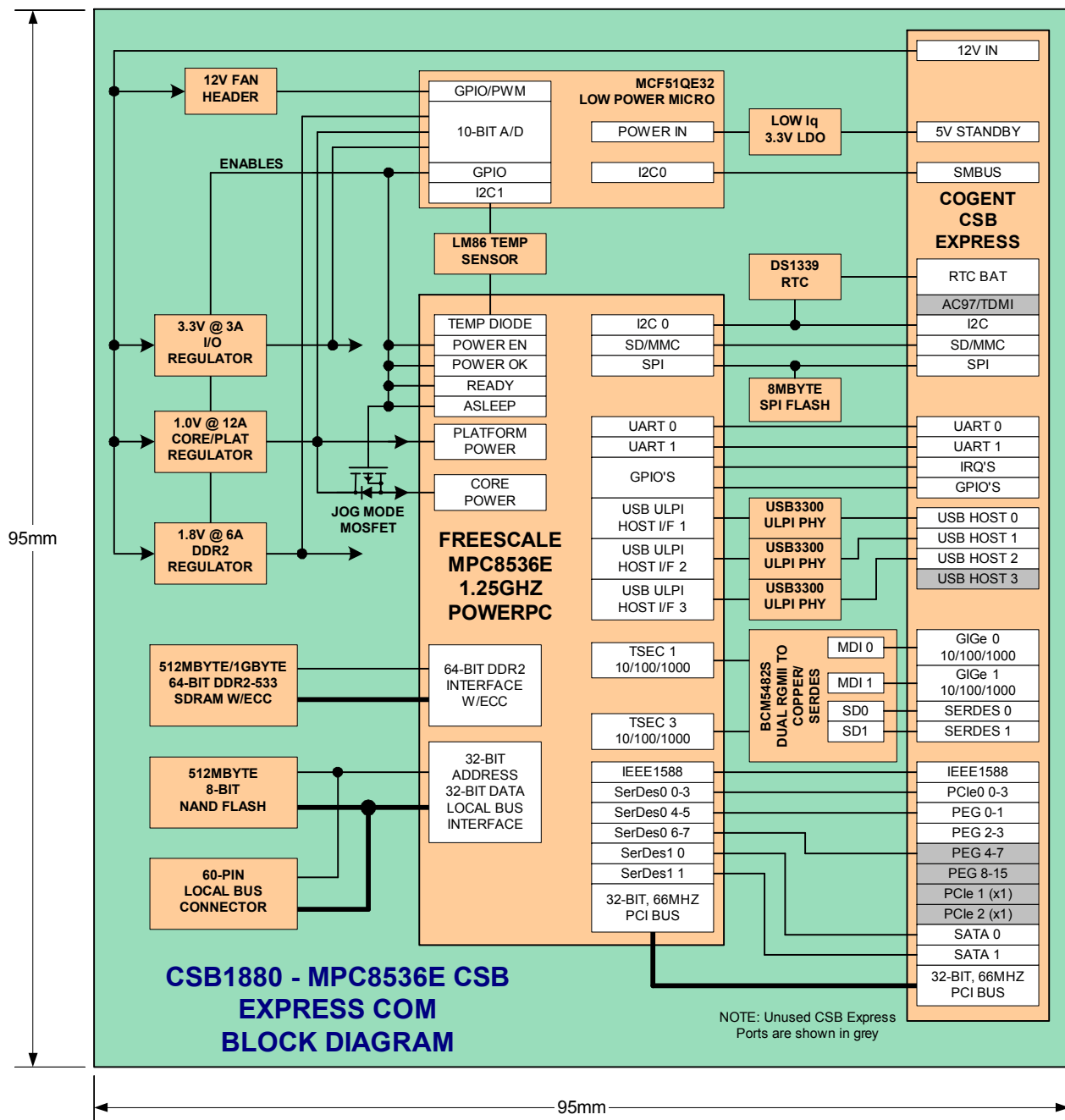
Specifications and Features

- **CPU -** 1.25Ghz e500v2 PowerPC Core
- **CACHE -** 32KByte Instruction and Data Caches, plus On-Chip 512KByte L2 Cache
- **FPU -** IEEE 754 Compliant Floating Point Unit and Vector Coprocessor Unit
- **SDRAM -** 512MByte (1GByte Option) 64-Bit Wide DDR2-533 Memory with ECC
- **NAND FLASH -** On-Board 512MByte NAND
- **SD/MMC** - 4-Bit SD/MMC Port, Supports up to 2GB Cards (socket is off board)
- **PCI Express -** One x4 Link and Two x2 Links (may also be configured as one x8, or two x4's)
- **PCI BUS -** 32-Bit, 66Mhz V2.2 with 4 Bus Request/Grant Pairs
- **GIGABIT ETHERNET -** Two Copper or SGMII (Auto selected via PHY) 10/100/1000 Ports
- **SECURITY -** On-Chip Offload Engines Support Various Encryption/Decryption Algorithms including: IPsec/SSL; DES/3DES; MD-5 and SHA1 hashing; Kasumi f8 and f9; and others
- **XOR/RAID -** High Speed XOR DMA Engine for RAID Storage Applications
- **Serial Attached Storage (SATA) -** Dual SATA Gen 2 (1.5Gbit or 3Gbit/sec) Channels
  USB - Three 480Mbit USB 2.0 Host Ports via Low Power ULPI PHY's
- **SERIAL I/O -** Two 4-wire TTL Serial Ports and One I2C Port
- **GPIO -** Eight GPIO lines and Four PCI Interrupts
- **JTAG -** Standard PowerPC 16-Pin JTAG Header
- **POWER MANAGEMENT -** 12V Input via Board Mounting Holes; On-Board 3.3V (I/O), 1.8V (SDRAM), 1.0V (CPU Core); and On-Board MCF51QE32 IPM Micro for Power Sequencing, Boot Configuration, FAN Control and Thermal Monitoring
- **OPERATING TEMPERATURE -** 0C to 70C Operation with Supplied Passive Heatsink
- **OPERATING POWER -** <8W typ., <12W Max and <10mw Power Down (via 3.3V Standby rail)
- **CSB Express Compliant -** Based on COM Express Type III with Cogent Enhancements
- **Compact Size -** 95mm (3.75") x 95mm (3.75") x 9mm high (Mounted Height without Heatsink)
- **OS Support -** Linux 2.6.25+ and Android 1.5 Cupcake

Introduction and Overview

The high-performance Superscalar e500 Power Architecture Core, multiple PCI Express links, high speed USB, Dual Serial ATA; Dual 10/100/1000 Ethernet, and highly efficient on-board regulators all combine to make the CSB1880 the ideal engine for size restricted, high performance, Gigabit Ethernet based applications. For the lowest power consumption, the IPM Micro can power the board down completely while remaining on standby power. In this mode the consumption drops to <10milliwatts with IPM bus wakeup events still active. The IPM Micro also controls power sequencing; thermal and voltage monitoring; FAN Control; and boot configuration.

The CSB1880 is constructed using state of the art PCB layout and packaging technology such as: 533Mhz DDR2 balanced tree routing; 3GHZ+ high-speed differential signaling; 15 Amp peak switching regulators; and 8-layer, low EMI, impedance controlled PCB stackup. The CSB1880 gives you access to this technology without the learning curve or the risk. You can integrate the CSB1880 using a simple, low cost 4 or 6-layer PCB, in just weeks, not months! We can even do it for you through our custom design services group.

CSB1880 - MPC8536E CSB EXPRESS COM BLOCK DIAGRAM

95mm × 95mm

12V FAN HEADER

MCF51QE32 LOW POWER MICRO
- GPIO/PWM
- 10-BIT A/D
- GPIO
- I2C1
- POWER IN
- I2C0

ENABLES

LOW Iq 3.3V LDO

COGENT CSB EXPRESS
- 12V IN
- 5V STANDBY
- SMBUS

LM86 TEMP SENSOR

DS1339 RTC
- RTC BAT
- AC97/TDMI
- I2C
- SD/MMC
- SPI

3.3V @ 3A I/O REGULATOR

1.0V @ 12A CORE/PLAT REGULATOR

1.8V @ 6A DDR2 REGULATOR

JOG MODE MOSFET

FREESCALE MPC8536E 1.25GHZ POWERPC
- TEMP DIODE
- POWER EN
- POWER OK
- READY
- ASLEEP
- PLATFORM POWER
- CORE POWER
- I2C 0
- SD/MMC
- SPI
- UART 0
- UART 1
- GPIO'S
- USB ULPI HOST I/F 1
- USB ULPI HOST I/F 2
- USB ULPI HOST I/F 3
- TSEC 1 10/100/1000
- TSEC 3 10/100/1000
- IEEE1588
- SerDes0 0-3
- SerDes0 4-5
- SerDes0 6-7
- SerDes1 0
- SerDes1 1
- 32-BIT, 66MHZ PCI BUS

8MBYTE SPI FLASH

USB3300 ULPI PHY (×3)

BCM5482S DUAL RGMII TO COPPER/ SERDES
- MDI 0
- MDI 1
- SD0
- SD1

512MBYTE/1GBYTE 64-BIT DDR2-533 SDRAM W/ECC → 64-BIT DDR2 INTERFACE W/ECC

512MBYTE 8-BIT NAND FLASH

60-PIN LOCAL BUS CONNECTOR

32-BIT ADDRESS 32-BIT DATA LOCAL BUS INTERFACE

Cogent CSB Express ports:
- UART 0
- UART 1
- IRQ'S
- GPIO'S
- USB HOST 0
- USB HOST 1
- USB HOST 2
- USB HOST 3
- GIGe 0 10/100/1000
- GIGe 1 10/100/1000
- SERDES 0
- SERDES 1
- IEEE1588
- PCIe0 0-3
- PEG 0-1
- PEG 2-3
- PEG 4-7
- PEG 8-15
- PCIe 1 (x1)
- PCIe 2 (x1)
- SATA 0
- SATA 1
- 32-BIT, 66MHZ PCI BUS

NOTE: Unused CSB Express Ports are shown in grey

Development Boards and I/O Expansion

The CSB1880 is fully compatible with the CSB1801 Flex-ATX Development Platform. This platform provides a CSB Express Compatible Socket, Dual RS-232 COM Ports, SDIO Socket, Dual 10/100/1000 Ethernet; UXGA (1280 x 1024) Video; Dual SATA I/F and 4 USB Host Ports to facilitate self-hosted Linux development. This platform also provides Three PCIe Slots (lanes per socket are CPU specific), One 3.3V PCI Slot and optional headers for CPU Bus and I/O expansion. The CSB1801 and CSB1880 are installed in a compact (12.7" x 11" x 5.5"), Micro-ATX Case with 300W Power Supply. Contact Cogent for more detailed information about the CSB1801 and the KIT1880 Development Kit.