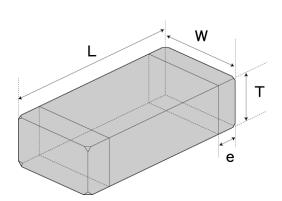
Spec Sheet

Super Low Distortion Multilayer Ceramic Capacitors (CFCAP(TM))

UMK107SD222KA-T



Features

- Item Summary 50V, 0.0022 μ F, $\pm 10\%$, 0603
- Lifecycle Stage
 - Mass Production
- Standard packaging quantity (minimum)
 Taping paper 4000pcs

Products characteristics table

CaseSize (EIA/JIS)	0603/1608
Rated Voltage (max)	50V
Capacitance	0.0022 µF
Capacitance Tolerance	±10%
Resistance	10,000 MΩ min.
Dielectric Loss Tangent	0.1%MAX
Temperature Range (EIA/JIS)	-55 to +125℃/-55 to +125℃
RoHS Compliance	Yes
Halogen Free	Yes
Soldering Method	Reflow

■ External Dimensions

L	1.6mm ±0.10
W	0.8mm ±0.10
Т	0.8mm ±0.10
е	0.35mm ±0.25

The data is reference only. Electrical characteristics vary depending on environment or measurement condition. TAIYO YUDEN reserves the right to make change to the Date at any time without notice. Before making final selection, please check product specification.