

TSM1285 Demo Board

A 300ksps, Single-supply, 12-Bit Serial-output ADC

FEATURES

- Quick and easy Interface to computer for evaluation via Touchstone Viperboard and USB cable
- Input BNC connection
- On-board +3.3V supply voltage
- > External power supply connection available
- Fully Assembled and Tested
- 2in x 3in 2-layer circuit board



. Figure 1. TSM1285 Evaluation Board with Viperboard



Figure 2. TSM1285 Evaluation Board (Top View)

DESCRIPTION

The demo board for the TSM1285 is a completely assembled and tested circuit board that can be used for evaluating the TSM1285. For easy and quick evaluation of the TSM1285, a Touchstone viperboard with an on-board microcontroller and FPGA for signal processing is available. The viperboard interfaces to any computer via USB port and a user-friendly Windows OS compatible software is available for evaluation.

The TSM1285 – a single-supply, single-channel, 12bit analog-to-digital converter (ADC) - is an alternate source for the MAX1285 and a higher-speed upgrade to the MAX1240 and MAX1240 ADCs. The TSM1285 combines a high-bandwidth track-and-hold (T/H), a high-speed serial digital interface, an internal +2.5V reference, and low conversion-mode power consumption. The TSM1285 operates from a single +2.7V to+3.6V supply and draws less than 2.5mA at 300ksps.

Product datasheet and additional documentation can be found on the factory web site at <u>www.touchstonesemi.com</u>.

ORDERING INFORMATION

Order Number	Description
TSM1285DB	TSM1285
	Demo Board
TSDA-VB	Touchstone Viperboard



Figure 3. TSM1285 Evaluation Board (Bottom View)

Description

The TSM1285 demo board provides an easy-to-use scheme for evaluating the TSM1285. The default configuration of the demo board in conjunction with the viperboard is for operation at a supply voltage of V_{DD} =+3.3V.

The TSM1285 demo board includes a 74ALVC164245 signal level translator in order to translate signal levels from V_{DD} of the TSM1285 to +3.3V signal voltage levels necessary for the viperboard. The TSM1285 and the viperboard are connected via an on-board 34-pin ribbon socket (X1).

A separate power supply can be used to power the TSM1285 from +2.7V to +3.6V. If evaluating the TSM1285 demo board without the viperboard, a single power supply is needed. A BNC connector is available for the input of the TSM1285, AIN, and it is recommended to use a low-noise signal generator in order to acquire optimal SNR and THD results. Refer to Table 1. Figure 6 shows the TSM1285 demo board schematic, Figure 2 and Figure 3 show a picture of the top and bottom of the TSM1285 demo board, respectively. Figure 1 shows a picture of the TSM1285 demo board with the viperboard connected.

A USB cable is used to interface the viperboard to a PC with Windows Vista 32-bit/Vista 64-bit/XP/7. For the "Touchstone ADC Evaluation Platform GUI" user's manual and for the necessary driver and GUI installer files needed to successfully evaluate the TSM1285 with the viperboard, please refer to our website at www.Touchstonesemi.com.

Quick Start Procedure

Required Equipment

- TSM1285 Demo Board
- Touchstone Viperboard
- USB cable
- Computer with Windows Vista 32-bit/64-bit/XP/7
- Viperboard driver and GUI software installed to PC(refer to "Touchstone ADC Evaluation Platform GUI" user's manual)
- Low-noise Signal Generator Stanford Research Systems Model DS360 or better w/ BNC cable

In order to evaluate the TSM1285, jumper J2 is to be set to position 1-2. The default configuration for J2 is



1-2. Refer to Figure 6 and Table 1. The default configuration on the board is for $V_{DD} = +3.3V$. The following steps are to be performed:

- 1) Connect the TSM1285 demo board to the viperboard via the 34-pin socket (X1).
- Set the function generator frequency, amplitude, and offset to 70kHz, 2.499V_{PP}, and 1.25V, respectively. Then, connect the BNC cable from the signal generator to the BNC connector AIN on the TSM1285 demo board. Set the output of the signal generator to OFF.
- 3) To run the GUI, go to Start > All Programs > TouchStoneEvalBoard 2.2.10 > 01 Touchstone Evaluation App. The GUI should appear and on the top right hand side, the status should be "PC disconnected" and "No Type" for demo board type. Both should be highlighted in red indicating the viperboard and the TSM1285 demo board is not connected.
- 4) Connect one side of the supplied USB cable to the viperboard USB connector. Connect the other side to a USB port on the PC. After approximately three seconds, the status on the GUI screen will switch to "PC Connected" and "TSM1285" indicating the viperboard and the TSM1285 demo board were recognized successfully by the software. The green LED on the TSM1285 demo board should be ON and the green LED on the viperboard, D2, should be ON. Use the default sampling rate of 300kSPS and an FFT length of 8192.
- 5) Turn on the signal generator. On the GUI screen, click on "continuous" in the sampling section, and monitor the results on the screen. The top plot shows a samples vs code plot and the bottom plot is an FFT plot. The SNR and THD values should be approximately 70dB and -80dB, respectively. Please refer to Figure 4 for an example of what the results should look like. In this case, the SNR and THD values are 69dB and -85dB, respectively.

Additional Functions

6) As shown in the TSM1285 ADC section, a shutdown control function is available. If the "shutdown" button is selected when sampling in continuous mode, the TSM1285 will be in shutdown mode and no additional conversions will occur. To de-assert the shutdown function, press the button again and the conversions will



resume. Figure 5 shows what the GUI looks like when the shutdown control function is enabled.

- 7) A shutdown between conversions function is available by selecting the "Shutdown between conversions" box. Refer to Figure 4. When performing a continuous conversion, press the "shutdown" button and then select the "Shutdown between conversions" box. The samples vs code and the FFT plot will update after every conversion.
- 8) To perform a single conversion, press the "single" button in the sampling section.
- 9) For details on the Touchstone evaluation platform GUI, refer to the "Touchstone ADC Evaluation Platform GUI" user's manual on our website at: www.Touchstonesemi.com.



Figure 4. TSM1285 Response with GUI





Figure 5. TSM1285 Shutdown Control Enabled



Evaluation with TSM1285 Demo Board Only

Required Equipment

- TSM1285 Demo Board
- DC Power Supply
- Low-noise Signal Generator Stanford Research Systems Model DS360 or better w/ BNC cable
- > Ammeter

In order to evaluate the TSM1285 using the TSM1285 demo board only, additional input signals for the \overline{CS} SCLK and \overline{SHDN} pins will be necessary along with an oscilloscope to monitor the inputs and output. The following steps are to be performed:

 Set the voltage of the power supply between +2.7V and +3.6V and turn it off. Connect the positive terminal of the DC power supply to pin 12 labeled as "3.3V" on X1, and the ground terminal to pin 2 labeled as "GND". Refer to Figure 6.

- 2) Timing signals for the TSM1285 input signals AIN, CS, SHDN, and SCLK should be set according to the desired measurement based while following the product datasheet specifications. Refer to the TSM1285 product datasheet "Description of Operation" for details. Access to these pins is available through the 34pin socket, X1, labeled as "SCLK", "CS", "SHDN", and "DOUT". The corresponding pins on X1 are 10, 8, 4, and 6, respectively.
- 3) To measure the supply current of the TSM1285, place jumper J2 to position 2-3. Then, apply an external power supply to J1. An ammeter can now be connected in series with the power supply for a supply current measurement of the TSM1285. The supply current should be less than 650µA. Please refer to Figure 6 and table 1.



Figure 6. TSM1285 Evaluation Board Circuit



	J1	J2	X1	External Power Supplies
TSM1285DB w/	NA	1-2 (V _{DD} = 3.3V only)	24 pip socket	0
Viperboard	+2.7V to +3.6V	2-3	54-pin socket	1
TSM1285 Only	NA	1-2	Apply +2.7 to +3.6V to pin 12	1
TSM1285 Only (Supply Current Measurement)	+2.7V to +3.6V	2-3	NA	1

Table 1. J1, J2, and X1 connections

DESIGNATION	QTY	DESCRIPTION
C4, C10, C11	3	0.1µF ±10%
		capacitor (0603)
C12, C13,	2	10µF ±10%
		capacitor
		(SMD6032)
C5, C6	2	4.7µF ±10%
		capacitor
		(SMD6032)
C2	1	100pF ±10%
		capacitor (0603)
R16	1	150Ω± 1% (0603)
R15	1	1kΩ± 1% (0402)
LED	1	Green LED (0603)
AIN	1	BNC connector
J1	1	2-pin terminal
		block
X1	1	34-pin socket
U2	1	74ALVC164245
		translating
		transceiver
J2	1	Jumper
U3	1	TSM1285

Table 2. Component list





Figure 6. Top Layer Component View



Figure 7. Top Layer Trace View



Figure 8. Bottom Layer #1



Figure 9. Bottom Layer #2