TOSHIBA Field Effect Transistor Silicon N Channel MOS Type (DTMOS II)

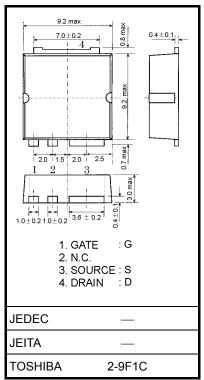
TK12X60U

Switching Regulator Applications

- Low drain-source ON resistance: $RDS(ON) = 0.36 \Omega$ (typ.)
- High forward transfer admittance: $|Y_{fs}| = 7.0 \text{ S}$ (typ.)
- Low leakage current: $I_{DSS} = 100 \ \mu A (max) (V_{DS} = 600 \ V)$
- Enhancement-mode: $V_{th} = 3.0 \sim 5.0 \text{ V} (V_{DS} = 10 \text{ V}, \text{ID} = 1 \text{ mA})$

Absolute Maximum Ratings (Ta = 25°C)

Characteristics		Symbol	Rating	Unit	
Drain-source voltage		V _{DSS}	600	V	
Gate-source voltage		V _{GSS}	±30	V	
Drain current	DC (Note 1)	۱ _D	12		
	Pulse (t = 1 ms) (Note 1)	I _{DP}	24	A	
Drain power dissipati	on (Tc = 25°C)	PD	100	W	
Single pulse avalanche energy (Note 2)		E _{AS}	69	mJ	
Avalanche current		I _{AR}	8	А	
Repetitive avalanche energy (Note 3)		E _{AR}	10	mJ	
Channel temperature		T _{ch}	150	°C	
Storage temperature range		T _{stg}	-50 to 150	°C	



Weight : 0.74 g (typ.)

Note: Using continuously under heavy loads (e.g. the application of high temperature/current/voltage and the significant change in temperature, etc.) may cause this product to decrease in the reliability significantly even if the operating conditions (i.e. operating temperature/current/voltage, etc.) are within the absolute maximum ratings. Please design the appropriate reliability upon reviewing the Toshiba Semiconductor Reliability Handbook ("Handling Precautions"/ "Derating Concept and Methods") and individual reliability data (i.e. reliability test report and estimated failure rate, etc).

Thermal Characteristics

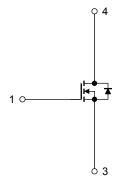
Characteristics	Symbol	Мах	Unit
Thermal resistance, channel to case	R _{th (ch-c)}	1.25	°C/W

Note 1: Please use devices on conditions that the channel temperature is below 150°C.

Note 2: V_{DD} = 90 V, T_{ch} = 25°C (initial), L = 1.89 mH, R_G = 25 Ω , I_{AR} = 8 A

Note 3: Repetitive rating: pulse width limited by maximum channel temperature

This transistor is an electrostatic sensitive device. Please handle with caution.



Unit: mm

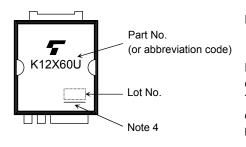
Electrical Characteristics (Ta = 25°C)

Char	acteristics	Symbol	Test Condition	Min	Тур.	Max	Unit
Gate leakage current		I _{GSS}	$V_{GS}=\pm 30~V,~V_{DS}=0~V$			±1	μA
Drain cut-off curr	rent	I _{DSS}	$V_{DS} = 600 \text{ V}, \text{ V}_{GS} = 0 \text{ V}$			100	μA
Drain-source bre	akdown voltage	V (BR) DSS	$I_D = 10 \text{ mA}, V_{GS} = 0 \text{ V}$	600		_	V
Gate threshold v	oltage	V _{th}	$V_{DS} = 10 \text{ V}, \text{ I}_{D} = 1 \text{ mA}$	3.0		5.0	V
Drain-source ON	resistance	R _{DS (ON)}	$V_{GS} = 10 \text{ V}, \text{ I}_{D} = 6 \text{ A}$		0.36	0.4	Ω
Forward transfer	admittance	Y _{fs}	$V_{DS} = 10 \text{ V}, \text{ I}_{D} = 6 \text{ A}$	2.0	7.0	_	S
Input capacitance		C _{iss}			720	_	
Reverse transfer capacitance		C _{rss}	$V_{DS} = 10 \text{ V}, \text{ V}_{GS} = 0 \text{ V}, \text{ f} = 1 \text{ MHz}$		55		pF
Output capacitance		C _{oss}			1700		
Switching time	Rise time	tr	$I_D = 6 \text{ A } V_{OUT}$	_	30		
	Turn-on time	t _{on}	$0 \vee - \downarrow \leftarrow \downarrow$		60	_	ns
	Fall time	t _f	} // // V _{DD} ≈ 300 V		8	_	
	Turn-off time	t _{off}	Duty \leq 1%, t _w = 10 μ s	_	75	—	
Total gate charge		Qg			14		
Gate-source charge		Q _{gs}	$V_{DD}\approx 400~V,~V_{GS}=10~V,~I_{D}=12~A$		8.5		nC
Gate-drain charge		Q _{gd}	1		5.5		

Source-Drain Ratings and Characteristics (Ta = 25°C)

Characteristics	Symbol	Test Condition	Min	Тур.	Max	Unit
Continuous drain reverse current (Note 1)	I _{DR}	—	_	_	12	А
Pulse drain reverse current (Note 1)	I _{DRP}	—	_	_	24	А
Forward voltage (diode)	V _{DSF}	$I_{DR} = 12 \text{ A}, V_{GS} = 0 \text{ V}$	_	_	-1.7	V
Reverse recovery time	t _{rr}	$I_{DR} = 12 \text{ A}, V_{GS} = 0 \text{ V},$	_	380	_	ns
Reverse recovery charge	Q _{rr}	dl _{DR} /dt = 100 A/μs	_	5.3		μC

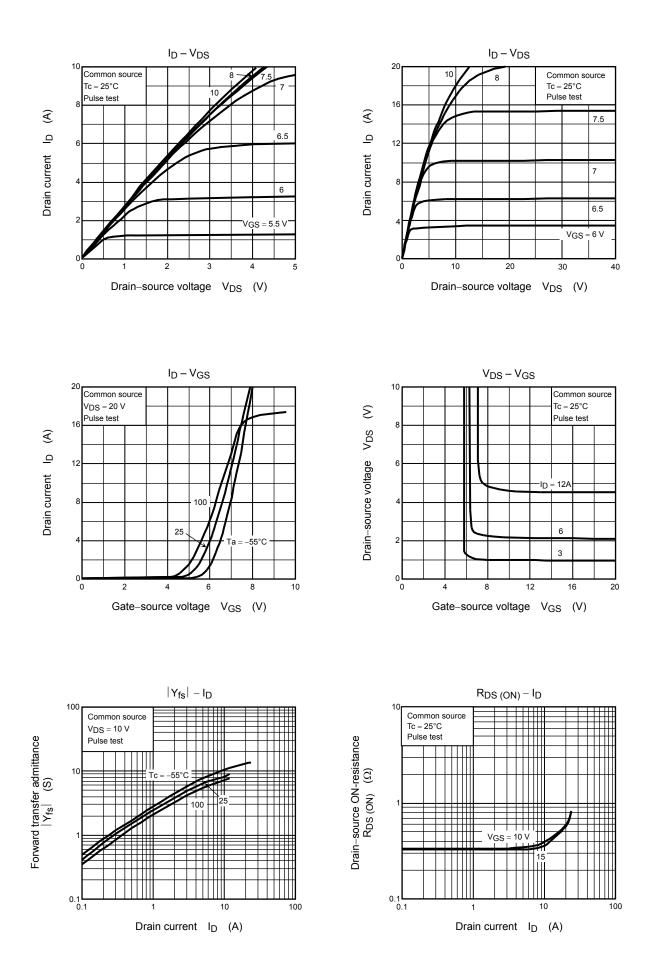
Marking



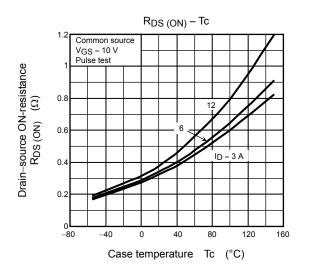
Note 4 : A line under a Lot No. identifies the indication of product Labels [[G]]/RoHS COMPATIBLE or [[G]]/RoHS [[Pb]]

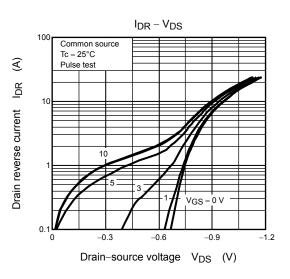
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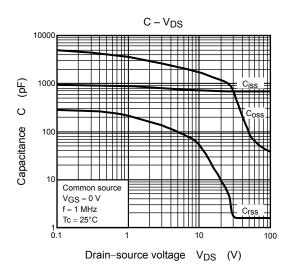
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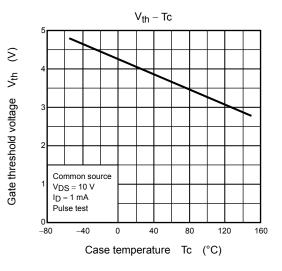


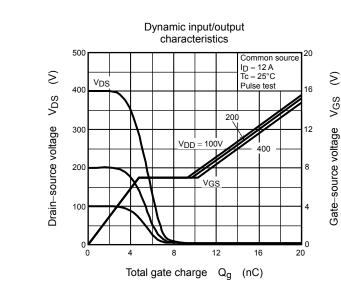
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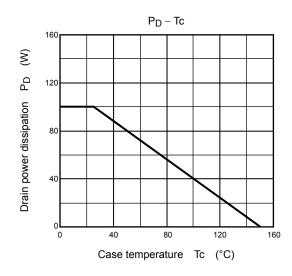


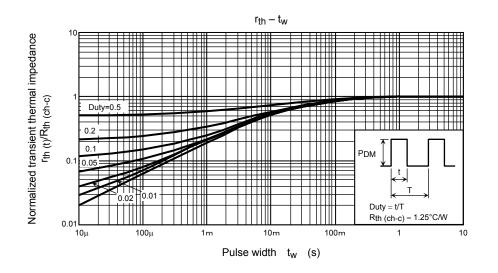


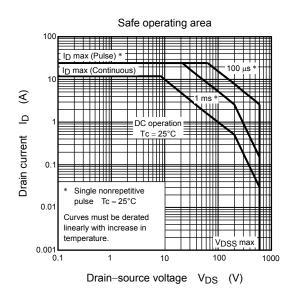


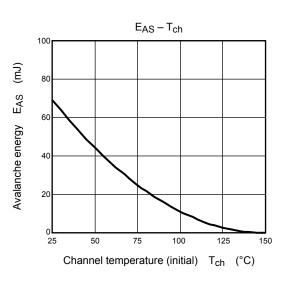


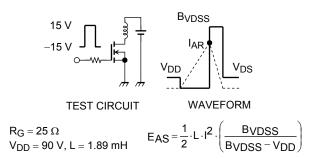












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