

6.0A SBR® **SUPER BARRIER RECTIFIER** POWERDI®5

Product Summary

V _{RRM} (V)	I _O (A)	TRR Typ (nS)	VF max (V)	I _{R max} (mA)
400	6	120	0.91	0.05

Description and Applications

The SBR6U400P5 uses patented SBR technology which offers ultra low VF, excellent high temperature stability and soft switching characteristics for reduced EMI.

Packaged in the compact patented PowerDI-5 package, this product also offers excellent thermal efficiency and high surge current handling capability.

- DC DC Converters
- High Frequency Rectification
- **Telecom Power Supply**

Features and Benefits

- Ultra Low Forward Voltage Drop
- **Excellent High Temperature Stability**
- Patented Interlocking Clip Design for High Surge Current Capacity
- Patented Super Barrier Rectifier Technology
- Soft, Fast Switching Capability
- 175°C Operating Junction Temperature
- Lead Free Finish, RoHS Compliant (Note 1)
 - "Green" Molding Compound (No Br, Sb)

Mechanical Data

- Case: POWERDI®5
- Case Material: Molded Plastic, UL Flammability Classification Rating 94V-0
- Moisture Sensitivity: Level 1 per J-STD-020
- Terminals: Finish Matte Tin annealed over Copper leadframe. Solderable per MIL-STD-202, Method 208 @3
- Weight: 0.093grams (approximate)

POWERDI®5



Top View



LEFT PIN O **BOTTOMSIDE** HEAT SINK RIGHT PIN O

Note: Pins Left & Right must be electrically connected at the printed circuit board.

Ordering Information (Note 2)

Part Number	Case	Packaging
SBR6U400P5-13	POWERDI [®] 5	5000/Tape & Reel

Notes:

- 1. EU Directive 2002/95/EC (RoHS). All applicable RoHS exemptions applied, see EU Directive 2002/95/EC Annex Notes.
- 2. For packaging details, go to our website at http://www.diodes.com.

Marking Information



S6U400 = Product Type Marking Code Olli= Manufacturers' code marking K = Factory designator YYWW = Date Code Marking YY = Last two digits of year (ex: 10 for 2010) WW = Week code (01 to 53)



Maximum Ratings @TA = 25°C unless otherwise specified

Single phase, half wave, 60 Hz, resistive or inductive load.

For capacitance load, derate current by 20%.

Characteristic	Symbol	Value	Unit
Peak Repetitive Reverse Voltage Working Peak Reverse Voltage DC Blocking Voltage	V _{RRM} V _{RWM} V _{RM}	400	٧
Average Rectified Output Current (See Figure 1)	I _O	6	Α
Non-Repetitive Peak Forward Surge Current 8.3ms Single Half Sine-Wave Superimposed on Rated Load	I _{FSM}	220	Α

Thermal Characteristics

Characteristic	Symbol	Value	Unit
Maximum Thermal Resistance Junction to Ambient (Note 3)	$R_{\Theta JA}$	30	°C/W
Maximum Thermal Resistance Junction to Case (Note 4)	R _{eJC}	2.5	°C/W
Operating and Storage Temperature Range	T _J , T _{STG}	-65 to +175	°C

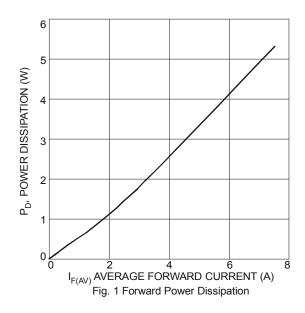
Electrical Characteristics @TA = 25°C unless otherwise specified

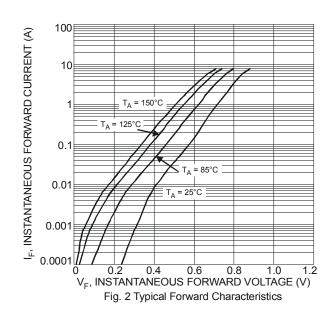
Characteristic	Symbol	Min	Тур	Max	Unit	Test Condition
		-	0.83	-		I _F = 5A, T _J = 25°C
Forward Voltage Drop	V_{F}	-	0.85	0.91	\/	I _F = 6A, T _J = 25°C
		-	0.72	0.77	v	I _F = 6A, T _J = 125°C
Leakage Current (Note 5)		-	0.8	50	μA	V _R = 400V, T _J = 25 °C
Leakage Current (Note 5)	IR	-	-	5	mA	V _R = 400V, T _J = 125 °C
	+		120		nS	$I_F = 0.5A$, $I_R = 1.0A$,
Reverse Recovery Time	trr	_	120	_	113	$I_{RR} = 0.25A$
Junction Capacitance	CJ	-	100	-	pF	V _R = 4.0V, f = 1MHz

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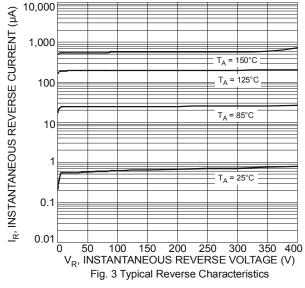
Notes:

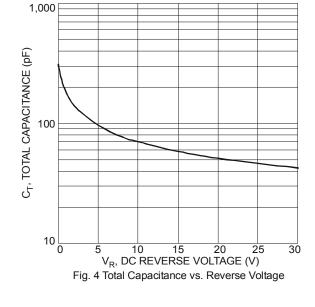
- 3. FR-4 PCB, 2 oz. Copper, minimum recommended pad layout per http://www.diodes.com.
- 4. Device mounted on Polymide substrate PC board, 16*MRP layout http://www.diodes.com.
- 5. Short duration pulse test used to minimize self-heating effect.

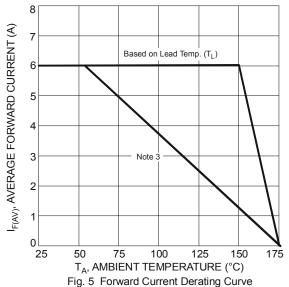




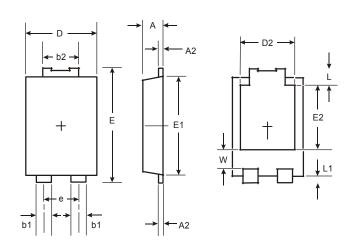








Package Outline Dimensions

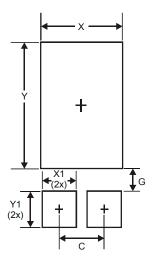


POWERDI°5			
Dim	Min	Max	
Α	1.05	1.15	
A2	0.33	0.43	
b1	0.80	0.99	
b2	1.70	1.88	
D	3.90	4.05	
D2	3.054 Typ		
Е	6.40	6.60	
е	1.84 Typ		
E1	5.30	5.45	
E2	3.549 Typ		
١	0.75	0.95	
L1	0.50	0.65	
W	1.10	1.41	
All Dimensions in mm			

DOWED DI®E



Suggested Pad Layout



Dimensions	Value (in mm)
С	1.840
G	0.852
Х	3.360
X1	1.390
Y	4.860
Y1	1.400

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