



SBR12A45SP5

12A SBR® SUPER BARRIER RECTIFIER POWERDI<sup>®</sup>5

#### **Features**

- Designed as Bypass Diodes for Solar Panels
- Selectively Rated for 200°C Maximum Junction Temperature for High Thermal Reliability
- Patented Super Barrier Rectifier Technology
- Low Forward Voltage Drop
- Excellent High Temperature Stability
- Lead Free Finish, RoHS compliant (Notes 1 & 2)
- Halogen and Antimony Free. "Green" Device (Note 3)

### **Mechanical Data**

- Case: POWERDI5 •
- Case Material: Molded Plastic, "Green" Molding Compound. UL Flammability Classification Rating 94V-0
- Moisture Sensitivity: Level 1 per J-STD-020

**-**

Terminals: Finish – Matte Tin annealed over Copper leadframe. Solderable per MIL-STD-202, Method 208 (3)

BOTTOMSIDE

HEAT SINK

Weight: 0.093 grams (approximate)



POWERDI5

#### Ordering Information (Note 4)

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Part Number	Case	Packaging
SBR12A45SP5-13	POWERDI5	5000/Tape & Reel
SBR12A45SP5-7	POWERDI5	1500/Tape & Reel

Notes: 1. EU Directive 2002/95/EC (RoHS) & 2011/65/EU (RoHS 2) compliant. All applicable RoHS exemptions applied.

2. See http://www.diodes.com for more information about Diodes Incorporated's definitions of Halogen- and Antimony-free, "Green" and Lead-free. 3. Halogen- and Antimony-free "Green" products are defined as those which contain <900ppm bromine, <900ppm chlorine (<1500ppm total Br + Cl) and

<1000ppm antimony compounds.

4. For packaging details, go to our website at http://www.diodes.com.

## **Marking Information**



S12A45S = Product Type Marking Code DII = Manufacturers' code marking K = Factory designator YYWW = Date Code Marking YY = Last two digits of year (ex: 09 for 2009) WW = Week code (01 - 53)



### Maximum Ratings (@T<sub>A</sub> = +25°C, unless otherwise specified.)

Single phase, half wave, 60Hz, resistive or inductive load.

Characteristic	Symbol	Value	Unit
Peak Repetitive Reverse Voltage Working Peak Reverse Voltage DC Blocking Voltage	V <sub>RRM</sub> V <sub>RWM</sub> V <sub>RM</sub>	45	V
Average Rectified Output Current	lo	12	A
Non-Repetitive Peak Forward Surge Current 8.3ms Single Half Sine-Wave Superimposed on Rated Load	I <sub>FSM</sub>	280	А
Non-Repetitive Avalanche Energy (TJ = +25°C, IAs = 2A, L = 8.5 mH)	EAS	30	mJ

## **Thermal Characteristics**

Characteristic		Symbol	Value	Unit
Typical Thermal Resistance Junction to Case (Note 5)		R <sub>θ</sub> JC	3	°C/W
Typical Thermal Resistance Junction to Ambient (Note 5)		$R_{ ext{ heta}JA}$	27	°C/W
	V <sub>R</sub> ≤ 80% V <sub>RRM</sub>		-65 to +150	
Operating Temperature Range	V <sub>R</sub> ≤ 50% V <sub>RRM</sub>	TJ	≤180	°C
	DC Forward Mode		≤200	
Storage Temperature Range		T <sub>STG</sub>	-65 to +175	°C

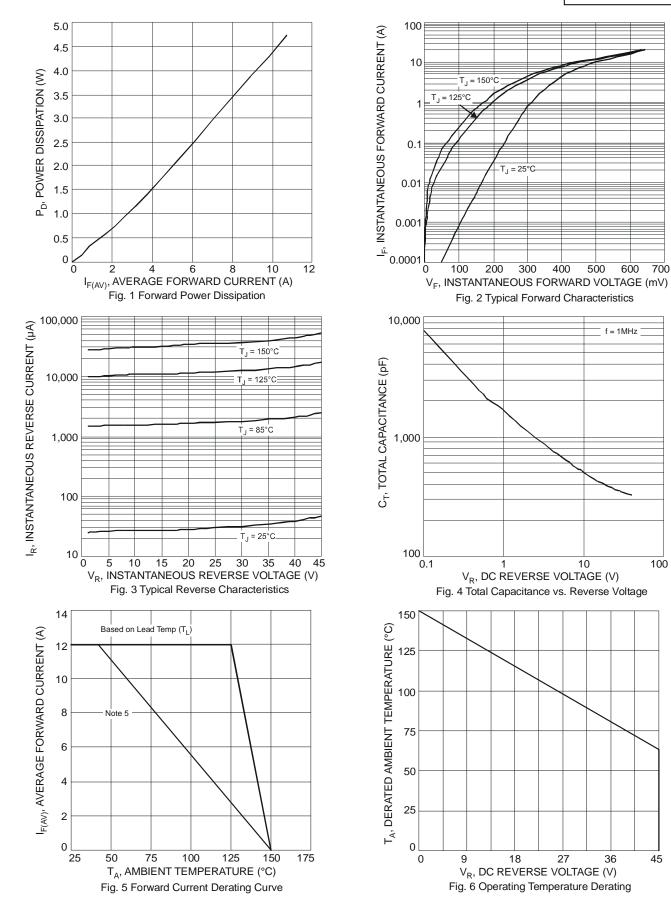
# Electrical Characteristics (@T<sub>A</sub> = +25°C, unless otherwise specified.)

Characteristic	Symbol	Min	Тур	Max	Unit	Test Condition
Forward Voltage Drop		-	0.43	-	V	I <sub>F</sub> = 6A, T <sub>J</sub> = +25°C
	V	-	0.50	0.60		I <sub>F</sub> = 12A, T <sub>J</sub> = +25°C
	V <sub>F</sub>	-	0.33	-		I <sub>F</sub> = 6A, T <sub>J</sub> = +125°C
		-	0.43	0.52		I <sub>F</sub> = 12A, T <sub>J</sub> = +125°C
Leakage Current (Note 6)		-	0.05	0.3	m (	V <sub>R</sub> = 45V, T <sub>J</sub> = +25°C
	IR	-	17	75	mA	V <sub>R</sub> = 45V, T <sub>J</sub> = +125°C
Typical Junction Capacitance	CJ	-	1000	-	pF	4.0V, 1MHz

 Polymide PCB, 2oz. Copper, minimum recommended pad layout per http://www.diodes.com.
Short duration pulse test used to minimize self-heating effect. Notes:



## SBR12A45SP5



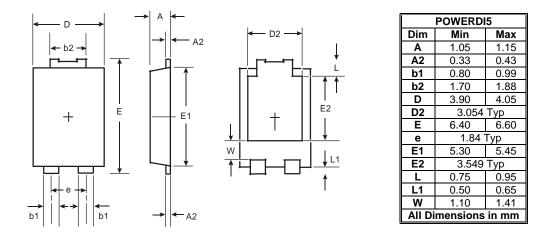
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3 of 5 www.diodes.com



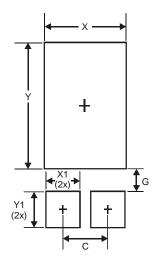
# **Package Outline Dimensions**

Please see AP02002 at http://www.diodes.com/datasheets/ap02002.pdf for latest version.



# **Suggested Pad Layout**

Please see AP02001 at http://www.diodes.com/datasheets/ap02001.pdf for the latest version.



Dimensions	Value (in mm)
С	1.840
G	0.852
Х	3.360
X1	1.390
Y	4.860
Y1	1.400



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