

PI2EQX6864-A

6.5Gbps, 4-lane, SAS 2.0/SATA 3.0/XAUI ReDriver™ with Equalization & Emphasis and Flow-through pinout

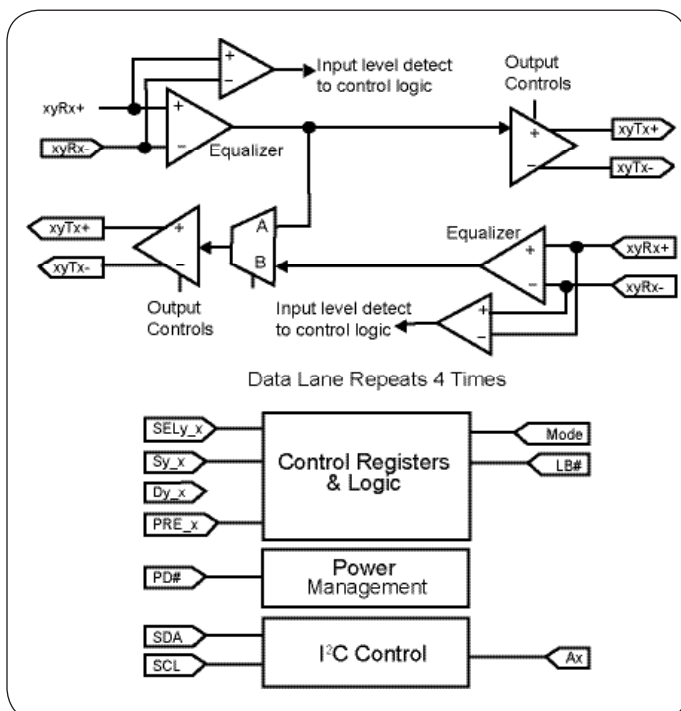
Pericom Semiconductor's PI2EQX6864-A is a low power, SAS2.0, SATA3.0, XAUI signal ReDriver. The device provides programmable equalization, amplification, and emphasis by using 8 select bits, to optimize performance over a variety of physical mediums by reducing Inter-symbol interference.

PI2EQX6864-A supports eight 100Ω Differential CML data I/O's between the Protocol ASIC to a switch fabric, across a backplane, or extends the signals across other distant data pathways on the user's platform.

The integrated equalization circuitry provides flexibility with signal integrity of the signal before the ReDriver, whereas the integrated emphasis circuitry provides flexibility with signal integrity of the signal after the ReDriver.

In addition to providing signal re-conditioning, Pericom's PI2EQX6864-A also provides power management Stand-by mode controlled via I²C register.

Block Diagram



Features

- Up to 6.5Gbps SAS2.0/SATA3.0/XAUI ReDriver
- Supporting 8 differential channels or 4 ports
- I²C configuration controls (3.3V Tolerant)
- Adjustable receiver equalization
- Adjustable transmitter amplitude and emphasis
- 50Ω input/output termination
- Mux/Demux feature
- Channel loop-back
- OOB fully supported
- Single supply voltage, 1.2V
- Power down modes
- Packaging: 56-contact TQFN (5mm x 11mm)

Applications:

- ☐ Server ☐ Workstation ☐ Data Storage

Figure1

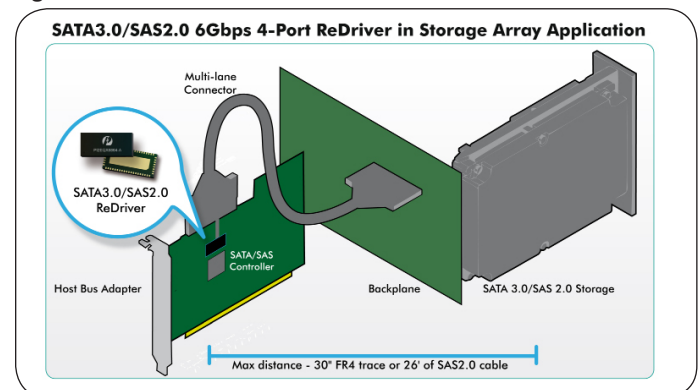


Figure1

Redrivers with emphasis and equalization signal conditioning technology ensure the integrity of high-frequency SAS 2.0/SATA 3.0 signals by opening closed signal eyes to recover data and meet strict compliance testing requirements. Increased signal margin also supports longer drive lengths over even low-quality cables