## Preliminary Data Sheet February 28, 2012



## NQR002A0X4: Non-Isolated DC-DC Power Modules 3Vdc –14Vdc input; 0.6Vdc to 5.5Vdc Output;2A Output Current



## **RoHS Compliant**

### **Applications**

- Distributed power architectures
- Intermediate bus voltage applications
- Telecommunications equipment
- Servers and storage applications
- Networking equipment
- Industrial applications

### **Features**

- Compliant to RoHS EU Directive 2002/95/EC (Z versions)
- Compatible in a Pb-free or SnPb wave-soldering environment (Z versions)
- Wide Input voltage range (3Vdc-14Vdc)
- Output voltage programmable from 0.6 Vdc to 5.5Vdc via external resistor
- Tunable Loop<sup>™</sup> to optimize dynamic output voltage response
- Fixed switching frequency
- Output overcurrent protection (non-latching)
- Over temperature protection
- Remote On/Off
- Small size: 10.4 mm x 13.5 mm x 8.1 mm

(0.41 in x 0.53 in x 0.32 in)

- Wide operating temperature range (-40°C to 85°C)
- UL\* 60950-1Recognized, CSA<sup>†</sup> C22.2 No. 60950-1-03 Certified, and VDE<sup>‡</sup> 0805:2001-12 (EN60950-1) Licensed
- ISO\*\* 9001 and ISO 14001 certified manufacturing facilities

## **Description**

The NQR002A0X4 SIP power modules are non-isolated dc-dc converters in an industry standard package that can deliver up to 2A of output current with a full load efficiency of TBD at 3.3Vdc output voltage ( $V_{IN} = 12Vdc$ ). These modules operate over a wide range of input voltage ( $V_{IN} = 3Vdc-14Vdc$ ) and provide a precisely regulated output voltage from 0.6Vdc to 5.5Vdc, programmable via an external resistor. Features include remote On/Off, adjustable output voltage, over current and over temperature protection. A new feature, the Tunable Loop<sup>TM</sup>, allows the user to optimize the dynamic response of the converter to match the load.

\* UL is a registered trademark of Underwriters Laboratories, Inc.
\* CSA is a registered trademark of Canadian Standards Association.

VDE is a trademark of Verband Deutscher Elektrotechniker e.V.

<sup>\*\*</sup> ISO is a registered trademark of the International Organization of Standards

## Absolute Maximum Ratings

Stresses in excess of the absolute maximum ratings can cause permanent damage to the device. These are absolute stress ratings only, functional operation of the device is not implied at these or any other conditions in excess of those given in the operations sections of the data sheet. Exposure to absolute maximum ratings for extended periods can adversely affect the device reliability.

Parameter	Device	Symbol	Min	Max	Unit
Input Voltage	All	V <sub>IN</sub>	-0.3	15	Vdc
Continuous					
Operating Ambient Temperature	All	T <sub>A</sub>	-40	85	°C
(see Thermal Considerations section)					
Storage Temperature	All	T <sub>stg</sub>	-55	125	°C

## **Electrical Specifications**

Unless otherwise indicated, specifications apply over all operating input voltage, resistive load, and temperature conditions.

Parameter	Device	Symbol	Min	Тур	Max	Unit
Operating Input Voltage	All	V <sub>IN</sub>	3	12	14	Vdc
Maximum Input Current	All	I <sub>IN,max</sub>			2.0	Adc
(V <sub>IN</sub> =3V to 14V, $I_0=I_{0, max}$ )						
Input No Load Current						
(V <sub>IN</sub> = 9Vdc, $I_{O}$ = 0, module enabled)	V <sub>O,set</sub> = 0.6 Vdc	IIN,No load		20		mA
$(V_{IN} = 12Vdc, I_{O} = 0, module enabled)$	$V_{O,set}$ = 5.0Vdc	I <sub>IN,No load</sub>		48		mA
Input Stand-by Current	All	I <sub>IN,stand-by</sub>		8		mA
$(V_{IN} = 12Vdc, module disabled)$						
Inrush Transient	All	l <sup>2</sup> t			1	A <sup>2</sup> s
Input Reflected Ripple Current, peak-to-peak (5Hz to 20MHz, 1 $\mu$ H source impedance; V <sub>IN</sub> =0 to 14V, I <sub>0</sub> = I <sub>Omax</sub> ; See Test Configurations)	All			20		mAp-p
Input Ripple Rejection (120Hz)	All			-65		dB

## **Electrical Specifications (continued)**

Parameter	Device	Symbol	Min	Тур	Max	Unit
Output Voltage Set-point (with 0.5% tolerance for external resistor used to set output voltage)	All	V <sub>O, set</sub>	-1.5		+1.5	% V <sub>O, set</sub>
Output Voltage	All	V <sub>O, set</sub>	-3.0	_	+3.0	% V <sub>O, set</sub>
(Over all operating input voltage, resistive load, and temperature conditions until end of life)						
Adjustment Range	All	Vo	0.6		5.5	Vdc
Selected by an external resistor						
Output Regulation (for $V_o \ge 2.5 Vdc$ )						
Line ( $V_{IN}=V_{IN, min}$ to $V_{IN, max}$ )	All		-0.4		+0.4	$\% V_{O, set}$
Load ( $I_0 = I_{O, min}$ to $I_{O, max}$ )	All				0.8	% V <sub>O, set</sub>
Output Regulation (for $V_o$ <2.5Vdc)						
Line ( $V_{IN}=V_{IN, min}$ to $V_{IN, max}$ )	All		-10		+10	mV
Load $(I_O=I_{O, min} \text{ to } I_{O, max})$	All				20	mV
Output Ripple and Noise on nominal output						
(V <sub>IN</sub> =V <sub>IN, nom</sub> and I <sub>O</sub> =I <sub>O, min</sub> to I <sub>O, max</sub> Cout = $22\mu$ F)						
Peak-to-Peak (5Hz to 20MHz bandwidth)	All		_	50	100	mV <sub>pk-pk</sub>
RMS (5Hz to 20MHz bandwidth)	All		_	20	38	mV <sub>rms</sub>
External Capacitance <sup>1</sup>						
Without the Tunable Loop <sup>™</sup>						
ESR ≥ 1 mΩ	All	C <sub>O, max</sub>	22		47	μF
With the Tunable Loop <sup>™</sup>						
ESR ≥ 0.15 mΩ	All	C <sub>O, max</sub>	0		1000	μF
ESR ≥ 10 mΩ	All	C <sub>O, max</sub>	0		3000	μF
Output Current	All	I <sub>o</sub>	0		2	Adc
Output Current Limit Inception (Hiccup Mode)	All	I <sub>O, lim</sub>		180		% I <sub>o,max</sub>
Output Short-Circuit Current	All	I <sub>O, s/c</sub>		140		Arms
$(V_0 \le 250 \text{mV})$ ( Hiccup Mode )						
Efficiency (V <sub>IN</sub> = 6Vdc)	V <sub>O,set</sub> = 0.6Vdc	η		69.2		%
V <sub>IN</sub> = 12Vdc, T <sub>A</sub> =25°C	V <sub>O, set</sub> = 1.2Vdc	η		80.4		%
I <sub>O</sub> =I <sub>O, max</sub> , V <sub>O</sub> = V <sub>O,set</sub>	V <sub>O,set</sub> = 1.8Vdc	η		85.5		%
	V <sub>O,set</sub> = 2.5Vdc	η		88.9		%
	V <sub>O,set</sub> = 3.3Vdc	η		91		%
	V <sub>O,set</sub> = 5.0Vdc	η		93.3		%
Switching Frequency	All	f <sub>sw</sub>	_	600	_	kHz

<sup>1</sup> External capacitors may require using the new Tunable Loop feature to ensure that the module is stable as well as getting the best transient response. See the Tunable Loop<sup>™</sup> section for details.

## **General Specifications**

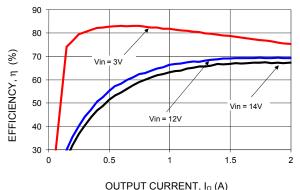
Parameter	Min	Тур	Max	Unit
Calculated MTBF (V_{IN}=12V, V_{O}=5Vdc, I_{O}=0.8I_{O, max}, T_{A}=40^{\circ}C) Per Telcordia Method		TBD		Hours
Weight	_	TBD	_	g (oz.)

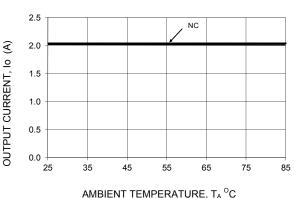
## **Feature Specifications**

Unless otherwise indicated, specifications apply over all operating input voltage, resistive load, and temperature conditions. See Feature Descriptions for additional information.

Parameter	Device	Symbol	Min	Тур	Max	Unit
On/Off Signal interface						
$(V_{IN}=V_{IN, min}$ to $V_{IN, max}$ ; Open collector or equivalent						
signal referenced to GND)						
Logic High (Enable pin open - Module ON)						
Input High Current	All	Ін		—	1	mA
Input High Voltage	All	Vін	3.0	—	$V_{\text{IN, max}}$	V
Logic Low (Module Off)						
Input Low Current	All	lı∟	_	—	10	μA
Input Low Voltage	All	VIL	-0.3	—	0.3	V
Turn-On Delay and Rise Times						
$(I_O=I_{O, max}, V_{IN} = V_{IN, nom}, V_o$ to within ±1% of steady state)						
Case 1: Enable input is enabled and then input power is applied (delay from instant at which $V_{IN} = V_{IN, min}$ until Vo=10% of Vo,set)	All	Tdelay		5		msec
Case 2: Input power is applied for at least one second and then Enable input is set enabled (delay from instant at which Enable is enabled until Vo=10% of Vo, set)	All	Tdelay		5.2		msec
Output voltage Rise time (time for Vo to rise from 10% of $V_{o,set}$ to 90% of Vo, set)	All	Trise		1.4		msec
Output voltage overshoot					3.0	% V <sub>O, set</sub>
$I_{O}\text{=}$ $I_{O,\text{ max}}\text{; }V_{IN}$ = $V_{IN,\text{ min}}$ to $V_{IN,\text{ max}}\text{, }T_{A}$ = 25 $^{\circ}\text{C}$						
Overtemperature Protection	All	T <sub>ref</sub>		117		°C
Input Undervoltage Lockout						
Turn-on Threshold	All			2.95		Vdc
Turn-off Threshold	All			2.8		Vdc

The following figures provide typical characteristics for the NQR002 (0.6V, 2A) at 25°C





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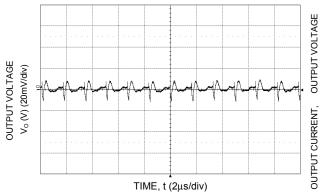


Figure 3. Typical output ripple and noise ( $V_{IN}$  = 12V,  $I_o$  =  $I_{o,max}$ ).

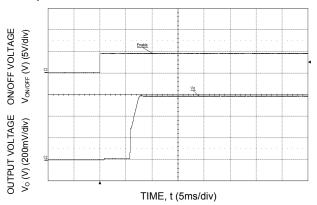


Figure 5. Typical Start-up Using On/Off Voltage ( $I_0 = I_{0,max}, V_{in=12V}, Cext= 22uF$ ).

Figure 2. Derating Output Current versus Ambient Temperature and Airflow.

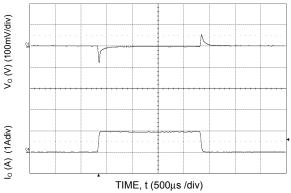


Figure 4. Transient Response to Dynamic Load Change from 0% to 50% to 0% .

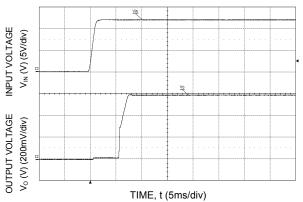
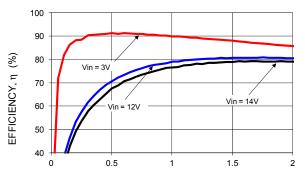
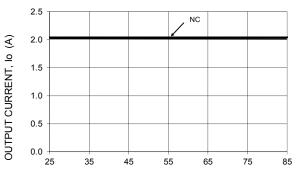


Figure 6. Typical Start-up Using Input Voltage (VIN = 12V, Cext= 22uF ,Io = Io,max).

The following figures provide typical characteristics for the NQR002 (1.2V, 2A) at 25°C



OUTPUT CURRENT, Io (A)



AMBIENT TEMPERATURE, T<sub>A</sub> <sup>O</sup>C



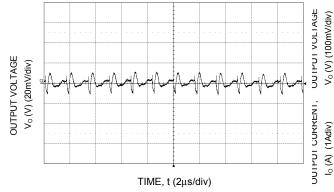


Figure 9. Typical output ripple and noise ( $V_{IN}$  = 12V,  $I_o$  =  $I_{o,max}$ ).

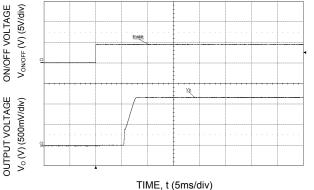


Figure 11. Typical Start-up Using On/Off Voltage ( $I_0 = I_{0,max}, V_{in=1}2V, Cext= 22uF$ ).

Figure 8. Derating Output Current versus Ambient Temperature and Airflow.

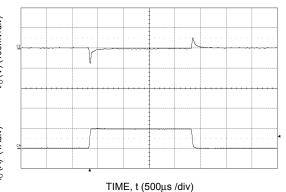
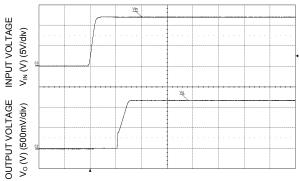


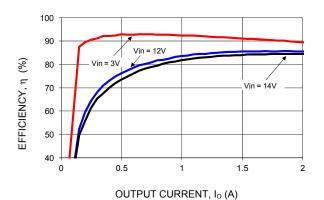
Figure 10. Transient Response to Dynamic Load Change from 0% to 50% to 0%.



TIME, t (5ms/div)

Figure 12. Typical Start-up Using Input Voltage (ViN = 12V, Cext= 22uF,  $I_0 = I_{0,max}$ ).

The following figures provide typical characteristics for the NQR002 (1.8V, 2A) at 25°C.



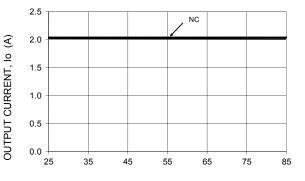


Figure 13. Converter Efficiency versus Output Current.

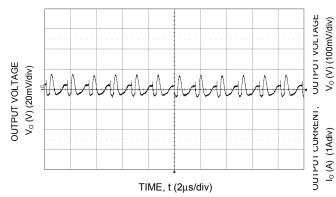


Figure 15. Typical output ripple and noise (VIN = 12V,  $I_0 = I_{0,max}$ ).

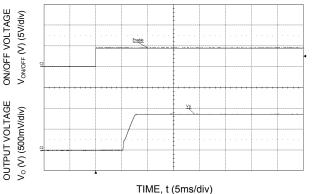


Figure 17. Typical Start-up Using On/Off Voltage ( $I_0 = I_{0,max}$ , Vin=12V,Cext=22uF,).

AMBIENT TEMPERATURE, T<sub>A</sub> <sup>o</sup>C Figure 14. Derating Output Current versus Ambient Temperature and Airflow.

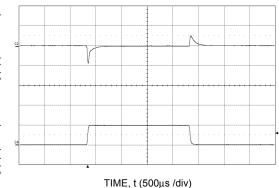


Figure 16. Transient Response to Dynamic Load Change from 0% to 50% to 0%.

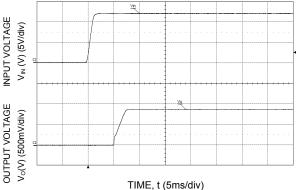
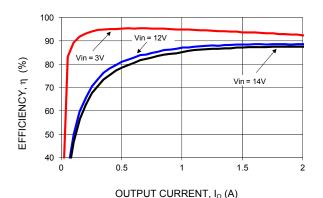
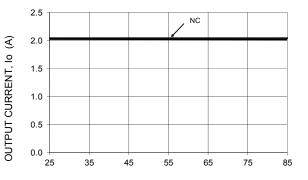
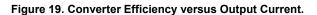


Figure 18. Typical Start-up Using Input Voltage ( $V_{IN} = 12V$ , Cext=22uF, I<sub>0</sub> = I<sub>0,max</sub>).

The following figures provide typical characteristics for the NQR002 (2.5V, 2A) at 25°C.







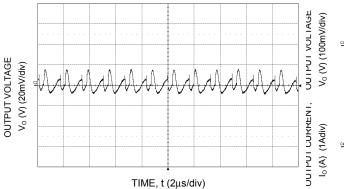


Figure 21. Typical output ripple and noise ( $V_{IN} = 12V$ ,  $I_o = I_{o,max}$ ).

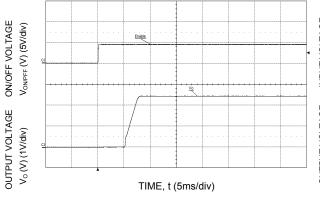


Figure 23. Typical Start-up Using On/Off Voltage (I\_o =  $I_{o,max},$  Vin=12V,Cext= 22uF ).

AMBIENT TEMPERATURE, T<sub>A</sub> <sup>o</sup>C Figure 20. Derating Output Current versus Ambient Temperature and Airflow.

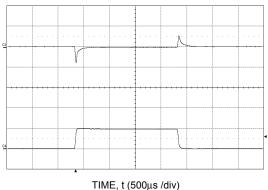


Figure 22. Transient Response to Dynamic Load Change from 0% to 50% to 0%.

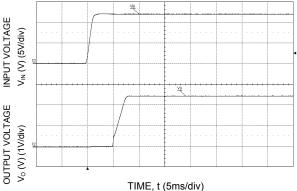
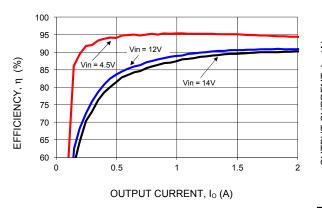


Figure 24. Typical Start-up Using Input Voltage (VIN = 12V, Cext= 22uF,  $l_0 = l_{0,max}$ ).



The following figures provide typical characteristics for the NQR002 (3.3V, 2A) at 25°C.

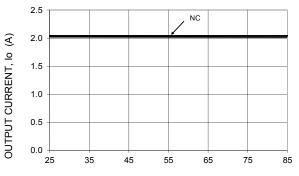


Figure 25. Converter Efficiency versus Output Current.

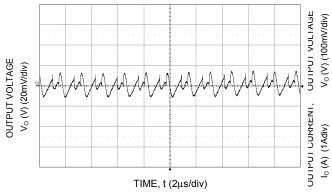


Figure 27. Typical output ripple and noise (VIN = 12V,  $I_0 = I_{0,max}$ ).

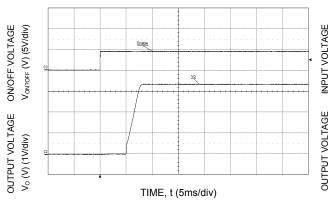


Figure 29. Typical Start-up Using On/Off Voltage ( $I_0 = I_{0,max}$ , Vin=12V,Cext= 22uF)

AMBIENT TEMPERATURE, T<sub>A</sub> <sup>o</sup>C Figure 26. Derating Output Current versus Ambient Temperature and Airflow.

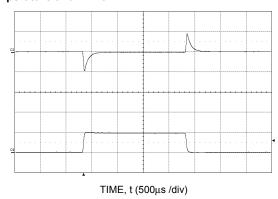


Figure 28. Transient Response to Dynamic Load Change from 0% to 50% to 0%.

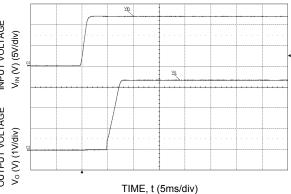
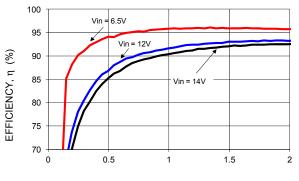
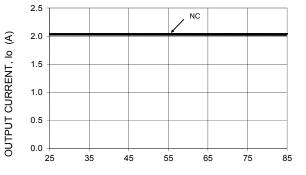


Figure 30. Typical Start-up Using Input Voltage ( $V_{IN}$  = 12V, Cext= 22uF, I<sub>0</sub> = I<sub>0,max</sub>).

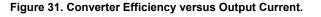
The following figures provide typical characteristics for the NQR002 (5V, 2A) at 25°C.







AMBIENT TEMPERATURE, T<sub>A</sub> <sup>O</sup>C



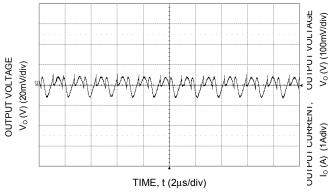


Figure 33. Typical output ripple and noise (VIN = 12V,  $I_0 = I_{0,max}$ ).

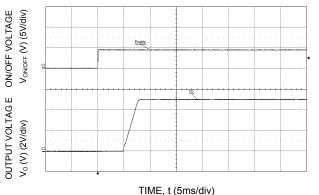


Figure 35. Typical Start-up Using On/Off Voltage ( $I_0 = I_{0,max}, V_{in=12V,Cext= 22uF$ ).

Figure 32. Derating Output Current versus Ambient Temperature and Airflow.

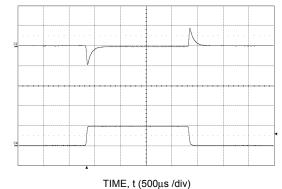
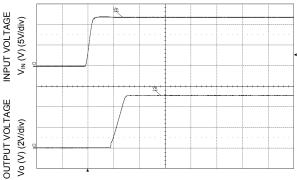
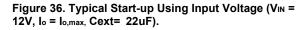


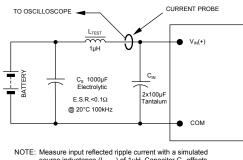
Figure 34. Transient Response to Dynamic Load Change from 0% to 50% to 0%.



TIME, t (5ms/div)

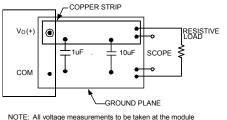


## **Test Configurations**

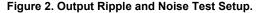


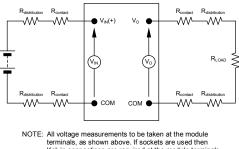
OTE: Measure input reflected ripple current with a simulated source inductance (L<sub>TEST</sub>) of 1µH. Capacitor C<sub>s</sub> offsets possible battery impedance. Measure current as shown above

#### Figure 1. Input Reflected Ripple Current Test Setup.



NOTE: All voltage measurements to be taken at the module terminals, as shown above. If sockets are used then Kelvin connections are required at the module terminal to avoid measurement errors due to socket contact resistance.





terminals, as shown above. If sockets are used then Kelvin connections are required at the module terminals to avoid measurement errors due to socket contact resistance.

Figure 3. Output Voltage and Efficiency Test Setup.

Efficiency 
$$\eta = \frac{V_0. I_0}{V_{IN} I_{IN}} \times 100 \%$$

#### **Design Considerations**

#### **Input Filtering**

The NQR002A0X4 2A module should be connected to a low ac-impedance source. A highly inductive source can affect the stability of the module. An input capacitance must be placed directly adjacent to the input pin of the module, to minimize input ripple voltage and ensure module stability.

To minimize input voltage ripple, low-ESR ceramic or polymer capacitors are recommended at the input of the module. Figure 4 shows the input ripple voltage for various output voltages at 2A of load current with 1x22  $\mu$ F or 2x22  $\mu$ F ceramic capacitors and an input of 12V.



#### Output Voltage (Vdc)

Figure 4. Input ripple voltage for various output voltages with  $1x22 \ \mu$ F or  $2x22 \ \mu$ F ceramic capacitors at the input (2A load). Input voltage is 12V.

#### **Output Filtering**

The NQR002A0X4 2A modules are designed for low output ripple voltage and will meet the maximum output ripple specification with no external capacitors. However, additional output filtering may be required by the system designer for a number of reasons. First, there may be a need to further reduce the output ripple and noise of the module. Second, the dynamic response characteristics may need to be customized to a particular load step change.

To reduce the output ripple and improve the dynamic response to a step load change, additional capacitance at the output can be used. Low ESR ceramic and polymer are recommended to improve the dynamic response of the module. Figure 5 provides output ripple information for different external capacitance values at various Vo and for a load current of 2A. For stable operation of the module, limit the capacitance to less than the maximum output capacitance as specified in the electrical specification table. Optimal performance of the module can be achieved by using the Tunable Loop<sup>TM</sup> feature described later in this data sheet.

Figure 5. Output ripple voltage for various output voltages with external 1x10  $\mu$ F, 1x47  $\mu$ F, 2x47  $\mu$ F or 4x47  $\mu$ F ceramic capacitors at the output (2A load). Input voltage is 12V.

## **Safety Considerations**

For safety agency approval the power module must be installed in compliance with the spacing and separation requirements of the end-use safety agency standards, i.e., UL 60950-1, CSA C22.2 No. 60950-1-03, and VDE 0850:2001-12 (EN60950-1) Licensed.

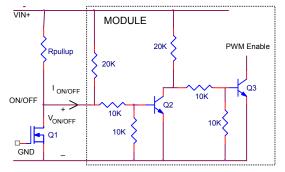
For the converter output to be considered meeting the requirements of safety extra-low voltage (SELV), the input must meet SELV requirements. The power module has extra-low voltage (ELV) outputs when all inputs are ELV.

An input fuse for the module is recommended. Due to the wide input voltage and output voltage ranges of the module, a 4A, 125Vdc fast acting fuse is recommended

#### **Feature Descriptions**

#### Enable (Remote On/Off)

The NQR002A0X4 2A power modules feature a Enable pin with positive logic for remote On/Off operation. If the Enable pin is not being used, leave the pin open (the module will be ON). The Enable signal ( $V_{On/Off}$ ) is referenced to ground. During a Logic High on the Enable pin, the module remains ON. During Logic-Low, the module is turned OFF.



# Figure 6. Remote On/Off Implementation(positive logic).

#### **Overcurrent Protection**

To provide protection in a fault (output overload) condition, the unit is equipped with internal current-limiting circuitry and can endure current limiting continuously. At the point of current-limit inception, the unit enters hiccup mode. The unit operates normally once the output current is brought back into its specified range. The average output current during hiccup is 10% I<sub>0. max</sub>.

#### **Overtemperature Protection**

To provide protection in a fault condition, these modules are equipped with a thermal shutdown circuit. The unit will shut down if the overtemperature threshold of 130°C is exceeded at the thermal reference point  $T_{ref}$ . The thermal shutdown is not intended as a guarantee that the unit will survive temperatures beyond its rating. Once the unit goes into thermal shutdown it will then wait to cool before attempting to restart.

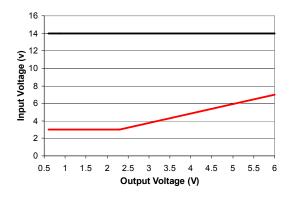
#### Input Undervoltage Lockout

At input voltages below the input undervoltage lockout limit, the module operation is disabled. The module will begin to operate at an input voltage above the undervoltage lockout turn-on threshold.

## **Feature Descriptions (continued)**

#### **Output Voltage Programming**

The output voltage of the NQR002A0X4 2A module can be programmed to any voltage from 0.6dc to 5.5Vdc by connecting a resistor between the Trim+ and GND pins of the module. Certain restrictions apply on the output voltage set point depending on the input voltage. These are shown in the Output Voltage vs. Input Voltage Set Point Area plot in Fig. 7. The Lower Limit curve shows that for output voltages of 2.4V and higher, the input voltage needs to be larger than the minimum of 3V.



#### Figure 7. Output Voltage vs. Input Voltage Set Point Area plot showing limits where the output voltage can be set for different input voltages.

Without an external resistor between Trim+ and GND pins, the output of the module will be 0.6Vdc. To calculate the value of the trim resistor, *Rtrim* for a desired output voltage, use the following equation:

$$Rtrim = \left[\frac{12}{(Vo - 0.6)}\right] k\Omega$$

Rtrim is the external resistor in  $k\Omega$ 

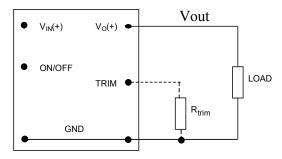
Vo is the desired output voltage

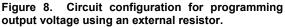
Table 1 provides Rtrim values required for some common output voltages.

Table 1

V <sub>O, set</sub> (V)	Rtrim (KΩ)
0.6	Open
0.9	40
1.0	30
1.2	20
1.5	13.33
1.8	10
2.5	6.316
3.3	4.444
5.0	2.727

By using a  $\pm 0.5\%$  tolerance trim resistor with a TC of  $\pm 25$ ppm, a set point tolerance of  $\pm 1.5\%$  can be achieved as specified in the electrical specification. The POL Programming Tool available at www.lineagepower.com under the Design Tools section, helps determine the required trim resistor needed for a specific output voltage.

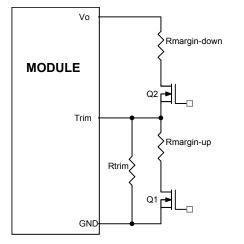




#### Voltage Margining

Output voltage margining can be implemented in the NQR002A0X4 2A modules by connecting a resistor, R<sub>margin-up</sub>, from the Trim pin to the ground pin for margining-up the output voltage and by connecting a resistor, R<sub>margin-down</sub>, from the Trim pin to output pin for margining-down. Figure 9 shows the circuit configuration for output voltage margining. The POL Programming Tool, available at www.lineagepower.com under the Design Tools section, also calculates the values of R<sub>margin-up</sub> and R<sub>margin-down</sub> for a specific output voltage and % margin. Please consult your local Lineage Power Field Application Engineer or Account Manager for additional details.

## **Feature Descriptions (continued)**



## Figure 9. Circuit Configuration for margining Output voltage.

#### **Monotonic Start-up and Shutdown**

The NQR002A0X4 2A modules have monotonic start-up and shutdown behavior for any combination of rated input voltage, output current and operating temperature range.

## Tunable Loop<sup>™</sup>

The NQR002A0X4 2A modules have a new feature that optimizes transient response of the module called Tunable Loop<sup>TM</sup>. External capacitors are usually added to improve output voltage transient response due to load current changes. Sensitive loads may also require additional output capacitance to reduce output ripple and noise. Adding external capacitance however affects the voltage control loop of the module, typically causing the loop to slow down with sluggish response. Larger values of external capacitance could also cause the module to become unstable.

To use the additional external capacitors in an optimal manner, the Tunable  $Loop^{TM}$  feature allows the loop to be tuned externally by connecting a series R-C between the VOUT and TRIM pins of the module, as shown in Fig. 10. This R-C allows the user to externally adjust the voltage loop feedback compensation of the module to match the filter network connected to the output of the module.

Recommended values of  $R_{TUNE}$  and  $C_{TUNE}$  are given in Tables 2 and 3. Table 2 lists recommended values of  $R_{TUNE}$  and  $C_{TUNE}$  in order to meet 2% output voltage deviation limits for some common output voltages in the presence of a 1A to 2A step change (50% of full load), with an input voltage of 12V. Table 3 shows the recommended values of  $R_{TUNE}$  and  $C_{TUNE}$  for different values of ceramic output capacitors up to TBD, again for an input voltage of 12V. The value of  $R_{TUNE}$  should never be lower than the values shown in Tables 3 and 4. Please contact your Lineage Power technical representative to obtain more details of this feature as well as for guidelines on how to select the right value of external R-C to tune the module for best transient performance and stable operation for other output capacitance values.

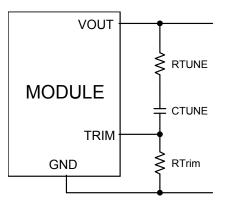


Figure. 10. Circuit diagram showing connection of  $R_{\text{TUME}}$  and  $C_{\text{TUNE}}$  to tune the control loop of the module.

Table 2. Recommended values of R<sub>TUNE</sub> and C<sub>TUNE</sub> to obtain transient deviation of 2% of Vout for a 1A step load with Vin=12V. TBD

Table 3. General recommended values of of  $R_{\text{TUNE}}$  and  $C_{\text{TUNE}}$  for Vin=12V and various external ceramic capacitor combinations.

#### TBD

## NQR002A0X4: Non-Isolated DC-DC Power Modules 3 – 14Vdc input; 0.6Vdc to 5.5Vdc Output; 2A output current

## **Thermal Considerations**

Power modules operate in a variety of thermal environments; however, sufficient cooling should be provided to help ensure reliable operation.

Considerations include ambient temperature, airflow, module power dissipation, and the need for increased reliability. A reduction in the operating temperature of the module will result in an increase in reliability. The thermal data presented here is based on physical measurements taken in a wind tunnel. The test set-up is shown in Figure 11. The preferred airflow direction for the module is in Figure 12.

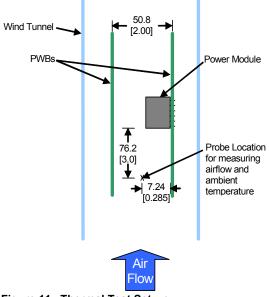
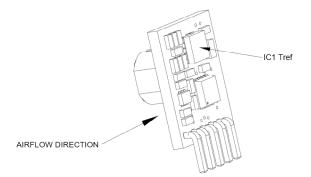


Figure 11. Thermal Test Set-up.

The thermal reference point,  $T_{ref}$  used in the specifications of thermal derating curves is shown in Figure 12. For reliable operation this temperature should not exceed 120°C.

The output power of the module should not exceed the rated power of the module (Vo,set x lo,max).

Please refer to the Application Note "Thermal Characterization Process For Open-Frame Board-Mounted Power Modules" for a detailed discussion of thermal aspects including maximum device temperatures.





# Post solder Cleaning and Drying Considerations

Post solder cleaning is usually the final circuit-board assembly process prior to electrical board testing. The result of inadequate cleaning and drying can affect both the reliability of a power module and the testability of the finished circuit-board assembly. For guidance on appropriate soldering, cleaning and drying procedures, refer to *Board Mounted Power Modules: Soldering and Cleaning* Application Note.

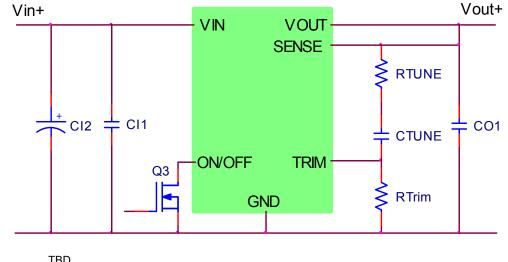
# Through-Hole Lead-Free Soldering Information

These RoHS-compliant through-hole products use the SAC (Sn/Ag/Cu) Pb-free solder and RoHS-compliant components. They are designed to be processed through single or dual wave soldering machines. The pins have an RoHS-compliant finish that is compatible with both Pb and Pb-free wave soldering processes. A maximum preheat rate of 3°C/s is suggested. The wave preheat process should be such that the temperature of the power module board is kept below 210°C. For Pb solder, the recommended pot temperature is 260°C, while the Pb-free solder pot is 270°C max. Not all RoHS-compliant through-hole products can be processed with paste-through-hole Pb or Pb-free reflow process. If additional information is needed, please consult with your Lineage Power representative for more details.

## **Example Application Circuit**

## Requirements:

Vin:	12V
Vout:	1.8V
lout:	1A max., worst case load transient is from 1A to 1.5A
∆Vout:	1.5% of Vout (27mV) for worst case load transient
Vin, ripple	1.5% of Vin (180mV, p-p)



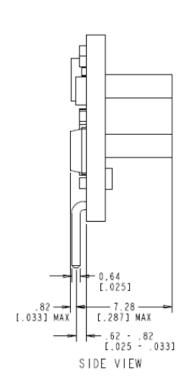
CI1	TBD
CI2	TBD
CO1	TBD
CTune	TBD
RTune	TBD
RTrim	$10 k\Omega$ SMT resistor (can be 1206, 0805 or 0603 size, recommended tolerance of 0.1%)

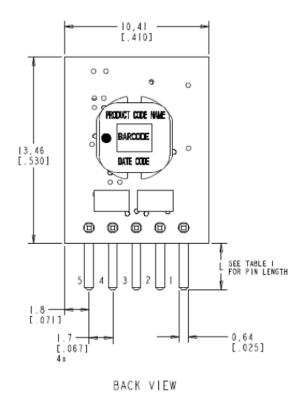
## NQR002A0X4: Non-Isolated DC-DC Power Modules 3 – 14Vdc input; 0.6Vdc to 5.5Vdc Output; 2A output current

## **Mechanical Outline**

Dimensions are in millimeters and (inches).

Tolerances: x.x mm  $\pm$  0.5 mm (x.xx in.  $\pm$  0.02 in.) [unless otherwise indicated] x.xx mm  $\pm$  0.25 mm (x.xxx in  $\pm$  0.010 in.)







PIN	Function
	On/Off
2	Vin
3	GND
4	Vout
5	Trim+

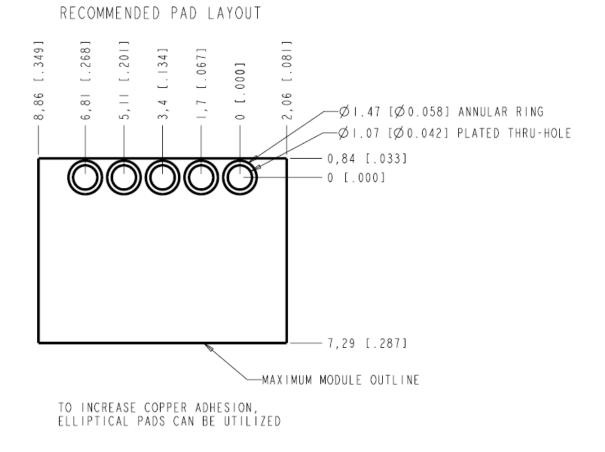
TABLE I

PRODUCT OPTION	PIN LENGTH "L" MM EINCHI
STANDARD	3.29 E.130]
OPTION - 6	2.85 E.1121
OPTION - 54	5.08 [.200]

## **Recommended Pad Layout**

Dimensions are in millimeters and (inches).

Tolerances: x.x mm  $\pm$  0.5 mm (x.xx in.  $\pm$  0.02 in.) [unless otherwise indicated] x.xx mm  $\pm$  0.25 mm (x.xxx in  $\pm$  0.010 in.)



#### LINEAGE POWER

## **Ordering Information**

Please contact your Lineage Power Sales Representative for pricing, availability and optional features.

#### **Table 4. Device Codes**

Device Code	Input Voltage Range	Output Voltage	Output Current	On/Off Logic	Connector Type	Comcodes
NQR002A0X4Z	3 – 14Vdc	0.6 - 5.5Vdc	2A	Positive	SIP	CC109171468

-Z refers to RoHS compliant parts



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20 Document No: DS11-017 ver. 0.27 PDF name: NQR002A0X\_ds.pdf