



Target Applications

- Barcode scanners
- · Portable media players
- Programmable logic controllers

Kinetis K20 Family

Low-power MCUs with USB On-The-Go

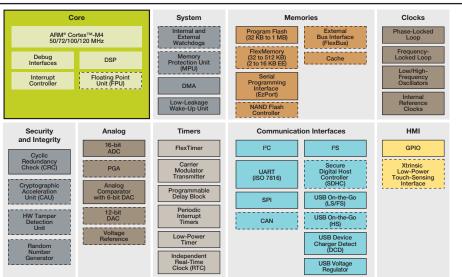
Overview

The Kinetis MCU portfolio consists of multiple pin-, peripheral- and softwarecompatible MCU families based on the ARM® Cortex™-M4 core. Families are built from innovative 90 nm thin-film storage (TFS) flash technology with unique FlexMemory (EEPROM) capability, and offer industry-leading low-power and mixedsignal analog integration.

The K20 MCU family is pin, peripheral and software compatible the K10 MCU family and adds Full- and High-Speed USB 2.0 On-The-Go with device charger detect capability. Devices start from 32 KB of flash in 5 x 5 mm 32 QFN packages extending up to 1 MB in a 144 MAPBGA package with a rich suite of analog, communication, timing and control peripherals. High memory density K20 family devices include a single precision floating point unit and NAND flash controller.

Kinetis K20 Family

Standard Feature Optional Feature





One-Stop Enablement Offering-MCU + IDE + RTOS

Freescale Tower System hardware development environment:

- Integrated development environments
 - Eclipse-based CodeWarrior V10.x IDE and Processor Expert
 - IAR Embedded Workbench
 - Keil MDK
 - CodeSourcery Sourcery G++ (GNU)
- Runtime software and RTOS
 - Math, DSP and encryption libraries
 - o Motor control libraries
 - Complimentary bootloaders (USB, Ethernet, RF, serial)
 - Complimentary Freescale embedded GUI
 - Complimentary Freescale MQX™
 - Cost-effective Nano™ SSL/Nano™ SSH for Freescale MQX RTOS
 - o Micrium uC/OS-III
 - Express Logic ThreadX
 - SEGGER embOS
 - o freeRTOS
 - o Mocana (security)
- Full ARM ecosystem

Features and Benefits

Features	Benefits
ARM® Cortex™-M4 core with DSP instruction support and optional	Up to 120 MHz core supporting a broad range of processing bandwidth needs
 single precision floating point unit Up to 32-channel DMA. Up to 16 KB of cache. Cross bar switch 	Peripheral and memory servicing with reduced CPU loading. Optimized bus bandwidth and flash execution performance. Concurrent multi-master bus accesses for increased bus bandwidth
USB On-The-Go (Full- and High- Speed) with device charger detect	Optimized charging current/time for portable USB devices, enabling longer battery life. USB low-voltage regulator supplies up to 120 mA off chip at 3.3 V to power external components from 5 V input
Memory protection unit	Provides memory protection for all cross bar switch masters, increasing software reliability
 Hardware cyclic redundancy check engine 	Validates memory contents and communication data, increasing system reliability
 Independent-clocked COP. External watchdog monitor 	Prevents code runaway in fail-safe applications. Drives output pin to safe state external components if watchdog event occurs
 Cryptographic acceleration unit (CAU)* Hardware tamper detection unit* 	Secure data transfer and storage. Faster than software implementations and with minimal CPU loading. Supports a wide variety of algorithms: DES, 3DES, AES, MDS, SHA-1, SHA-256
Random number generator*	Secure key storage with internal/external tamper detect for unsecured flash, temperature/clock/supply voltage variations and physical attack
Up to four FlexTimers with up to 20 channels	General-purpose timers with hardware dead-time insertion and quadrature decoding for motor control
Carrier modulator transmitter	Infrared waveform generation for remote control applications
 4-channel, 32-bit periodic interrupt 	Time base generation for RTOS task scheduler or trigger source for ADC conversion and programmable delay block
FlexBus external bus interface	Enables the connection of external memories and peripherals (e.g., graphics displays)
Secure digital host controller NAND flash controller	Connection to SD, SDIO, MMC or CE-ATA cards for in-application software upgrades, file systems or adding Wi-Fi® or Bluetooth® support
	Supports up to 32-bit ECC current and future NAND types with minimal software overhead
32 KB–1 MB flash. Up to 128 KB of SRAM	High reliability, fast access program memory with 4-level security protection. Independent flash banks allow concurrent code execution and firmware updating
32–512 KB FlexMemory	FlexMemory provides 32 byte–16 KB of user-segmentable byte write/ erase EEPROM. 32–512 KB FlexNVM for extra program code, data or EEPROM backup
	Maximize board space
 Wafer level chip scale package (WLCSP) 	32-bit M4 performance in small package
(2001)	Miniaturization of existing applications

^{*}Optional

Freescale Development Tool Part Numbers

Tower Module Part Number	Kinetis Family Support	RSL Price	Extended H/W Support Options				
FRDM-K20D50M	50 MHz MK10 and MK20	\$18					
TWR-K20D50M	50 MHz MK10 and MK20	\$99	802.11.x, Serial Connectivity, Analog, Sensors, Graphics LCD,				
TWR-K21D50M	50 MHz MK11, MK12, MK21 and MK22	\$99	Audio, Tower Dock (iPad®, iPhone®, iPod®), General I/O, 2.4GHz				
TWR-K20D72M	72 MHz MK10 and MK20	\$119	and Sub 1 GHz RF, Modules, Motor Control, Memory Modules etc.				
TWR-K21F120M	120 MHz MK21 and MK22	\$99					

Bundled kits with peripheral boards are available. For more information, visit freescale.com/Tower

K20 Family Options

Part Number	Memory					Feature Options											Packages									
	(;	<u>.</u>	(B)	e e	(B)	Single Precision Floating Point Unit	ction		Ital	ج ۲ ا	sn	Interface 12-bit DAC	Prog. Gain Amplifier	5 V Tolerant I/O		120 WLCSP B (5×5) B	FM	FT	LF	MP	LH	LK	LL	мс	LQ	MD
	CPU (MHz)	Flash (KB)	Flex NVM (KB)	SRAM (KB)	I/D Cache (KB)		Memory Protection Unit	CAN	Secure Digital Host Controller	NAND Flash Controller	External Bus Interface				Other		32 QFN (5×5)	48 QFN (7×7)	48 LQFP (7×7)	64 MAPBGA (5 x 5)	64 LQFP (10 x 10)	80 LQFP (12 x 12)	100 LQFP (14 x 14)	121 BGA (8 x 8)	144 LQFP (20 × 20)	144 BGA (13 x 13)
MK20DN32Vyy5	50	32		8											USB OTG (FS)		1	1	1	1	1					
MK20DN64Vyy5	50	64		16											USB OTG (FS)		1	1	1	1	1					
MK20DN128Vyy5	50	128		16											USB OTG (FS)		1	1	V	1	1					
MK20DN512Vyy10	100	512		128			1	1	1		V	1	1	1	USB OTG (FS)							V	1	1	1	V
MK20FN1M0Vyy12	120	1 MB		128	8/8	J	1	J	√	1	J	1	1	1	USB OTG (FS/HS)										1	J
MK20DX32Vyy5	50	32	32	8											USB OTG (FS)		1	1	V	1	1					
MK20DX64Vyy5	50	64	32	16											USB OTG (FS)		1	1	1	1	1					
MK20DX128Vyy5	50	128	32	16											USB OTG (FS)		1	1	V	1	1					
MK20DX64Vyy7	72	64	32	16				J			J	1	1	1	USB OTG (FS)						J	V		V		
MK20DX128Vyy7	72	128	32	32				1			J	1	1	1	USB OTG (FS)						1	V	1	V		
MK20DX256Vyy7	72	256	32	64				1			1	1	1	1	USB OTG (FS)						1	V	1	1		
MK20DX128Vyy10	100	128	128	32			1	1	1		1	1	1	1	USB OTG (FS)										1	V
MK20DX256Vyy10	100	256	256	64			1	1	1		1	1	1	1	USB OTG (FS)							V	1	V	1	V
MK20FX512Vyy12	120	512	512	128	8/8	J	1	J	1	1	1	1	J	1	USB OTG (FS/HS)										1	J
MK20DN512Zyy10R	100	512		128			1	1	√		J	1	1	1	USB OTG (FS)	1										
MK20DN512ZCyy10R	100	512		128			1	J	√		1	1	1	√	USB OTG (FS)	J										
MK21DX128Vyy5(R)	50	128	64	32								1			USB OTG (FS), HW Encryption and Tamper Detect							1		1		
MK21DX256Vyy5(R)	50	256	64	32								V			USB OTG (FS), HW Encryption and Tamper Detect							1		1		
MK21DN512VLK5(R)	50	512		64								V			USB OTG (FS), HW Encryption and Tamper Detect							1		1		
MK22DX128Vyy5(R)	50	128	64	32								1			USB OTG (FS)				1		J	√		V		
MK22DX256Vyy5(R)	50	256	64	32								1			USB OTG (FS)				V		J	V		V		
MK22DN512VLH5(R)	50	512		64								J			USB OTG (FS)						J	J		V		
MK21FN1M0Vyy12	120	1MB		128		1	J	J	1		1	1		1	USB OTG (FS), HW Encryption and Tamper Detect									1	1	1
MK21FX512VLQ12	120	512	128	128		J	1	J	1		1	J		1	USB OTG (FS), HW Encryption and Tamper Detect									1	1	J
MK22FN1M0VLH12	120	1MB		128		1	J	J			1	1		1	USB OTG (FS)						V					
MK22FN1M0VLK12	120	1MB		128		1	1	J	1		1	1		1	USB OTG (FS)							1	J	V	1	V
MK22FX512VLH12	120	512	128	128		1	1	J			1	1		1	USB OTG (FS)						1					
MK22FX512VLK12	120	512	128	128		1	1	1	1		1	1		1	USB OTG (FS)							1	1	1	√	1

yy = package designator



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