

GB01SLT12-252

Silicon Carbide Power Schottky Diode

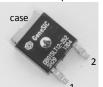
V_{RRM}	=	1200 V
V_{F}	=	1.6 V
I _F	=	1 A
\mathbf{Q}_{C}	=	13 nC

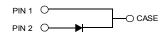
Features

- 1200 V Schottky rectifier
- 175 °C maximum operating temperature
- · Temperature independent switching behavior
- Superior surge current capability
- Positive temperature coefficient of V_F
- · Extremely fast switching speeds
- Superior figure of merit Q_C/I_F

Package

RoHS Compliant





TO - 252

Advantages

- Improved circuit efficiency (Lower overall cost)
- Low switching losses
- Ease of paralleling devices without thermal runaway
- Smaller heat sink requirements
- Low reverse recovery current
- · Low device capacitance
- Low reverse leakage current at operating temperature

Applications

- Power Factor Correction (PFC)
- Switched-Mode Power Supply (SMPS)
- Solar Inverters
- Wind Turbine Inverters
- Motor Drives
- Induction Heating
- Uninterruptible Power Supply (UPS)
- High Voltage Multipliers

Maximum Ratings at T_i = 175 °C, unless otherwise specified

Parameter	Symbol	Conditions	Values	Unit	
Repetitive peak reverse voltage	V_{RRM}		1200	V	
Continuous forward current	I _F	T _C ≤ 160 °C	1	Α	
RMS forward current	I _{F(RMS)}	T _C ≤ 160 °C	2	Α	
Surge non-repetitive forward current, Half Sine		T_C = 25 °C, t_P = 10 ms	10	А	
Wave	I _{F,SM}	$T_{\rm C}$ = 160 °C, $t_{\rm P}$ = 10 ms	8		
Non-repetitive peak forward current	$I_{F,max}$	$T_{\rm C}$ = 25 °C, $t_{\rm P}$ = 10 $\mu {\rm s}$	65	Α	
l ² t value	∫i² dt	$T_{\rm C}$ = 25 °C, $t_{\rm P}$ = 10 ms	0.5	A ² s	
i i value	Ji at	T_C = 160 °C, t_P = 10 ms	0.3	AS	
Power dissipation	P _{tot}	T _C = 25 °C	42	W	
Operating and storage temperature	T_{j} , T_{stg}		-55 to 175	°C	

Electrical Characteristics at T_i = 175 °C, unless otherwise specified

Parameter	Symbol	Conditions min.			Values		Unit
Parameter	Syllibol			min.	typ.	max.	Ulil
Diode forward voltage	\/	$I_F = 1 A, T_j = 2$	5 °C		1.6	1.8	V
Diode forward voltage	V_{F}	I _F = 1 A, T _j = 175 °C		2.4	3.7	V	
Reverse current	I _R	$V_R = 1200 \text{ V}, T_j = 25 \text{ °C}$		5	10	μA	
Reverse current		$V_R = 1200 \text{ V}, T_j = 175 ^{\circ}\text{C}$		10	100		
Total capacitive charge	0		V _R = 400 V		7		nC
Total capacitive charge	Q_{C}	$I_F \le I_{F,MAX}$ $dI_F/dt = 200 \text{ A/}\mu\text{s}$	$V_{R} = 960 \text{ V}$		13		IIC
Switching time	4	T _i = 175 °C	V _R = 400 V		< 17	20	
Switching time	t _s	V _R = 960 V			× 17		ns
	С	$V_R = 1 \text{ V, f} = 1 \text{ MHz, T}_j = 25 \text{ °C}$		69			
Total capacitance		$V_R = 400 \text{ V}, f = 1 \text{ MHz}, T_j = 25 ^{\circ}\text{C}$		10		pF	
		$V_R = 1000 \text{ V}, f = 1 \text{ MHz}, T_j = 25 ^{\circ}\text{C}$		8			

Thermal Characteristics

mermanesistance, junction - case	R _{thJC}	3.0	C/VV
Mechanical Properties			
• • • • • • • • • • • • • • • • • • • •			

М

Mounting torque

Nm

0.6



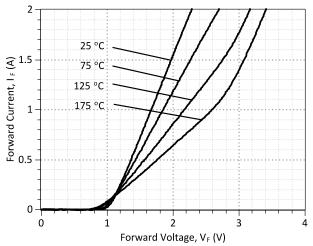


Figure 1: Typical Forward Characteristics

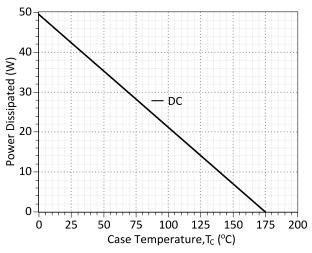


Figure 3: Power Derating Curve

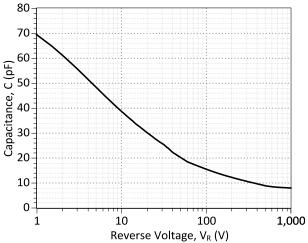


Figure 5: Typical Junction Capacitance vs Reverse Voltage Characteristics

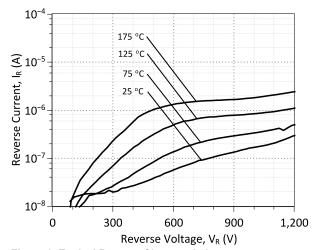


Figure 2: Typical Reverse Characteristics

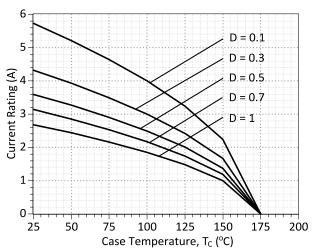


Figure 4: Current Derating Curves (D = t_P/T , t_P = 400 μ s) (Considering worst case Z_{th} conditions)

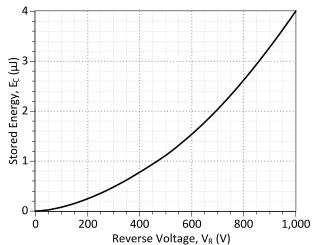


Figure 6: Typical Switching Energy vs Reverse Voltage Characteristics



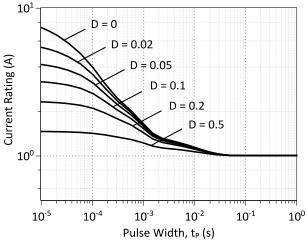


Figure 7: Current vs Pulse Duration Curves at T_{C} = 160 °C

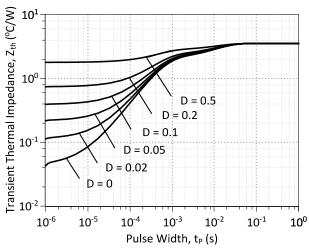
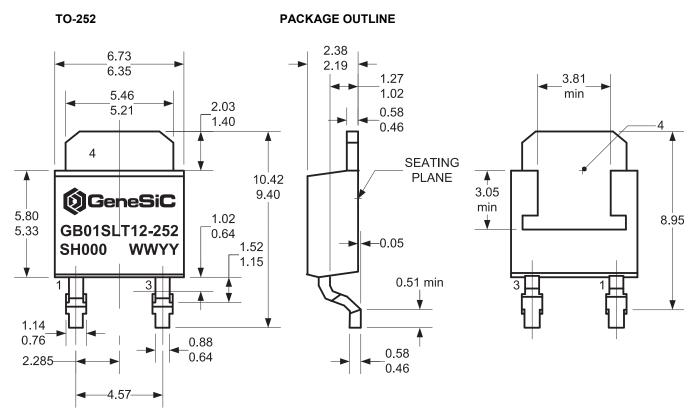


Figure 8: Transient Thermal Impedance

Package Dimensions:



NOTE

- 1. CONTROLLED DIMENSION IS INCH. DIMENSION IN BRACKET IS MILLIMETER.
- 2. DIMENSIONS DO NOT INCLUDE END FLASH, MOLD FLASH, MATERIAL PROTRUSIONS
- 3. CONTROLLED LEAD COPLANARITY <D> 0.004 INCH MAXIMUM



Revision History					
Date	Revision	Comments	Supersedes		
2013/11/12	3	Updated Electrical Characteristics			
2013/02/05	2	Second generation update			
2012/05/22	1	Second generation release			
2010/12/13	0	Initial release			

Published by GeneSiC Semiconductor, Inc. 43670 Trade Center Place Suite 155 Dulles, VA 20166

GeneSiC Semiconductor, Inc. reserves right to make changes to the product specifications and data in this document without notice.

GeneSiC disclaims all and any warranty and liability arising out of use or application of any product. No license, express or implied to any intellectual property rights is granted by this document.

Unless otherwise expressly indicated, GeneSiC products are not designed, tested or authorized for use in life-saving, medical, aircraft navigation, communication, air traffic control and weapons systems, nor in applications where their failure may result in death, personal injury and/or property damage.



SPICE Model Parameters

Copy the following code into a SPICE software program for simulation of the GB01SLT12-252 device.

```
MODEL OF GeneSiC Semiconductor Inc.
    $Revision: 1.0
     $Date: 04-SEP-2013
                               $
    GeneSiC Semiconductor Inc.
     43670 Trade Center Place Ste. 155
    Dulles, VA 20166
    http://www.genesicsemi.com/index.php/sic-products/schottky
    COPYRIGHT (C) 2013 GeneSiC Semiconductor Inc.
    ALL RIGHTS RESERVED
* These models are provided "AS IS, WHERE IS, AND WITH NO WARRANTY
* OF ANY KIND EITHER EXPRESSED OR IMPLIED, INCLUDING BUT NOT LIMITED
* TO ANY IMPLIED WARRANTIES OF MERCHANTABILITY AND FITNESS FOR A
* PARTICULAR PURPOSE."
* Models accurate up to 2 times rated drain current.
* Start of GB01SLT12-252 SPICE Model
.SUBCKT GB01SLT12 ANODE KATHODE
R1 ANODE INT R=((TEMP-24)*0.0069); Temperature Dependant Resistor
D1 INT KATHODE GB01SLT12 25C; Call the 25C Diode Model
D2 ANODE KATHODE GB01SLT12 PIN; Call the PiN Diode Model
.MODEL GB01SLT12 25C D
+ IS 7.27E-19
                                    0.592251
                          RS
+ N
                         IKF
                                    407.773
         1
         1.2
+ EG
                         XTI
+ CJO
                                    0.367
         7.90E-11
                        VJ
+ M
         1.63
                         FC
                                    0.5
+ TT
        1.00E-10
1.00E-03
                         BV
                                    1200
+ IBV
                         VPK
                                    1200
+ IAVE
                                    SiC Schottky
                          \mathtt{TYPE}
+ MFG GeneSiC Semiconductor
.MODEL GB01SLT12 PIN D
         1.08E-17
                                   1.8
+ IS
                         RS
+ N
         2.2313
                                    999
                         IKF
+ EG
         3.23
                         XTI
                                    -65
+ FC
         0.5
                         TT
+ BV
         1200
                         IBV
                                    1.00E-03
+ VPK
         1200
                         IAVE
+ TYPE SiC_PiN
.ENDS
```

^{*} End of GB01SLT12-252 SPICE Model