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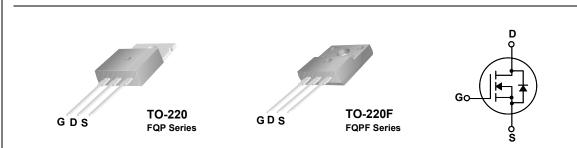
FQP32N20C / FQPF32N20C N-Channel QFET MOSFET 200 V, 28 A, 82 mΩ

Description

This N-Channel enhancement mode power MOSFET is produced using Fairchild Semiconductor®'s proprietary planar stripe and DMOS technology. This advanced MOSFET technology has been especially tailored to reduce on-state resistance, and to provide superior switching performance and high avalanche energy strength. These devices are suitable for switched mode power supplies, active power factor correction (PFC), and electronic lamp ballasts.

Features

- + 28 A, 200 V, $\mathsf{R}_{\mathsf{DS}(\mathsf{on})}$ = 82 m Ω (Max) @V_{GS} = 10 V, I_D = 14 A
- Low Gate Charge (Typ. 82.5 nC)
- Low Crss (Typ. 85 pF)
- 100% Avalanche Tested



Absolute Maximum Ratings T_c = 25°C unless otherwise noted

Symbol	Parameter		FQP32N20C	FQPF32N20C	Unit
V _{DSS}	Drain-Source Voltage		200		V
I _D	Drain Current - Continuous ($T_C = 25^{\circ}C$)		28.0	28.0 *	А
	- Continuous (T _C = 100°C)		17.8	17.8 *	А
I _{DM}	Drain Current - Pulsed	(Note 1)	112	112 *	А
V _{GSS}	Gate-Source Voltage		± 30		V
E _{AS}	Single Pulsed Avalanche Energy	(Note 2)	955		mJ
I _{AR}	Avalanche Current	(Note 1)	28.0		А
E _{AR}	Repetitive Avalanche Energy	(Note 1)	15.6		mJ
dv/dt	Peak Diode Recovery dv/dt (Note 3)		5.5		V/ns
P _D	Power Dissipation (T _C = 25°C)		156	50	W
	- Derate above 25°C		1.25	0.4	W/°C
T _J , T _{STG}	Operating and Storage Temperature Range		-55 to +150		°C
TL	Maximum lead temperature for soldering purposes, 1/8" from case for 5 seconds		300		°C
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Drain current lim	ited by maximum junction temperature.		1		

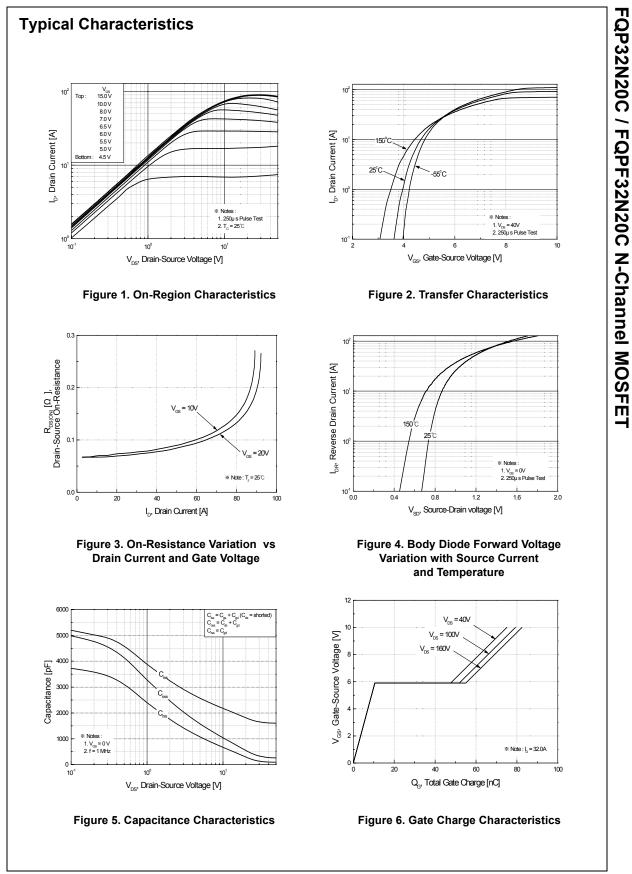
Thermal Characteristics

Symbol	Parameter	FQP32N20C	FQPF32N20C	Unit	
$R_{\theta JC}$	Thermal Resistance, Junction-to-Case	0.8	2.51	°C/W	
$R_{\theta JS}$	Thermal Resistance, Case-to-Sink Typ.	0.5		°C/W	
$R_{\theta JA}$	Thermal Resistance, Junction-to-Ambient	62.5	62.5	°C/W	

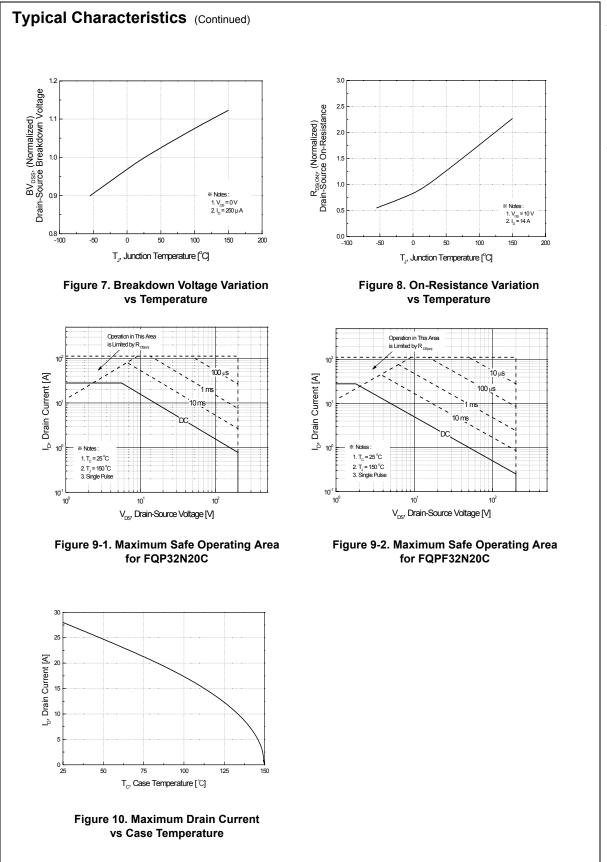
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March 2013

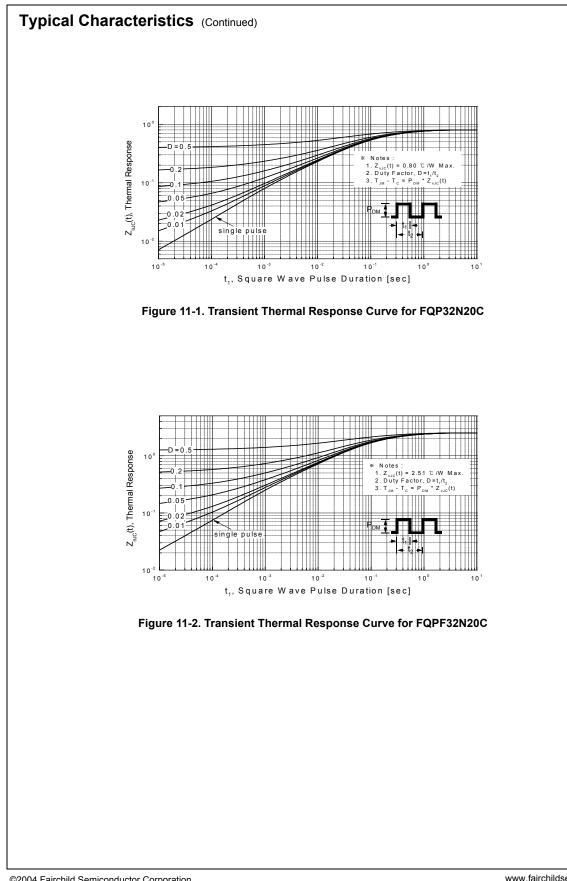
ff Cha /DSS ΔTJ SS SSF SSR	racteristics Drain-Source Breakdown Voltage Breakdown Voltage Temperature Coefficient Zero Gate Voltage Drain Current	$V_{GS} = 0 V, I_D = 250 \mu A$ $I_D = 250 \mu A, Referenced to 25°C$ $V_{DS} = 200 V, V_{GS} = 0 V$	200	 0.24		V V/°C
/DSS BV _{DSS} ΔT _J SS	Drain-Source Breakdown Voltage Breakdown Voltage Temperature Coefficient Zero Gate Voltage Drain Current	$I_D = 250 \ \mu$ A, Referenced to 25°C $V_{DS} = 200 \ V, V_{GS} = 0 \ V$				
ΔT _J SS	Coefficient Zero Gate Voltage Drain Current	V _{DS} = 200 V, V _{GS} = 0 V		0.24		V/°C
SSF						
		14 400 X X X 40700			10	μA
		V _{DS} = 160 V, T _C = 125°C			100	μA
SSR	Gate-Body Leakage Current, Forward	V_{GS} = 30 V, V_{DS} = 0 V			100	nA
	Gate-Body Leakage Current, Reverse	V_{GS} = -30 V, V_{DS} = 0 V			-100	nA
n Cha	racteristics					
GS(th)	Gate Threshold Voltage	V _{DS} = V _{GS} , I _D = 250 μA	2.0		4.0	V
DS(on)	Static Drain-Source On-Resistance	$V_{GS} = 10 V, I_D = 14 A$		0.068	0.082	Ω
s	Forward Transconductance	$V_{DS} = 40 \text{ V}, \text{ I}_{D} = 14 \text{ A}$ (Note 4)		20		S
		-				
	c Characteristics			1700	2220	
ss	Input Capacitance	$V_{DS} = 25 V, V_{GS} = 0 V,$		1700	2220	pF
iss ss	Output Capacitance Reverse Transfer Capacitance	f = 1.0 MHz		400	520 245	pF pF
on)	ng Characteristics Turn-On Delay Time Turn-On Rise Time	$V_{DD} = 100 \text{ V}, \text{ I}_{D} = 32 \text{ A},$		25 270	60 550	ns ns
	Turn-On Rise Time	$R_G = 25 \Omega$		270	550	ns
off)	Turn-Off Delay Time	(Note 4, 5)		245	500	ns
	Turn-Off Fall Time			210	430	ns
J	Total Gate Charge	V _{DS} = 160 V, I _D = 32 A,		82.5	110	nC
	Č Č	V _{GS} = 10 V				nC
d	Gate-Drain Charge	(Note 4, 5)		44.5		nC
rain-S	ource Diode Characteristics ar	nd Maximum Ratings				
	Maximum Continuous Drain-Source Dic	•			28	Α
и	Maximum Pulsed Drain-Source Diode F	Forward Current			112	Α
D	Drain-Source Diode Forward Voltage	V _{GS} = 0 V, I _S = 28 A			1.5	V
	Reverse Recovery Time	V _{GS} = 0 V, I _S = 32 A,		265		ns
		$dI_{\rm F} / dt = 100 {\rm A}/{\mu}{\rm s}$ (Note 4)		2.73		μC
И	Maximum Pulsed Drain-Source Diode F Drain-Source Diode Forward Voltage	$V_{GS} = 10 V$ (Note 4, 5) (265		 28 112 1.5

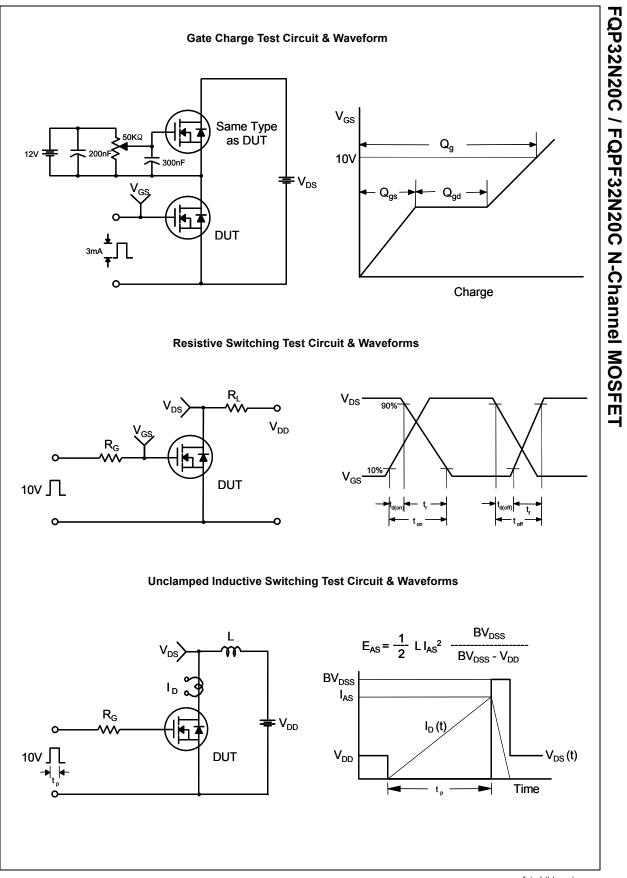


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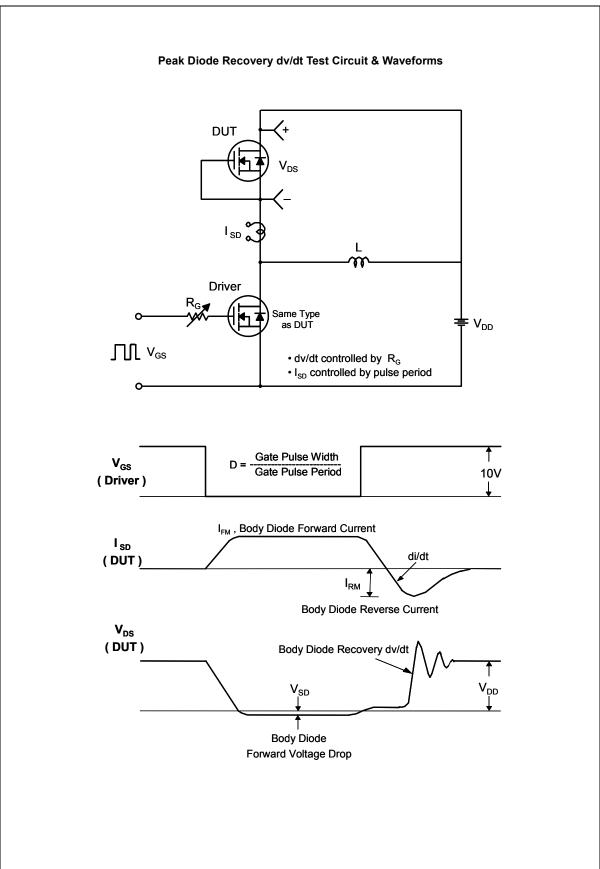
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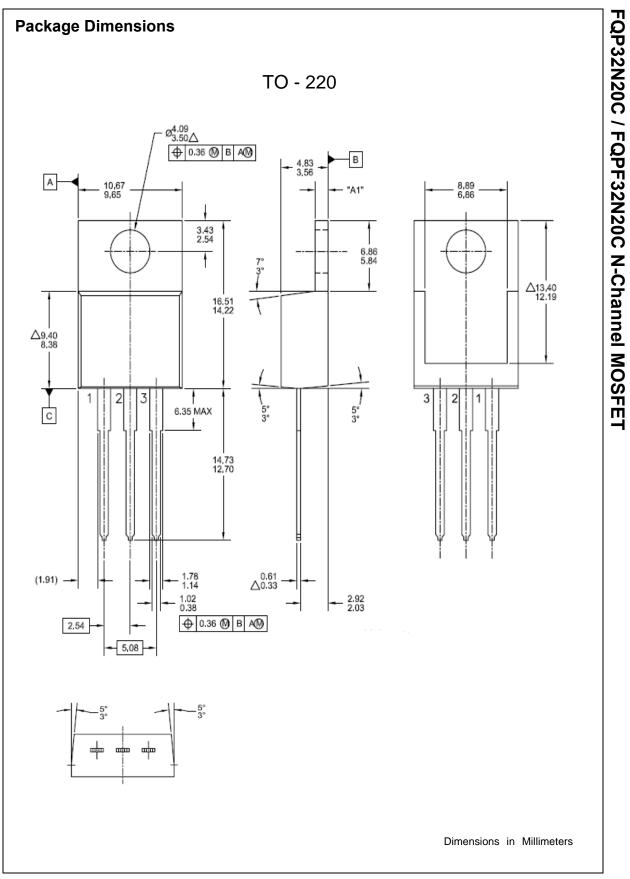




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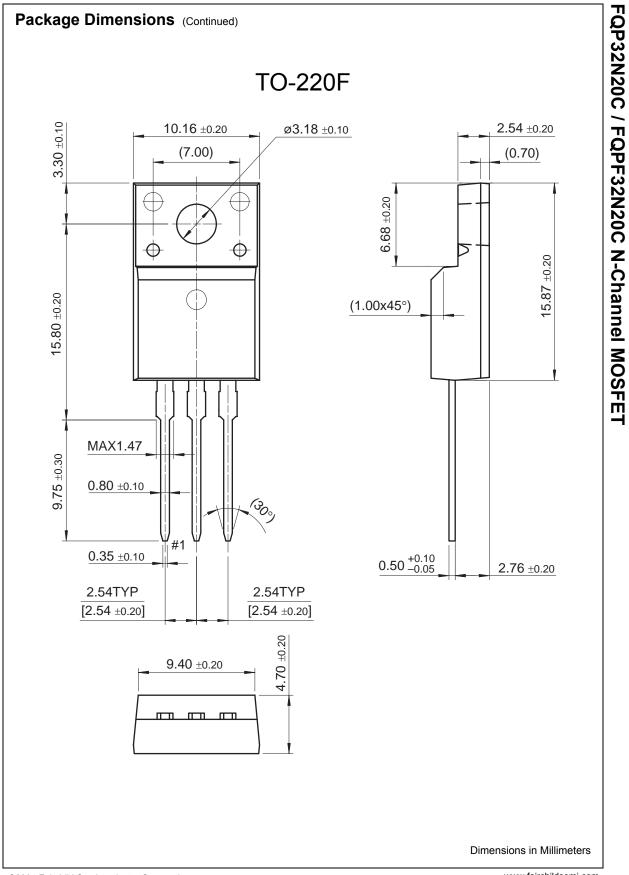
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