

FDPF6N60ZUT

N-Channel UniFET™ Ultra FRFET™ MOSFET 600 V, 4.5 A, 2 Ω

Features

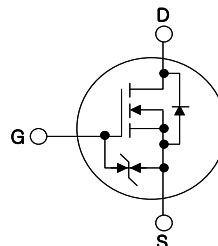
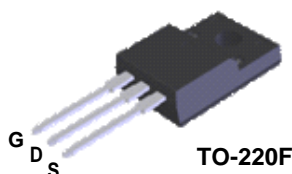
- $R_{DS(on)} = 1.7 \Omega$ (Typ.) @ $V_{GS} = 10 V$, $I_D = 2.25 A$
- Low Gate Charge (Typ. 14.5 nC)
- Low C_{rss} (Typ. 5 pF)
- 100% Avalanche Tested
- Improved dv/dt Capability
- RoHS Compliant

Applications

- LCD/LED TV
- Lighting
- Uninterruptible Power Supply
- AC-DC Power Supply

Description

UniFET™ MOSFET is Fairchild Semiconductor®'s high voltage MOSFET family based on planar stripe and DMOS technology. This MOSFET is tailored to reduce on-state resistance, and to provide better switching performance and higher avalanche energy strength. UniFET Ultra FRFET™ MOSFET has much superior body diode reverse recovery performance. Its t_{rr} is less than 50nsec and the reverse dv/dt immunity is 20V/nsec while normal planar MOSFETs have over 200nsec and 4.5V/nsec respectively. Therefore UniFET Ultra FRFET MOSFET can remove additional component and improve system reliability in certain applications that require performance improvement of the MOSFET's body diode. This device family is suitable for switching power converter applications such as power factor correction (PFC), flat panel display (FPD) TV power, ATX and electronic lamp ballasts.



MOSFET Maximum Ratings $T_C = 25^\circ C$ unless otherwise noted*

Symbol	Parameter		FDPF6N60ZUT	Unit
V_{DSS}	Drain to Source Voltage		600	V
V_{GSS}	Gate to Source Voltage		± 30	V
I_D	Drain Current	- Continuous ($T_C = 25^\circ C$)	4.5*	A
		- Continuous ($T_C = 100^\circ C$)	2.7*	
I_{DM}	Drain Current	- Pulsed (Note 1)	18*	A
E_{AS}	Single Pulsed Avalanche Energy	(Note 2)	150	mJ
I_{AR}	Avalanche Current	(Note 1)	4.5	A
E_{AR}	Repetitive Avalanche Energy	(Note 1)	10.5	mJ
dv/dt	Peak Diode Recovery dv/dt	(Note 3)	20	V/ns
P_D	Power Dissipation	($T_C = 25^\circ C$)	33.8	W
		- Derate above $25^\circ C$	0.27	W/ $^\circ C$
T_J, T_{STG}	Operating and Storage Temperature Range		-55 to +150	$^\circ C$
T_L	Maximum Lead Temperature for Soldering Purpose, 1/8" from Case for 5 Seconds		300	$^\circ C$

*Drain current limited by maximum junction temperature

Thermal Characteristics

Symbol	Parameter	FDPF6N60ZUT	Unit
$R_{\theta JC}$	Thermal Resistance, Junction to Case, Max.	3.7	$^\circ C/W$
$R_{\theta JA}$	Thermal Resistance, Junction to Ambient, Max.	62.5	

Package Marking and Ordering Information

Device Marking	Device	Package	Reel Size	Tape Width	Quantity
FDPF6N60ZUT	FDPF6N60ZUT	TO-220F	-	-	50

Electrical Characteristics $T_C = 25^\circ\text{C}$ unless otherwise noted

Symbol	Parameter	Test Conditions	Min.	Typ.	Max.	Unit
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Off Characteristics

BV_{DSS}	Drain to Source Breakdown Voltage	$I_D = 250\mu\text{A}$, $V_{GS} = 0\text{V}$, $T_J = 25^\circ\text{C}$	600	-	-	V
$\Delta BV_{DSS} / \Delta T_J$	Breakdown Voltage Temperature Coefficient	$I_D = 250\mu\text{A}$, Referenced to 25°C	-	0.75	-	$V/^\circ\text{C}$
I_{DSS}	Zero Gate Voltage Drain Current	$V_{DS} = 600\text{V}$, $V_{GS} = 0\text{V}$ $V_{DS} = 480\text{V}$, $T_C = 125^\circ\text{C}$	-	-	25 250	μA
I_{GSS}	Gate to Body Leakage Current	$V_{GS} = \pm 30\text{V}$, $V_{DS} = 0\text{V}$	-	-	± 10	μA

On Characteristics

$V_{GS(th)}$	Gate Threshold Voltage	$V_{GS} = V_{DS}$, $I_D = 250\mu\text{A}$	3.0	-	5.0	V
$R_{DS(on)}$	Static Drain to Source On Resistance	$V_{GS} = 10\text{V}$, $I_D = 2.25\text{A}$	-	1.7	2.0	Ω
g_{FS}	Forward Transconductance	$V_{DS} = 40\text{V}$, $I_D = 2.25\text{A}$	-	3.5	-	S

Dynamic Characteristics

C_{iss}	Input Capacitance	$V_{DS} = 25\text{V}$, $V_{GS} = 0\text{V}$ $f = 1\text{MHz}$	-	650	865	pF
C_{oss}	Output Capacitance		-	75	100	pF
C_{rss}	Reverse Transfer Capacitance		-	5	10	pF
Q_g	Total Gate Charge at 10V	$V_{DS} = 480\text{V}$, $I_D = 4.5\text{A}$ $V_{GS} = 10\text{V}$ (Note 4)	-	14.5	20	nC
Q_{gs}	Gate to Source Gate Charge		-	4	-	nC
Q_{gd}	Gate to Drain "Miller" Charge		-	6	-	nC

Switching Characteristics

$t_{d(on)}$	Turn-On Delay Time	$V_{DD} = 300\text{V}$, $I_D = 4.5\text{A}$ $R_G = 25\Omega$, $V_{GS} = 10\text{V}$ (Note 4)	-	19	48	ns
t_r	Turn-On Rise Time		-	25	60	ns
$t_{d(off)}$	Turn-Off Delay Time		-	25	60	ns
t_f	Turn-Off Fall Time		-	45	100	ns

Drain-Source Diode Characteristics

I _S	Maximum Continuous Drain to Source Diode Forward Current		-	-	4.5	A
I _{SM}	Maximum Pulsed Drain to Source Diode Forward Current		-	-	18	A
V _{SD}	Drain to Source Diode Forward Voltage	V _{GS} = 0V, I _{SD} = 4.5A	-	-	1.6	V
t _{rr}	Reverse Recovery Time	V _{GS} = 0V, I _{SD} = 4.5A dI _F /dt = 100A/μs	-	36	-	ns
Q _{rr}	Reverse Recovery Charge		-	37	-	nC

Notes:

- 1: Repetitive Rating: Pulse width limited by maximum junction temperature
- 2: $L = 15\text{mH}$, $I_{AS} = 4.5\text{A}$, $V_{DD} = 50\text{V}$, $R_G = 25\Omega$, Starting $T_J = 25^\circ\text{C}$
- 3: $I_{SD} \leq 4.5\text{A}$, $di/dt \leq 200\text{A}/\mu\text{s}$, $V_{DD} \leq BV_{DSS}$, Starting $T_J = 25^\circ\text{C}$
- 4: Essentially Independent of Operating Temperature Typical Characteristics

Typical Performance Characteristics

Figure 1. On-Region Characteristics

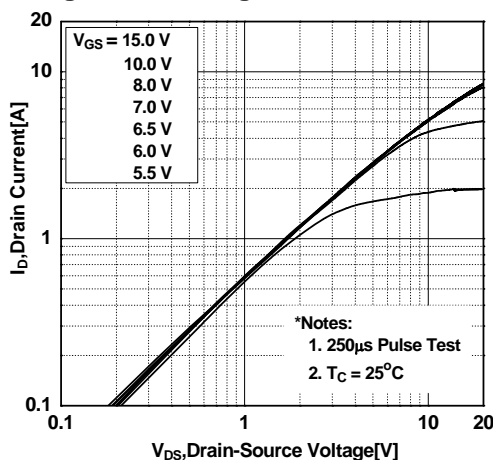


Figure 2. Transfer Characteristics

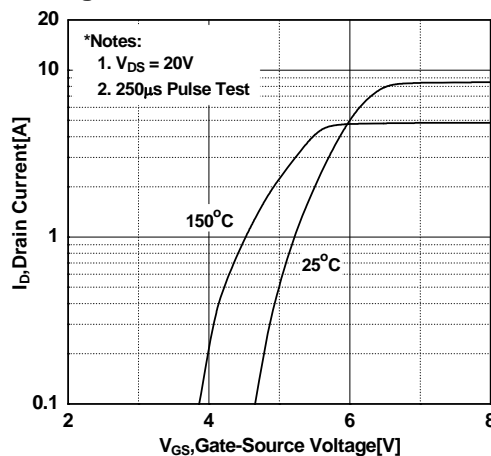


Figure 3. On-Resistance Variation vs. Drain Current and Gate Voltage

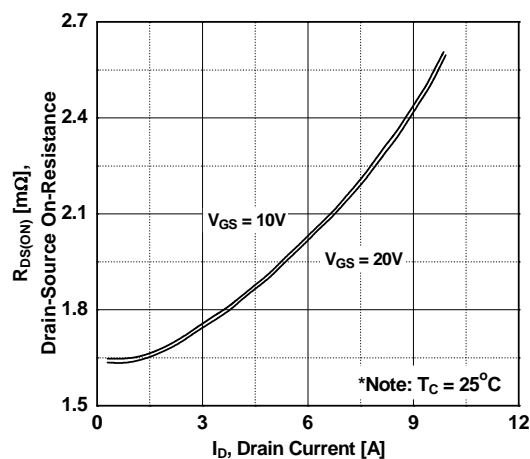


Figure 4. Body Diode Forward Voltage Variation vs. Source Current and Temperature

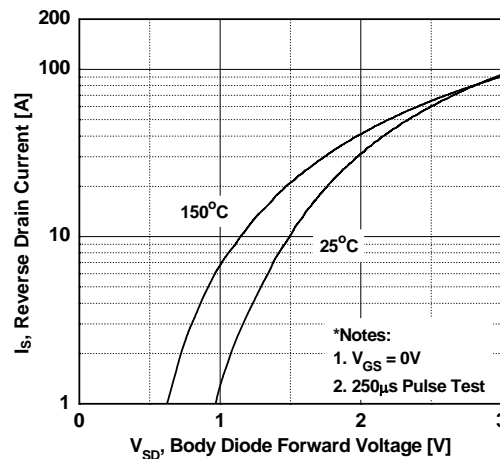


Figure 5. Capacitance Characteristics

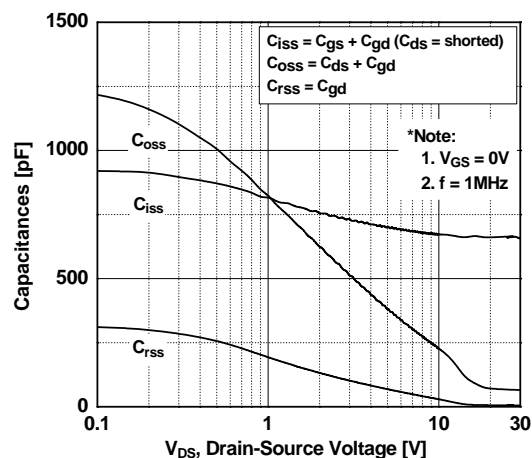
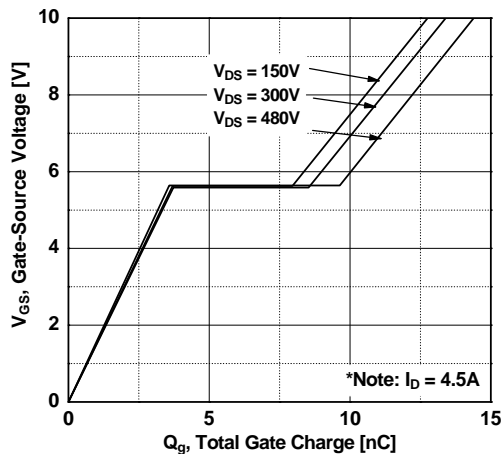


Figure 6. Gate Charge Characteristics



Typical Performance Characteristics (Continued)

Figure 7. Breakdown Voltage Variation vs. Temperature

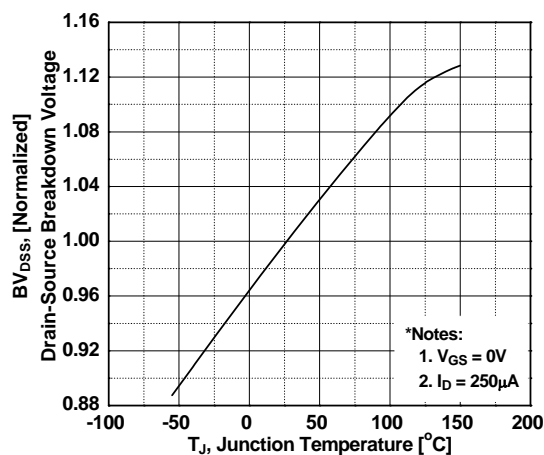


Figure 8. Maximum Safe Operating Area - FDPF6N60ZUT

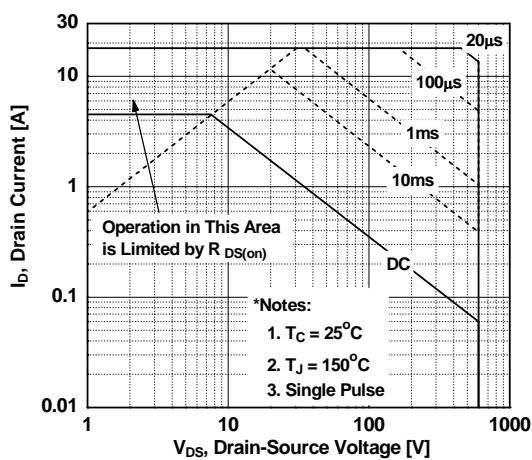


Figure 9. Maximum Drain Current vs. Case Temperature

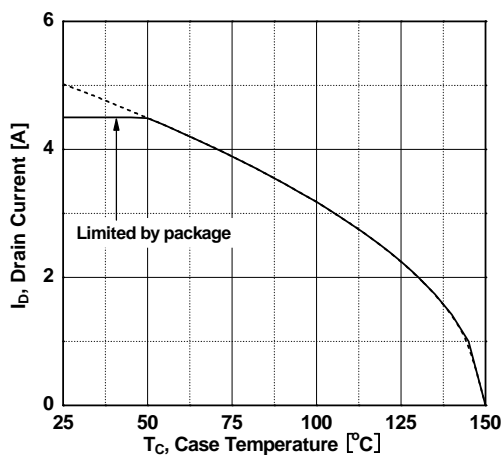
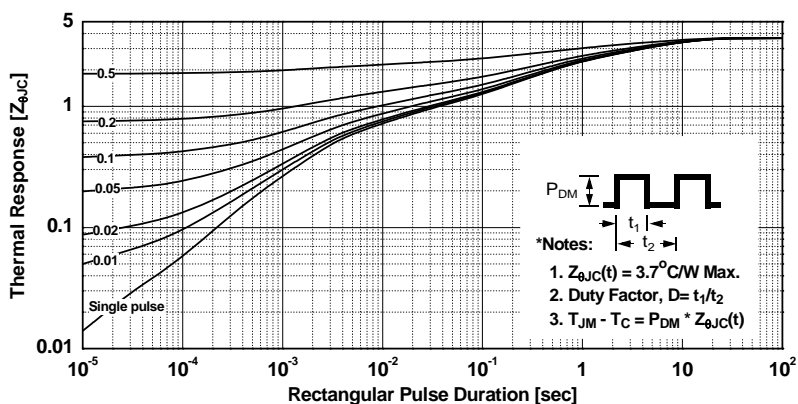
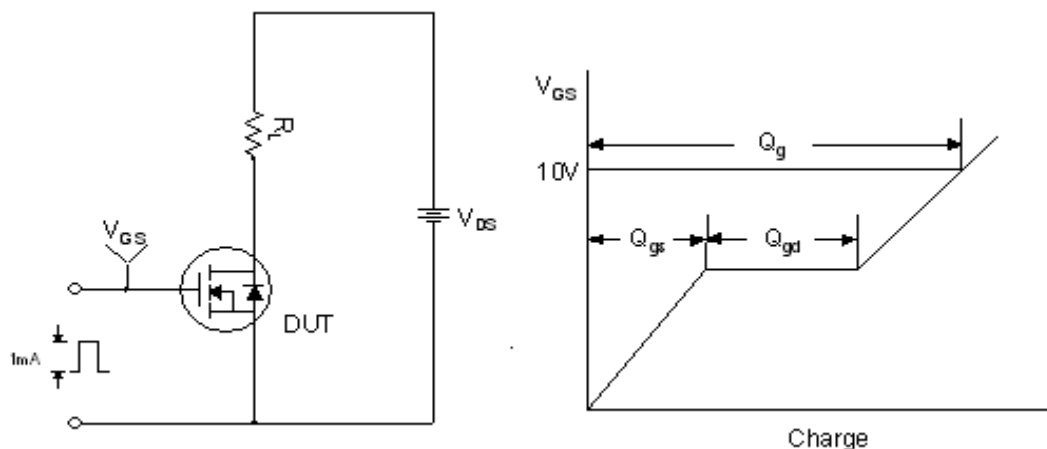


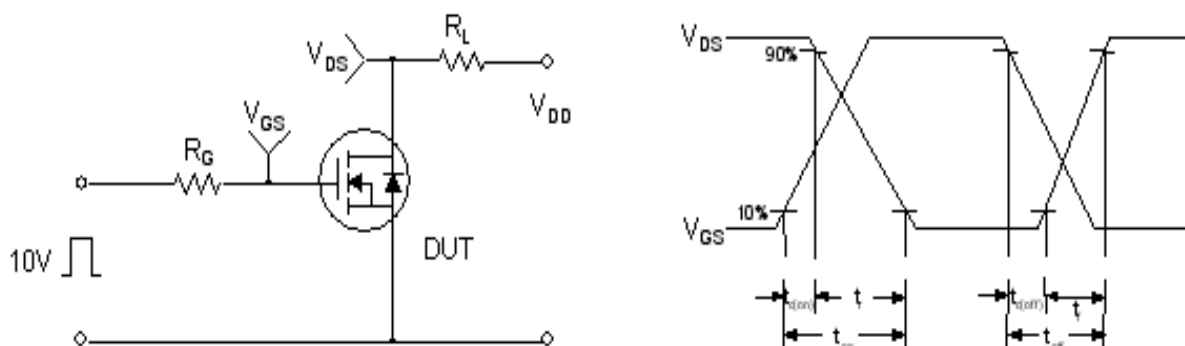
Figure 10. Transient Thermal Response Curve - FDPF6N60ZUT



Gate Charge Test Circuit & Waveform



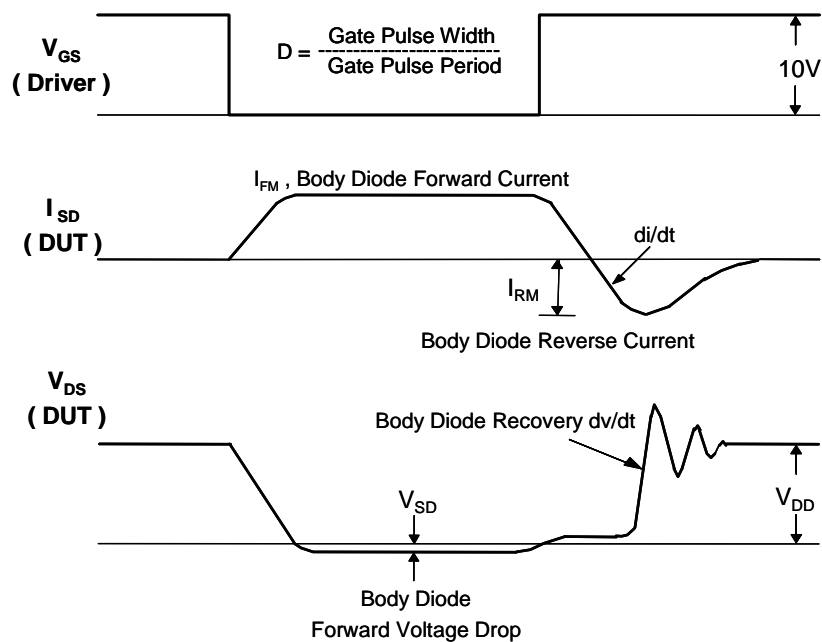
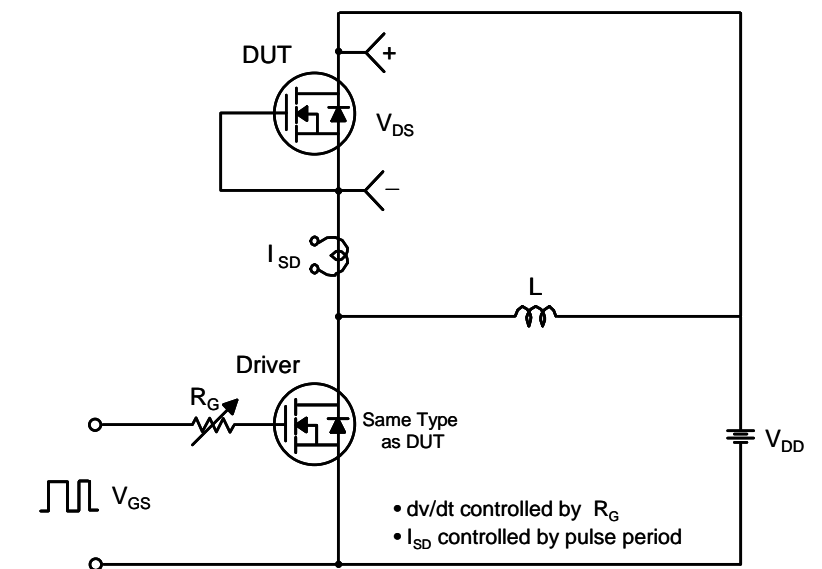
Resistive Switching Test Circuit & Waveforms




Unclamped Inductive Switching Test Circuit & Waveforms




Peak Diode Recovery dv/dt Test Circuit & Waveforms



2Cool™
 AccuPower™
 AX-CAP®*
 BitSiC™
 Build it Now™
 CorePLUS™
 CorePOWER™
 CROSSVOLT™
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