

FDP33N25 N-Channel UniFETTM MOSFET

250 V, 33 A, 94 m Ω

Features

- $R_{DS(on)}$ = 94 m Ω (Max.) @ V_{GS} = 10 V, I_D = 16.5 A
- Low Gate Charge (Typ.36.8 nC)
- Low Crss (Typ. 39 pF)
- 100% Avalanche Tested

Applications

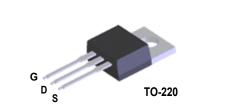
- PDP TV
- Lighting
- Uninterruptible Power Supply
- AC-DC Power Supply

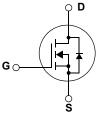
March 2013



Description

UniFETTM MOSFET is Fairchild Semiconductor[®], shigh voltage MOSFET family based on planar stripe and DMOS technology. This MOSFET is tailored to reduce on-state resistance, and to provide better switching performance and higher avalanche energy strength. This device family is suitable for switching power converter applications such as power factor correction (PFC), flat panel display (FPD) TV power, ATX and electronic lamp ballasts.





Absolute Maximum Ratings

Symbol	Parameter			FDP33N25	Unit		
V _{DSS}	Drain-Source Voltag	e		250	V		
I _D	Drain Current	- Continuous (T _C = 25°C) - Continuous (T _C = 100°C)		33 20.4	A A		
I _{DM}	Drain Current - Pulsed		(Note 1)	132	А		
V _{GSS}	Gate-Source voltage			± 30	V		
E _{AS}	Single Pulsed Avalanche Energy		(Note 2)	918	mJ		
I _{AR}	Avalanche Current		Avalanche Current		(Note 1)	33	A
E _{AR}	Repetitive Avalanche Energy		(Note 1)	23.5	mJ		
dv/dt	Peak Diode Recovery dv/dt		(Note 3)	4.5	V/ns		
P _D	Power Dissipation	(T _C = 25°C) - Derate above 25°C		235 1.89	W W/°C		
T _{J,} T _{STG}	Operating and Storage Temperature Range			-55 to +150	°C		
TL	Maximum Lead Temperature for Soldering Purpose, 1/8" from Case for 5 Seconds			300	°C		

Thermal Characteristics

Symbol	Parameter	FDP33N25	Unit	
$R_{\theta JC}$	Thermal Resistance, Junction-to-Case, Max.	0.53	°C/W	
$R_{ ext{ heta}CS}$	Thermal Resistance, Case-to-Sink, Typ,	0.5	°C/W	
R_{\thetaJA}	Thermal Resistance, Junction-to-Ambient, Max.	62.5	°C/W	

		Device	Pac	ckage	Reel Size	Тар	ape Width		Quantity	
		D-220 -			-		50			
Electric	al Chai	racteristics T _c	; = 25°C unle	ss otherwise n	oted					
Symbol	Parameter		Conditions		Min	Тур	Max	Unit		
Off Charac	teristics								-	
BV _{DSS}	Drain-Sou	Drain-Source Breakdown Voltage		$V_{GS} = 0V, I_D = 250\mu A, T_J = 25^{\circ}C$		250			V	
ΔΒV _{DSS} / ΔΤ _J	Breakdow Coefficier	eakdown Voltage Temperature		$I_D = 250 \mu A$, Referenced to 25°C			0.25		V/°C	
I _{DSS}	Zero Gate	Gate Voltage Drain Current		$V_{DS} = 250V, V_{GS} = 0V$ $V_{DS} = 200V, T_{C} = 125^{\circ}C$				1 10	μΑ μΑ	
I _{GSSF}	Gate-Bod	y Leakage Current, F	orward	V _{GS} = 30	V, V _{DS} = 0V				100	nA
I _{GSSR}	Gate-Bod	dy Leakage Current, Reverse		$V_{GS} = -30V, V_{DS} = 0V$				-100	nA	
On Charac	teristics							1		1
V _{GS(th)}	Gate Thre	hreshold Voltage		$V_{DS} = V_{GS}, I_{D} = 250 \mu A$		3.0		5.0	V	
R _{DS(on)}	Static Drain-Source On-Resistance		V _{GS} = 10V, I _D = 16.5A			0.077	0.094	Ω		
9 _{FS}	Forward ⁻	ard Transconductance		V _{DS} = 40V, I _D = 16.5A			26.6		S	
Dynamic C	haracteris	tics								1
C _{iss}	Input Cap	acitance	V _{DS} = 25V, V _{GS} = 0V,				1640	2135	pF	
C _{oss}	Output Ca	Capacitance Transfer Capacitance		f = 1.0MHz			330	430	pF	
C _{rss}	Reverse ⁻						39	59	pF	
Switching	Characteri	istics								
t _{d(on)}	Turn-On Delay Time		V _{DD} = 125V, I _D = 33A			35	80	ns		
t _r	Turn-On I	Rise Time		R _G = 25Ω (Note 4)				230	470	ns
t _{d(off)}	Turn-Off [Delay Time						75	160	ns
t _f	Turn-Off I	all Time				(Note 4)		120	250	ns
Qg	Total Gate	e Charge		V _{DS} = 200V, I _D = 33A V _{GS} = 10V (Note 4)				36.8	48	nC
Q _{gs}	Gate-Sou	rce Charge						10		nC
Q _{gd}	Gate-Dra	in Charge					17		nC	
	ce Diode (Characteristics and	Maximun	n Ratings						
I _S	Maximum Continuous Drain-Source Dio		de Forward Current				33	Α		
I _{SM}	Maximum Pulsed Drain-Source Diode Fo		orward Current				132	Α		
V _{SD}	Drain-Sou	urce Diode Forward V	/oltage	$V_{GS} = 0V$, I _S = 33A				1.4	V
t _{rr}	Reverse I	Recovery Time			, I _S = 33A			220		ns
Q _{rr}	Reverse I	Recovery Charge		$dI_F/dt = 10$	00A/μs			1.71		μC

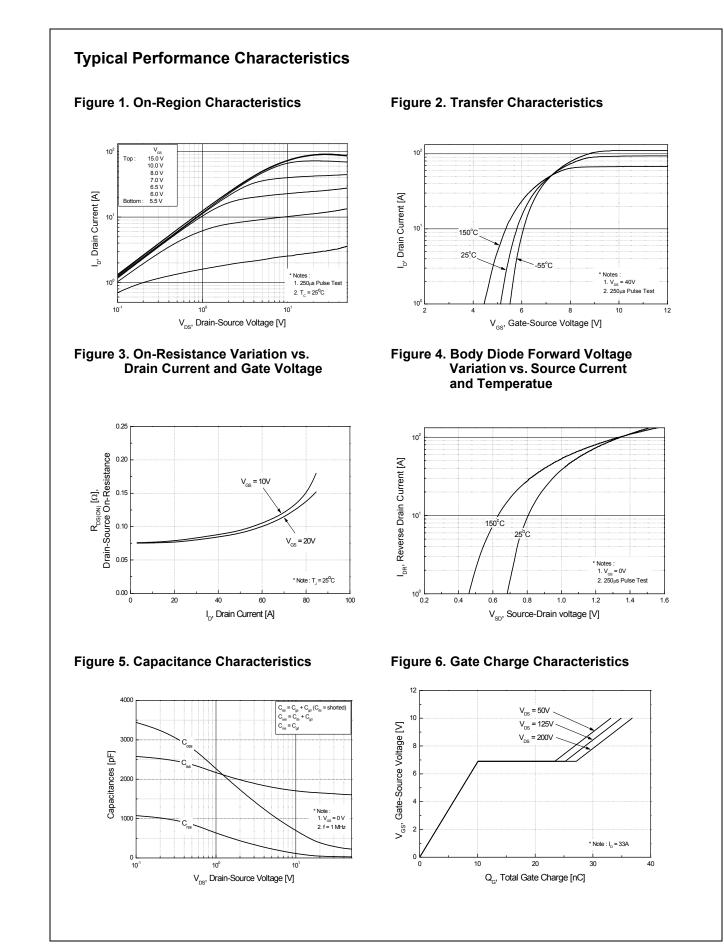
Notes:

1. Repetitive Rating: Pulse width limited by maximum junction temperature

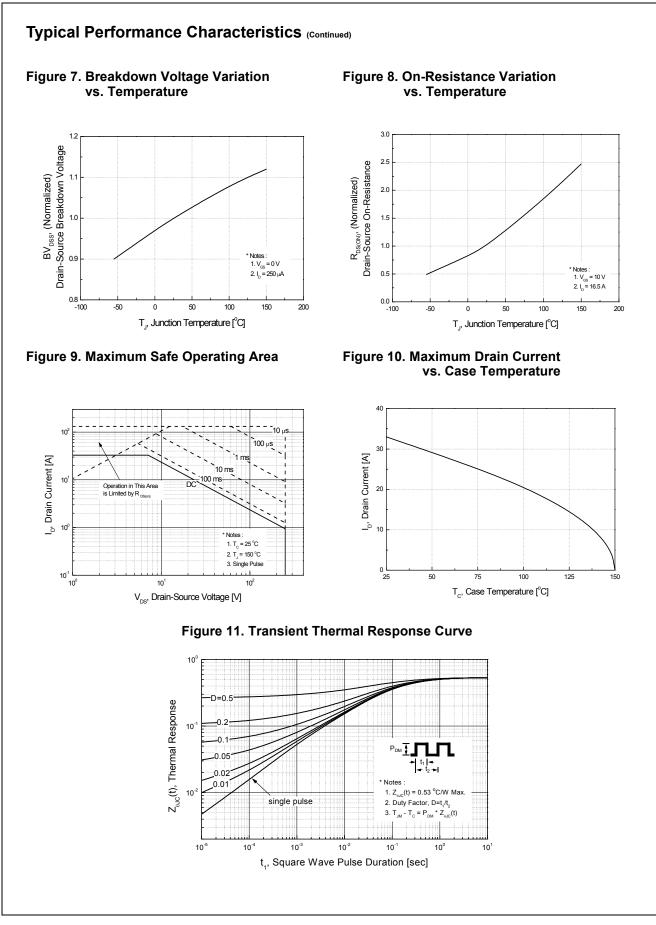
2. L = 1.35mH, I_{AS} = 33A, V_{DD} = 50V, R_G = 25 Ω , Starting T_J = 25°C

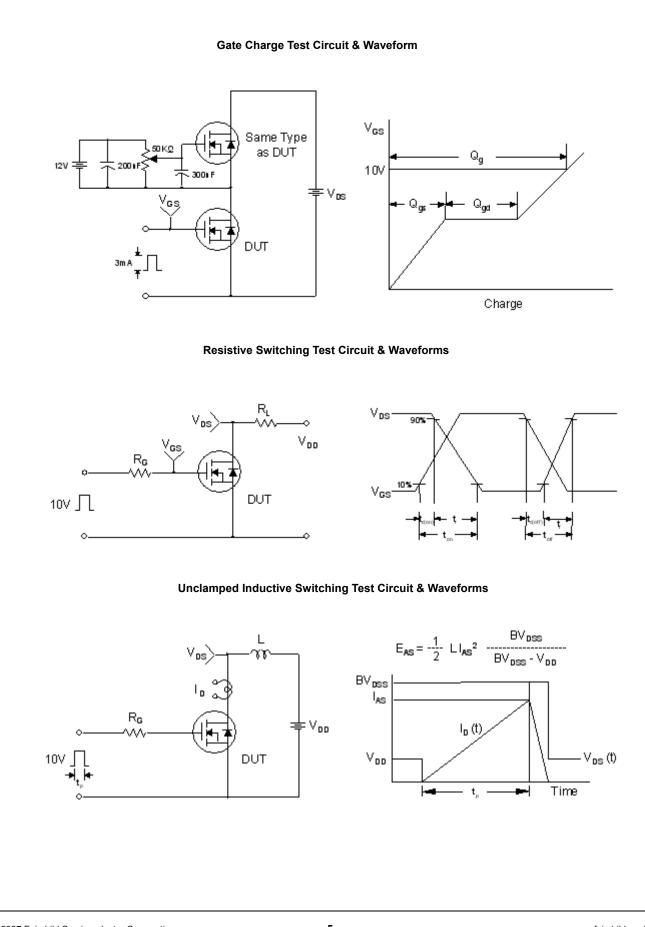
3. I_{SD} \leq 33A, di/dt \leq 200A/ $\mu s,~V_{DD} \leq$ BV_{DSS}, Starting T_J = 25°C

4. Essentially Independent of Operating Temperature Typical Characteristics

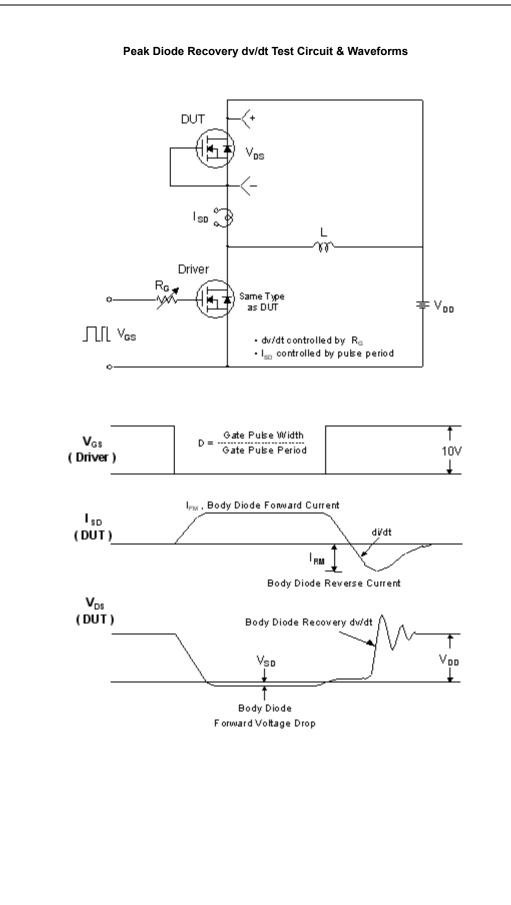


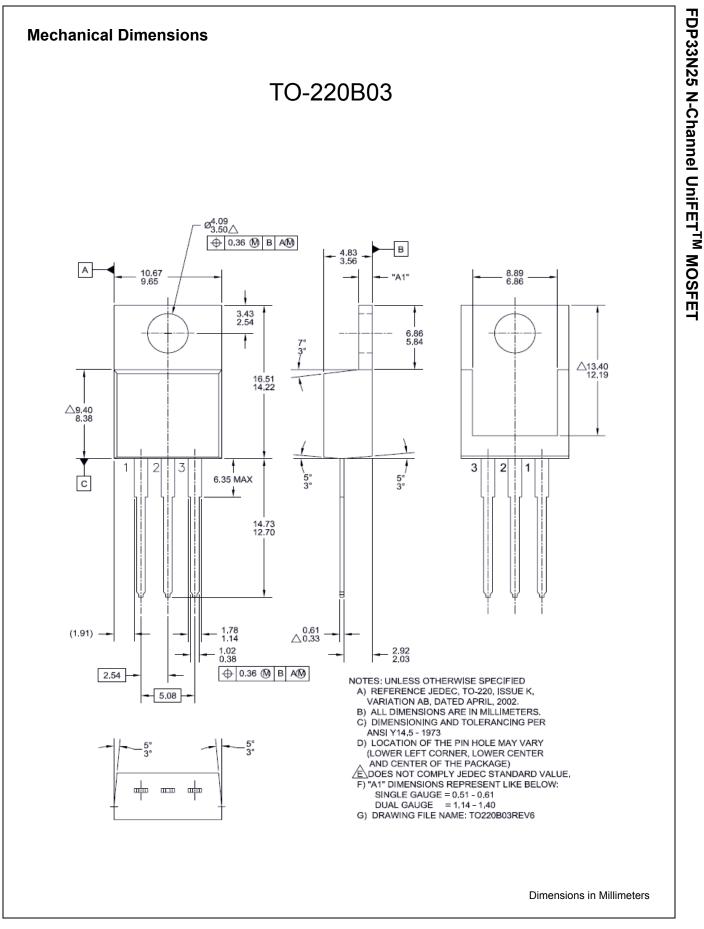
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