

March 2013

## **FDP036N10A**

# N-Channel PowerTrench<sup>®</sup> MOSFET 100 V, 214 A, 3.6 m $\Omega$

#### **Features**

- $R_{DS(on)} = 3.2 \text{ m}\Omega$  ( Typ.)@  $V_{GS} = 10 \text{ V}$ ,  $I_D = 75 \text{ A}$
- · Fast Switching Speed
- Low Gate Charge, Q<sub>G</sub> = 89 nC(Typ.)
- High Performance Trench Technology for Extremely Low  $R_{\mbox{\scriptsize DS(on)}}$
- · High Power and Current Handling Capability
- · RoHS Compliant

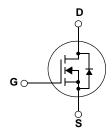
### **Description**

This N-Channel MOSFET is produced using Fairchild Semiconductor<sup>®</sup>'s PowerTrench<sup>®</sup> process that has been tailored to minimize the on-state resistance while maintaining superior switching performance.

## **Applications**

- Synchronous Rectification for ATX / Server / Telecom PSU
- · Battery Protection Circuit
- Motor Drives and Uninterruptible Power Supplies
- Micro Solar Inverter





#### **MOSFET Maximum Ratings** T<sub>C</sub> = 25°C unless otherwise noted

| Symbol                            |   | Parameter  | FDP036N10A  | Unit |
|-----------------------------------|---|--|-------------|------|
| V <sub>DSS</sub>                  | Drain to Source Voltage                           |  | 100         | V    |
| V <sub>GSS</sub>                  | Gate to Source Voltage                            |  | ±20         | V    |
|                                   |   |  | 214*        |      |
| I <sub>D</sub>                    |   |  | 151*        | Α    |
|                                   |   | -Continuous (T <sub>C</sub> = 25°C, Package Limited) | 120         |      |
| DM                                | Drain Current                                     | - Pulsed (Note 1)                                    | 856         | А    |
| E <sub>AS</sub>                   | Single Pulsed Avalanche E                         | ulsed Avalanche Energy (Note 2)                      |             | mJ   |
| dv/dt                             | Peak Diode Recovery dv/d                          | t (Note 3)   | 6.0         | V/ns |
| n                                 | Dower Discipation                                 | $(T_C = 25^{\circ}C)$                                | 333         | W    |
| $P_{D}$                           | Power Dissipation - Derate above 25°C             |  | 2.22        | W/ºC |
| Γ <sub>J</sub> , Τ <sub>STG</sub> | Operating and Storage Ten                         | nperature Range                                      | -55 to +175 | °C   |
| T <sub>L</sub>                    | Maximum Lead Temperatu 1/8" from Case for 5 Secon | • • •  | 300         | °C   |

<sup>\*</sup>Calculated continuous current based on maximum allowable junction temperature. Package limitation current is 120A.

#### **Thermal Characteristics**

| Symbol          | Parameter FDP036N10A                            |                                  |  |  |
|-----------------|---|----------------------------------|--|--|
| $R_{\theta JC}$ | Thermal Resistance, Junction to Case, Max. 0.45 |                                  |  |  |
| $R_{\theta JA}$ | Thermal Resistance, Junction to Ambient, Max.   | , Junction to Ambient, Max. 62.5 |  |  |

# **Package Marking and Ordering Information**

| Device Marking | Device     | Package | Reel Size | Tape Width | Quantity |
|----------------|------------|---------|-----------|------------|----------|
| FDP036N10A     | FDP036N10A | TO-220  | =         | -          | 50       |

# **Electrical Characteristics** $T_C = 25^{\circ}C$ unless otherwise noted

| Symbol                                 | Parameter                                    | Test Conditions  | Min. | Тур. | Max. | Unit |
|--|--|--|------|------|------|------|
| Off Charac                             | eteristics                                   |  |      |      |      |      |
| BV <sub>DSS</sub>                      | Drain to Source Breakdown Voltage            | $I_D = 250\mu A$ , $V_{GS} = 0V$ , $T_C = 25^{\circ}C$ | 100  | -    | -    | V    |
| $\frac{\Delta BV_{DSS}}{\Delta T_{J}}$ | Breakdown Voltage Temperature<br>Coefficient | I <sub>D</sub> = 250μA, Referenced to 25°C             | -    | 0.03 | -    | V/°C |
| 1                                      | Zero Gate Voltage Drain Current              | $V_{DS} = 80V, V_{GS} = 0V$                            | -    | -    | 1    | μА   |
| IDSS                                   | Zero Gate voltage Drain Current              | $V_{DS} = 80V, T_{C} = 150^{\circ}C$                   | -    | -    | 500  | μΑ   |
| $I_{GSS}$                              | Gate to Body Leakage Current                 | $V_{GS} = \pm 20V, V_{DS} = 0V$                        | -    | -    | ±100 | nA   |

#### **On Characteristics**

| V <sub>GS(th)</sub> | Gate Threshold Voltage               | $V_{GS} = V_{DS}, I_{D} = 250 \mu A$ | 2.0 | 3.0 | 4.0 | V  |
|---------------------|--------------------------------------|--------------------------------------|-----|-----|-----|----|
| R <sub>DS(on)</sub> | Static Drain to Source On Resistance | $V_{GS} = 10V, I_D = 75A$            | -   | 3.2 | 3.6 | mΩ |
| g <sub>FS</sub>     | Forward Transconductance             | $V_{DS} = 10V, I_{D} = 75A$          | -   | 167 | -   | S  |

#### **Dynamic Characteristics**

| C <sub>iss</sub>    | Input Capacitance                | V 05V V 0V                              | - | 5485 | 7295 | pF |
|---------------------|----------------------------------|---|---|------|------|----|
| C <sub>oss</sub>    | Output Capacitance               | $V_{DS} = 25V, V_{GS} = 0V$<br>f = 1MHz |   | 2430 | 3230 | pF |
| C <sub>rss</sub>    | Reverse Transfer Capacitance     | 1 - 11/11/2                             | - | 210  | 315  | pF |
| Q <sub>g(tot)</sub> | Total Gate Charge at 10V         |   | - | 89   | 116  | nC |
| $Q_{gs}$            | Gate to Source Gate Charge       | $V_{DS} = 80V, I_{D} = 75A$             | - | 24   | -    | nC |
| Q <sub>gs2</sub>    | Gate Charge Threshold to Plateau | V <sub>GS</sub> = 10V                   | - | 8    | -    | nC |
| $Q_{qd}$            | Gate to Drain "Miller" Charge    | (Note 4                                 | - | 25   | -    | nC |

#### **Switching Characteristics**

| t <sub>d(on)</sub>  | Turn-On Delay Time                 |                                     | -    | 22  | 54  | ns |
|---------------------|------------------------------------|-------------------------------------|------|-----|-----|----|
| t <sub>r</sub>      | Turn-On Rise Time                  | $V_{DD} = 50V, I_{D} = 75A$         | -    | 54  | 118 | ns |
| t <sub>d(off)</sub> | Turn-Off Delay Time                | $V_{GS} = 10V, R_{GEN} = 4.7\Omega$ | -    | 37  | 84  | ns |
| t <sub>f</sub>      | Turn-Off Fall Time                 | (Note                               | 4) - | 11  | 32  | ns |
| ESR                 | Equivalent Series Resistance (G-S) | f = 1MHz                            | -    | 1.2 | -   | Ω  |

#### **Drain-Source Diode Characteristics**

| I <sub>S</sub>  | Maximum Continuous Drain to Source Diode Forward Current |   |   | -   | 214  | Α  |
|-----------------|--|---|---|-----|------|----|
| I <sub>SM</sub> | Maximum Pulsed Drain to Source Diode Forward Current     |   | - | -   | 856  | Α  |
| V <sub>SD</sub> | Drain to Source Diode Forward Voltage                    | V <sub>GS</sub> = 0V, I <sub>SD</sub> = 75A | - | -   | 1.25 | V  |
| t <sub>rr</sub> | Reverse Recovery Time                                    | V <sub>GS</sub> = 0V, I <sub>SD</sub> = 75A | - | 72  | 93.6 | ns |
| $Q_{rr}$        | Reverse Recovery Charge                                  | $dI_F/dt = 100A/\mu s$                      | - | 129 | -    | nC |

#### Notes

- Repetitive Rating: Pulse width limited by maximum junction temperature
- 2. Starting  $T_J = 25^{\circ}C$ , L = 1mH,  $I_{SD} = 36.3A$
- 3.  $I_{SD} \le 75 A$ , di/dt  $\le 200 A/\mu s$ ,  $V_{DD} \le BV_{DSS}$ , Starting  $T_J = 25 ^{\circ}C$
- 4. Essentially Independent of Operating Temperature Typical Characteristics

# **Typical Performance Characteristics**

Figure 1. On-Region Characteristics

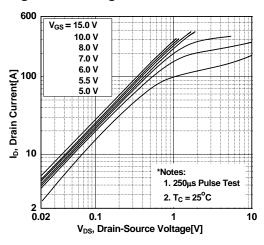


Figure 3. On-Resistance Variation vs.
Drain Current and Gate Voltage

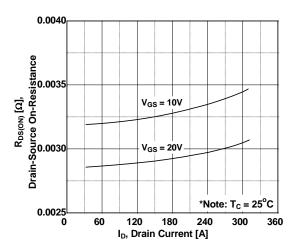


Figure 5. Capacitance Characteristics

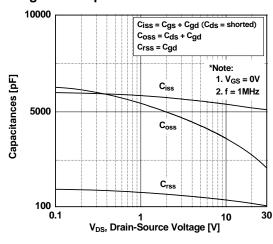


Figure 2. Transfer Characteristics

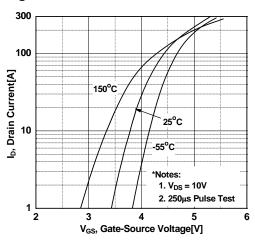


Figure 4. Body Diode Forward Voltage Variation vs. Source Current and Temperature

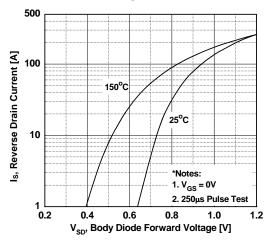
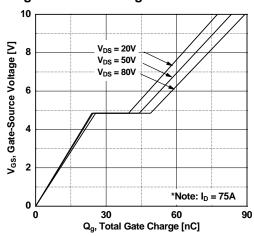


Figure 6. Gate Charge Characteristics



# **Typical Performance Characteristics**

Figure 7. Breakdown Voltage Variation vs. Temperature

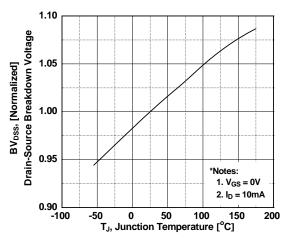


Figure 9. Maximum Safe Operating Area

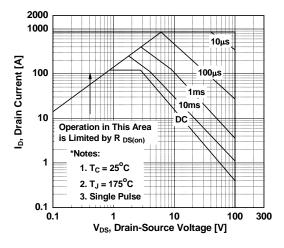


Figure 8. On-Resistance Variation vs. Temperature

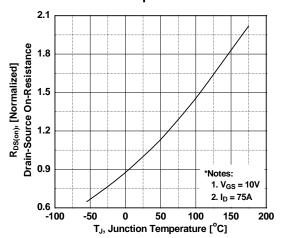


Figure 10. Maximum Drain Current vs. Case Temperature

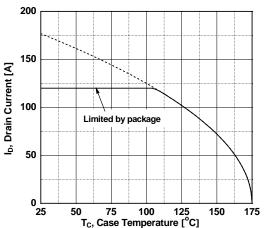
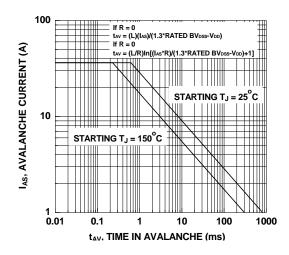
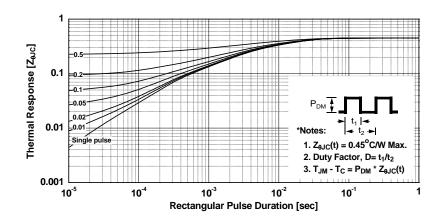


Figure 11. Unclamped Inductive Switching Capability

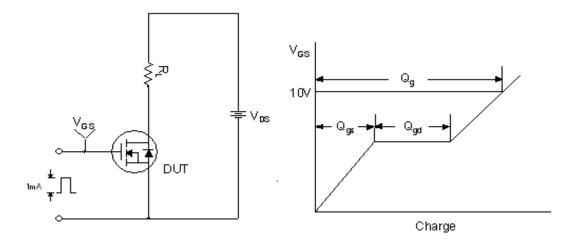


# **Typical Performance Characteristics**

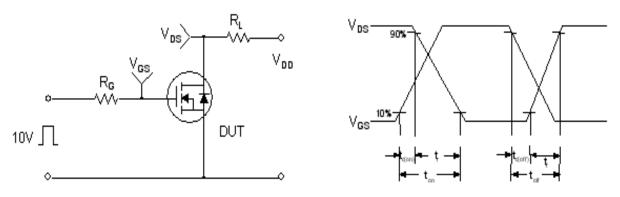
Figure 12. Transient Thermal Response Curve



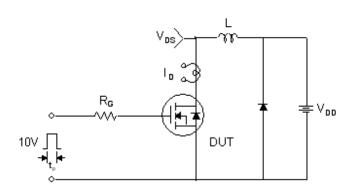
#### **Gate Charge Test Circuit & Waveform**

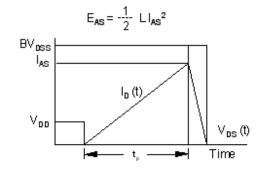


#### **Resistive Switching Test Circuit & Waveforms**

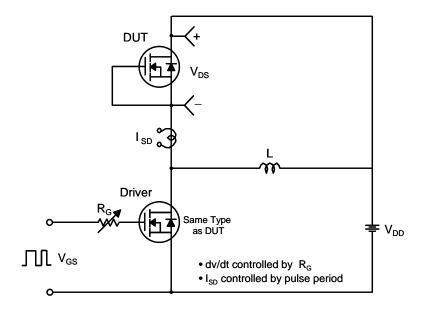


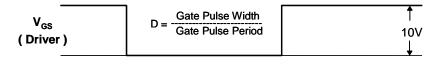
#### **Unclamped Inductive Switching Test Circuit & Waveforms**

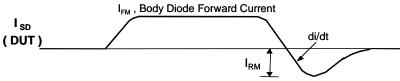




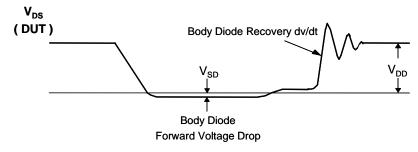
#### Peak Diode Recovery dv/dt Test Circuit & Waveforms





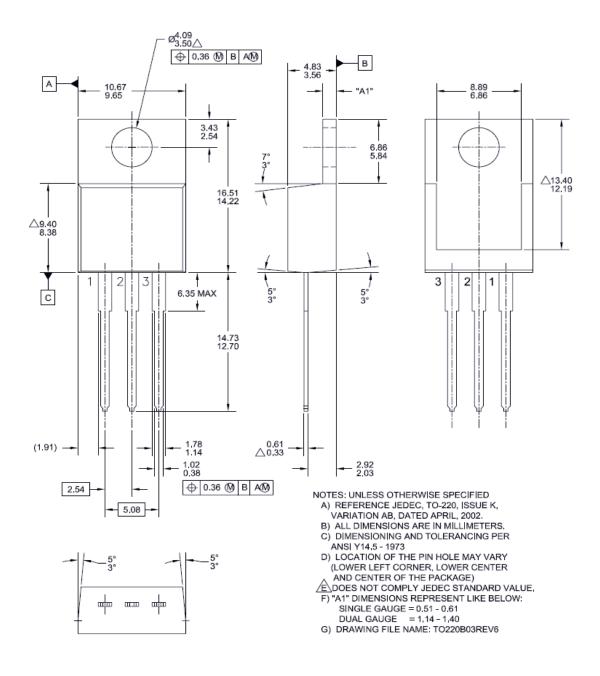


Body Diode Reverse Current



#### **Mechanical Dimensions**

# TO-220B03







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