FDP075N15A\_F102 / FDB075N15A N-Channel PowerTrench<sup>®</sup> MOSFET 150V, 130A, 7.5m $\Omega$ 

# Features

- R<sub>DS(on)</sub> = 6.25mΩ (Typ.)@ V<sub>GS</sub> = 10V, I<sub>D</sub> = 100A
- Fast Switching
- · Low Gate Charge

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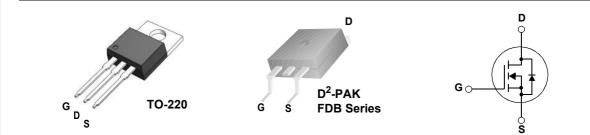
- High Performance Trench Technology for Extremely Low  $R_{\text{DS}(\text{on})}$
- High Power and Current Handling Capability
- RoHS Compliant

# Description

This N-Channel MOSFET is produced using Fairchild Semiconductor's advanced PowerTrench process that has been especially tailored to minimize the on-state resistance and yet maintain superior switching performance.

# Application

- DC to DC Converters
- Synchronous Rectification for Telecommunication PSU
- Battery Charger
- · AC motor drives and Uninterruptible Power Supplies
- Off-line UPS



## MOSFET Maximum Ratings T<sub>C</sub> = 25°C unless otherwise noted\*

Symbol		Parameter	FDP075N15A_F102 FDB075N15A	Units		
V <sub>DSS</sub>	Drain to Source Voltage			150	V	
V <sub>GSS</sub>	Gate to Source Voltage			±20	V	
1	Drain Current	-Continuous (T <sub>C</sub> = 25 <sup>o</sup> C)		130	•	
D	Drain Current	-Continuous (T <sub>C</sub> = 100 <sup>o</sup> C)		92	A	
I <sub>DM</sub>	Drain Current	- Pulsed	(Note 1)	522	А	
E <sub>AS</sub>	Single Pulsed Avalanche Ene	rgy	(Note 2)	588	mJ	
dv/dt	Peak Diode Recovery dv/dt		(Note 3)	6.0	V/ns	
P <sub>D</sub>	Device Dissingtion	(T <sub>C</sub> = 25°C)		333	W	
	Power Dissipation	- Derate above 25°C		2.22	W/ <sup>o</sup> C	
T <sub>J</sub> , T <sub>STG</sub>	Operating and Storage Temperature Range			-55 to +175	°C	
TL	Maximum Lead Temperature for Soldering Purpose, 1/8" from Case for 5 Seconds			300	°C	

\*Package limitation current is 120A.

# Thermal Characteristics

Symbol	Parameter	FDP075N15A_F102 FDB075N15A	Units
$R_{\theta JC}$	Thermal Resistance, Junction to Case	0.45	°C/W
R <sub>0JA</sub> Thermal Resistance, Junction to Ambient		62.5	C/W

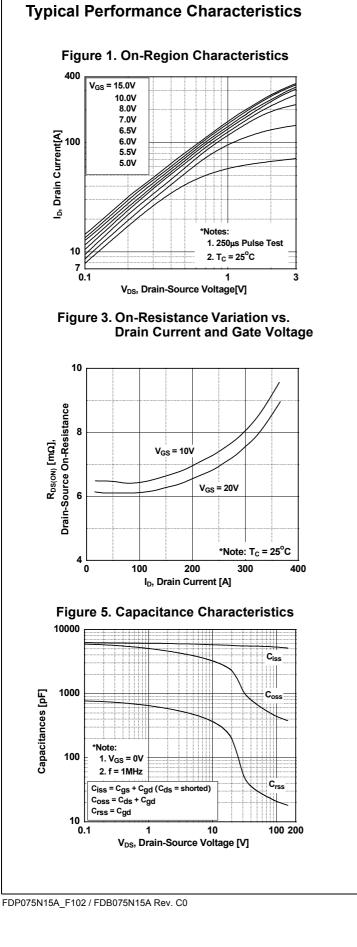
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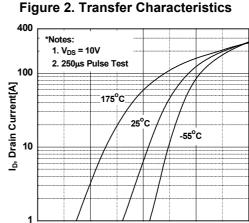


Device Marking		Device Packag		e	Desc	ription			Quantity		
FDP075N	N15A	FDP075N15A_F102	TO-22	C	F102: Trim	med Lead	ds		50		
Device Marking Device Packa		Packar	ge Reel Size Tape			o Width		Quantity			
FDB075N		FDB075N15A	Package D2-PAK		330mm	-	e Width 24mm		800	,	
		racteristics T <sub>c</sub> = :									
Symbol		Parameter		Test Conditions			Min.	Тур.	Max.	Units	
Off Charac	teristi	cs						51			
BV <sub>DSS</sub>		to Source Breakdown Vo	Itage	I <sub>D</sub> = 250μA, V <sub>GS</sub> = 0V			150	-	-	V	
ΔBV <sub>DSS</sub> ΔTJ		down Voltage Temperatu	-	$I_D = 250 \mu A$ , Referenced to $25^{\circ}C$			-	0.1	-	V/ºC	
	Zero (			V <sub>DS</sub> = 120V, V <sub>GS</sub> = 0V			-	-	1		
DSS	2010 0	Sate Voltage Drain Curre			120V, T <sub>C</sub> = 150 <sup>o</sup> C		-	-	500	μA	
GSS	Gate t	o Body Leakage Current		$V_{GS}$ =	±20V, V <sub>DS</sub> = 0V		-	-	±100	μA	
On Charac	teristic	cs									
V <sub>GS(th)</sub>	Gate 1	Threshold Voltage			V <sub>DS</sub> , I <sub>D</sub> = 250μA		2.0	-	4.0	V	
R <sub>DS(on)</sub>	Static	Drain to Source On Resi	stance	V <sub>GS</sub> =	V <sub>GS</sub> = 10V, I <sub>D</sub> = 100A		-	6.25	7.5	mΩ	
9 <sub>FS</sub>	Forwa	ard Transconductance		V <sub>DS</sub> =	10V, I <sub>D</sub> = 100A	(Note 4)	-	164	-	S	
Dynamic C	haract	teristics									
C <sub>iss</sub>	Input C	Capacitance					-	5525	7350	pF	
C <sub>oss</sub>	Output	t Capacitance		−V <sub>DS</sub> = 75V, V <sub>GS</sub> = 0V f = 1MHz		-	516	685	pF		
C <sub>rss</sub>	Revers	se Transfer Capacitance				-	21	-	pF		
C <sub>oss(er)</sub>	Energy	y Related Output Capacit	ance	V <sub>DS</sub> =	75V, V <sub>GS</sub> = 0V		-	909	-	pF	
Q <sub>g(tot)</sub>	Total G	Sate Charge at 10V					-	77	100	nC	
Q <sub>gs</sub>	Gate to	o Source Gate Charge		V <sub>DS</sub> = 75V, I <sub>D</sub> = 100A V <sub>GS</sub> = 10V		-	26	-	nC		
Q <sub>gs2</sub>	Gate C	Charge Threshold to Plate	eau			-	11	-	nC		
Q <sub>gd</sub>	Gate to	o Drain "Miller" Charge					-	16	-	nC	
ESR	Equiva	alent Series Resistance(	G-S)	Drain Open, f = 1MHz			-	2.29	-	Ω	
Switching	Chara	cteristics									
t <sub>d(on)</sub>	Turn-C	n Delay Time					-	28	66	ns	
t <sub>r</sub>	Turn-C	Turn-On Rise Time Turn-Off Delay Time		$V_{DD} = 75V, I_D = 100A$ $V_{GS} = 10V, R_{GEN} = 4.7\Omega$			-	37	84	ns	
t <sub>d(off)</sub>	Turn-C						-	62	134	ns	
t <sub>f</sub>		Furn-Off Fall Time					-	21	52	ns	
Drain-Sou	rce Dic	de Characteristics	;								
I <sub>S</sub>	Maxim	um Continuous Drain to	Source Diod	e Forwa	rd Current		-	-	130	Α	
I <sub>SM</sub>	Maxim	num Pulsed Drain to Source Diode Fo		prward Current			-	-	520	Α	
V <sub>SD</sub>	Drain t	o Source Diode Forward	Voltage	V <sub>GS</sub> = 0V, I <sub>SD</sub> = 100A			-	-	1.25	V	
rr	Revers	e Recovery Time		$V_{GS} = 0V, V_{DD} = 75V, I_{SD} = 100A$		-	97	-	ns		
	David	verse Recovery Charge		$dI_F/dt = 100A/\mu s$			-	264	-	nC	

5. Essentially Independent of Operating Temperature Typical Characteristics

FDP075N15A\_F102/FDB075N15A N-Channel PowerTrench<sup>®</sup> MOSFET





3

2

## Figure 4. Body Diode Forward Voltage Variation vs. Source Current and Temperature

4

V<sub>GS</sub>, Gate-Source Voltage[V]

5

6

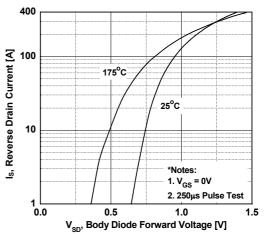
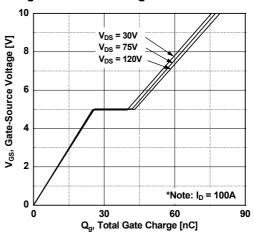
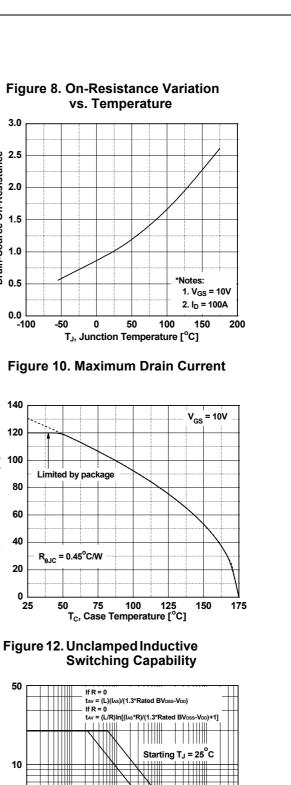
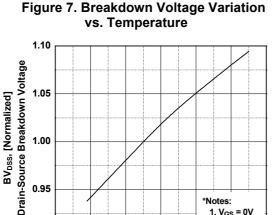


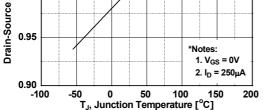
Figure 6. Gate Charge Characteristics





# Typical Performance Characteristics (Continued)







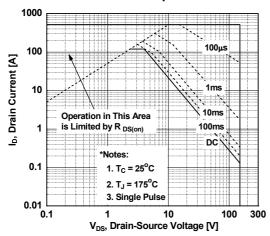
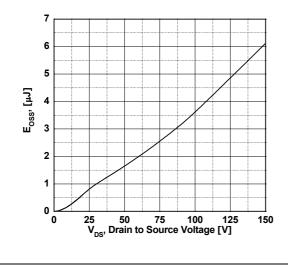


Figure 11. Eoss vs. Drain to Source Voltage





4

3.0

2.5

2.0

1.5

1.0

0.5

0.0

140

120

100

80

60

40

20

0

50

10

1 └─ 0.01

Starting T<sub>J</sub> = 150°C

0.1

1

tav, Time In Avalanche [ms]

10

I<sub>AS</sub>, Avalanche Current [A]

25

l<sub>b</sub>, Drain Current [A]

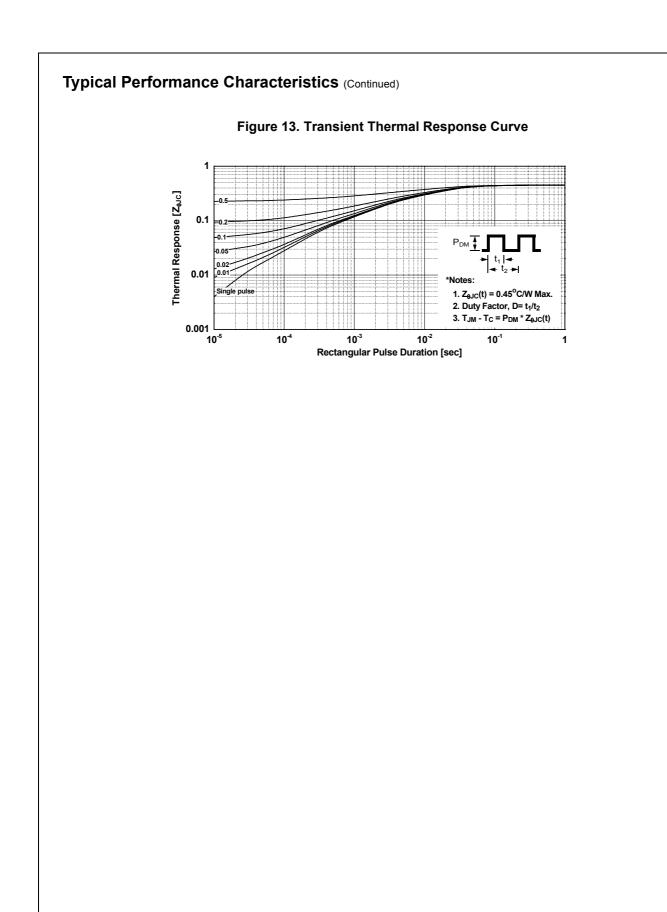
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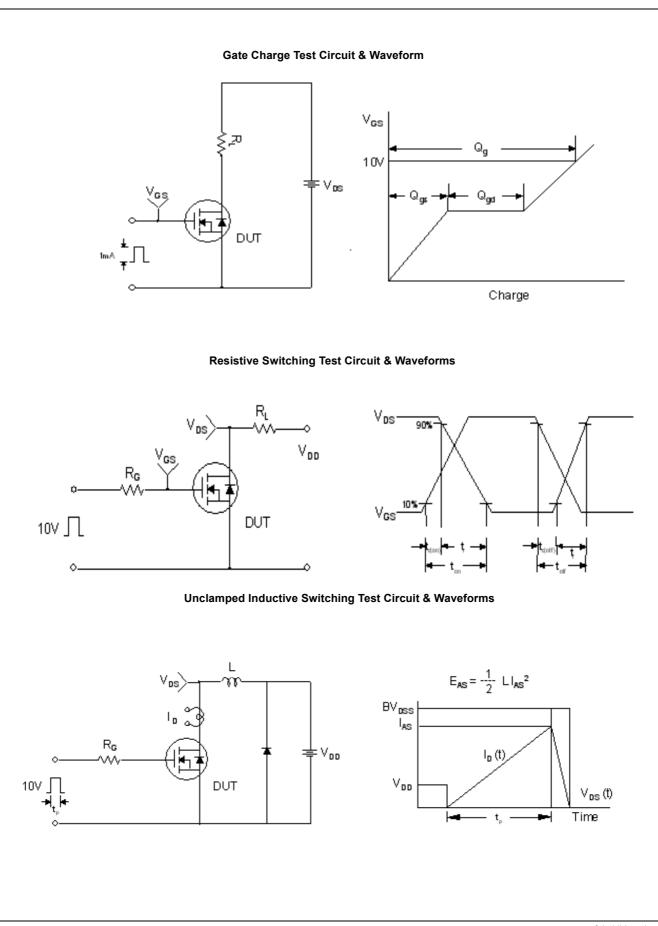
**Drain-Source On-Resistance** 

R<sub>DS(on)</sub>, [Normalized]

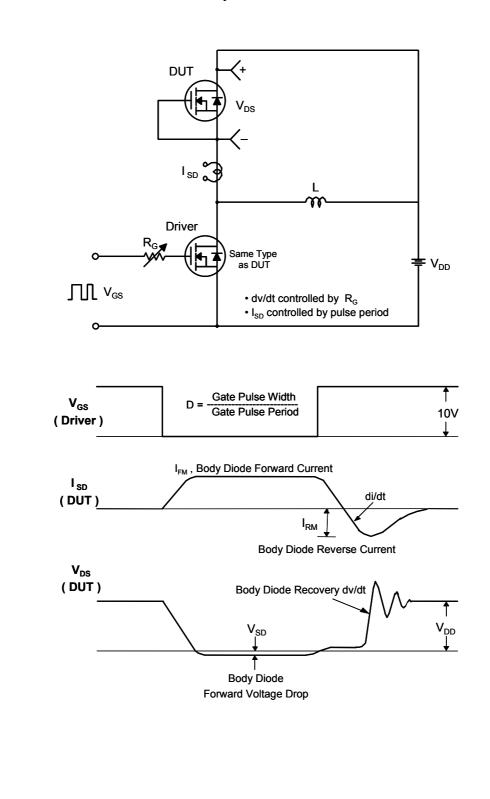
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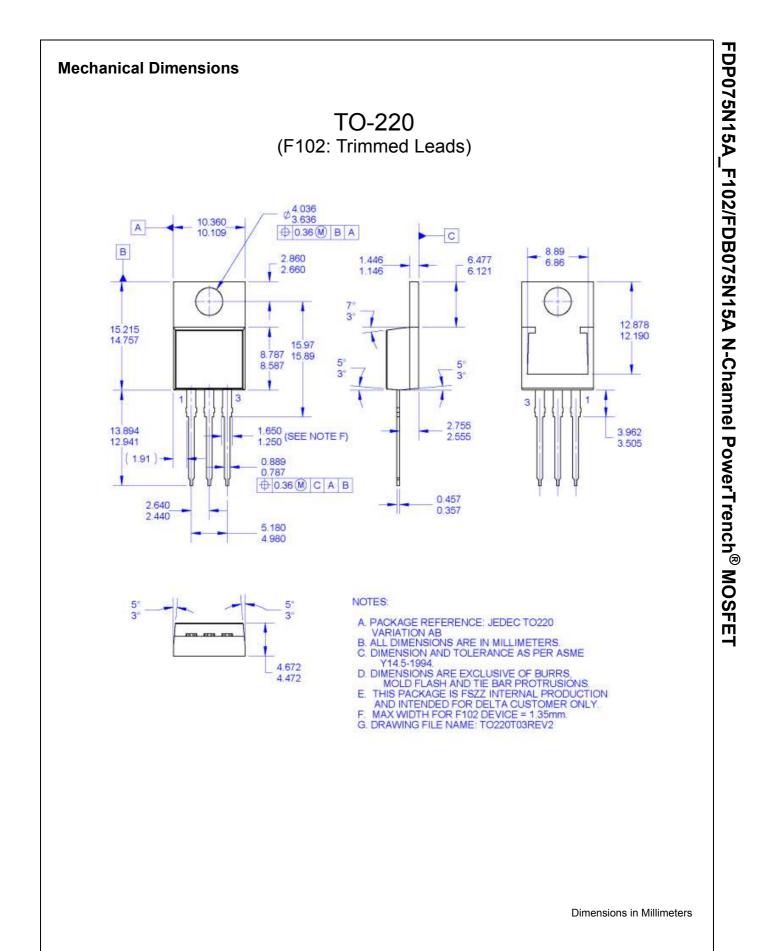
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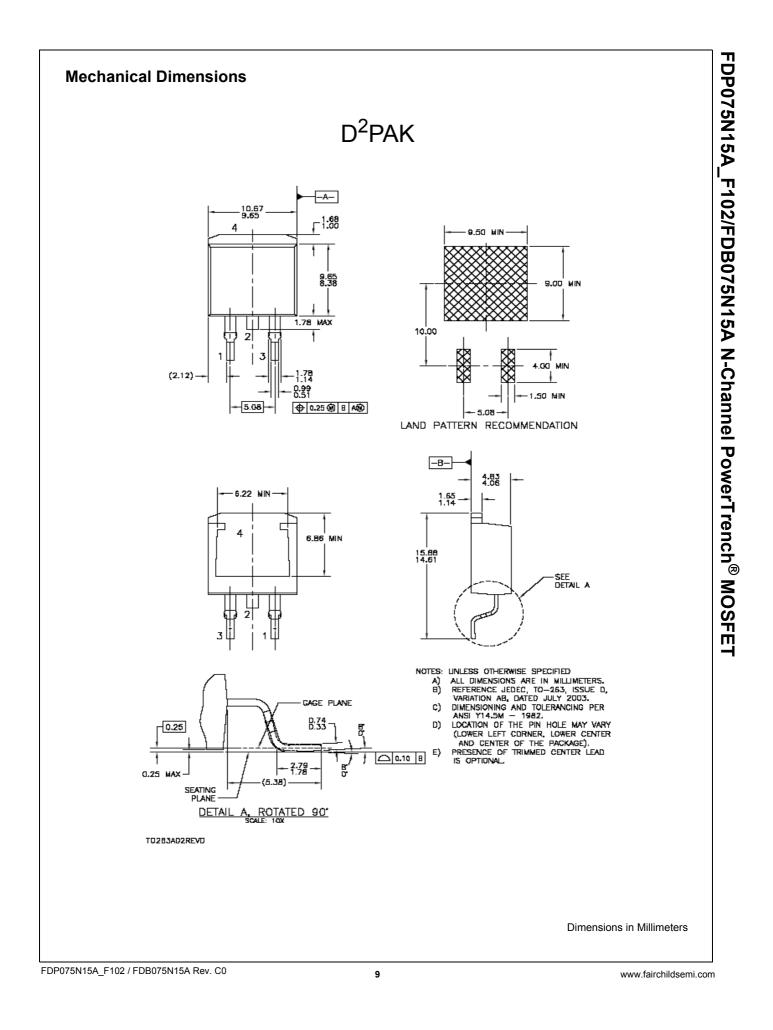




Peak Diode Recovery dv/dt Test Circuit & Waveforms









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