

March 2013

FDB088N08

N-Channel PowerTrench[®] MOSFET 75 V, 85 A, 8.8 m Ω

Features

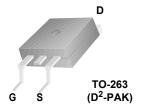
- $R_{DS(on)}$ = 7.3 m Ω (Typ.)@ V_{GS} = 10 V, I_D = 75 A
- · Fast Switching Speed
- · Low Gate Charge
- High Performance Trench Technology for Extremely Low $R_{DS(\text{on})}$
- · High Power and Current Handling Capability
- 100% Internal $R_{\rm q}$ Screening for Easy Paralleling Operation
- · RoHS Compliant

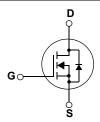
Description

This N-Channel MOSFET is produced using Fairchild Semiconductor®s adcanced PowerTrench® process that has been tailored to minimize the on-state resistance while maintaining superior switching performance.

Applications

- · Synchronous Rectification for ATX / Server / Telecom PSU
- · Battery Protection Circuit
- · Motor Drives and Uninterruptible Power Supplies





Absolute Maximum Ratings T_C = 25°C unless otherwise noted*

Symbol		Parameter		FDB088N08	Unit
V _{DSS}	Drain to Source Voltage			75	V
V _{GSS}	Gate to Source Voltage			±20	V
	Drain Current - C	ontinuous (T _C = 25°C, Silico	n Limited)	85*	Α
I _D		ontinuous (T _C = 100°C, Silico		60	Α
	- C	ontinuous (T _C = 25°C, Packa	ige Limited)	120	Α
I _{DM}	Drain Current - Pulsed		(Note 1)	340	Α
E _{AS}	Single Pulsed Avalanche Energy (Note 2)		(Note 2)	309	mJ
dv/dt	Peak Diode Recovery dv/dt		(Note 3)	10	V/ns
D	Dower Dissination	$(T_C = 25^{\circ}C)$		160	W
P_{D}	Power Dissipation	- Derate above 25°C		1.06	W/°C
T _J , T _{STG}	Operating and Storage Temperature Range			-55 to +175	°C
T _L	-	Maximum Lead Temperature for Soldering Purpose, 1/8" from Case for 5 Seconds			°C

^{*}Calculated continuous current based on maximum allowable junction temperature. Package limitation current is 120A.

Thermal Characteristics

Symbol	Parameter	FDB088N08	Unit
$R_{\theta JC}$	Thermal Resistance, Junction to Case, Max. 0.94		
В	Thermal Resistance, Junction to Ambient (minimum pad of 2 oz copper), Max. 62.5		°C/W
$R_{\theta JA}$	Thermal Resistance, Junction to Ambient (1 in ² pad of 2 oz copper), Max.	40	

Package Marking and Ordering Information $T_C = 25^{\circ}C$ unless otherwise noted

Device Marking	Device	Package	Reel Size	Tape Width	Quantity
FDB088N08	FDB088N08	D ² -PAK	330mm	24mm	800

Electrical Characteristics

Symbol	Parameter	Test Conditions	Min.	Тур.	Max.	Unit
Off Charac	cteristics					
BV _{DSS}	Drain to Source Breakdown Voltage	$I_D = 250 \mu A$, $V_{GS} = 0 V$, $T_C = 25 ^{\circ} C$	75	-	-	V
$\frac{\Delta BV_{DSS}}{\Delta T_{J}}$	Breakdown Voltage Temperature Coefficient	I _D = 250μA, Referenced to 25°C	-	0.07	-	V/°C
ı	Zero Gate Voltage Drain Current	V _{DS} = 75V, V _{GS} = 0V	-	-	1	
IDSS	Zero Gate voltage Drain Current	$V_{DS} = 75V, T_{C} = 150^{\circ}C$	-	-	500	μA
I _{GSS}	Gate to Body Leakage Current	$V_{GS} = \pm 20V, V_{DS} = 0V$	-	-	±100	nA

On Characteristics

V _{GS(th)}	Gate Threshold Voltage	$V_{GS} = V_{DS}, I_{D} = 250 \mu A$	2.0	-	4.0	V
R _{DS(on)}	Static Drain to Source On Resistance	V _{GS} = 10V, I _D = 75A	-	7.3	8.8	mΩ
9 _{FS}	Forward Transconductance	V _{DS} = 10V, I _D = 37.5A	-	300	-	S

Dynamic Characteristics

C _{iss}	Input Capacitance	V 05V V 0V		-	4960	6595	pF
C _{oss}	Output Capacitance	$V_{DS} = 25V, V_{GS} = 0V$ f = 1MHz		-	355	470	pF
C _{rss}	Reverse Transfer Capacitance	1 - 11/11/2		-	200	300	pF
Q _{g(tot)}	Total Gate Charge at 10V			-	91	118	nC
Q_{gs}	Gate to Source Gate Charge	$V_{DS} = 60V, I_{D} = 75A$		-	22	-	nC
Q _{gd}	Gate to Drain "Miller" Charge	V _{GS} = 10V	(Note 4)	-	28	-	nC
R_G	Gate Resistance	f = 1MHz		-	-	4	Ω

Switching Characteristics

t _{d(on)}	Turn-On Delay Time		-	45	100	ns
t _r	Turn-On Rise Time	$V_{DD} = 37.5V, I_{D} = 75A$	-	158	326	ns
t _{d(off)}	Turn-Off Delay Time	$R_{GEN} = 25\Omega, V_{GS} = 10V$	-	244	498	ns
t _f	Turn-Off Fall Time	(Note 4)	-	102	214	ns

Drain-Source Diode Characteristics

I _S	Maximum Continuous Drain to Source Diode Forward Current		-	-	85	Α
I _{SM}	Maximum Pulsed Drain to Source Diode Forward Current		-	-	340	Α
V_{SD}	Drain to Source Diode Forward Voltage	V _{GS} = 0V, I _{SD} = 75A	-	-	1.25	V
t _{rr}	Reverse Recovery Time	V _{GS} = 0V, I _{SD} = 75A	-	41.1	-	ns
Q _{rr}	Reverse Recovery Charge	$dI_F/dt = 100A/\mu s$	-	80.7	-	nC

- **Notes:**1. Repetitive Rating: Pulse width limited by maximum junction temperature
- 2. L = 0.11mH, I_{AS} = 75A, V_{DD} = 50V, R_{G} = 25 Ω , Starting T_{J} = 25 $^{\circ}C$
- 3. I $_{SD}$ \leq 75A, di/dt \leq 200A/ μ s, V $_{DD}$ \leq BV $_{DSS}$, Starting T $_{J}$ = 25°C
- 4. Essentially Independent of Operating Temperature Typical Characteristics

Typical Performance Characteristics

Figure 1. On-Region Characteristics

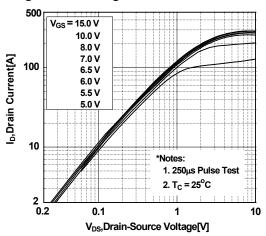


Figure 3. On-Resistance Variation vs.

Drain Current and Gate Voltage

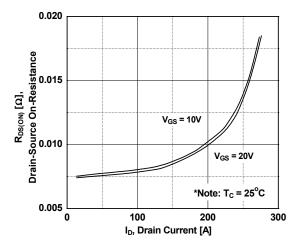


Figure 5. Capacitance Characteristics

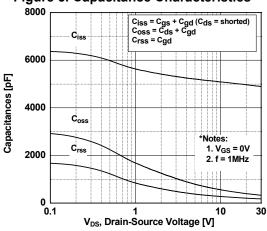


Figure 2. Transfer Characteristics

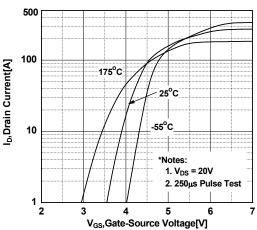


Figure 4. Body Diode Forward Voltage Variation vs. Source Current and Temperature

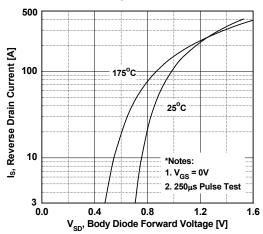
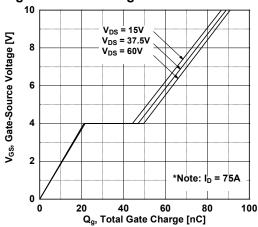


Figure 6. Gate Charge Characteristics



Typical Performance Characteristics (Continued)

Figure 7. Breakdown Voltage Variation vs. Temperature

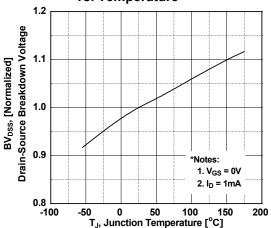


Figure 8. On-Resistance Variation vs. Temperature

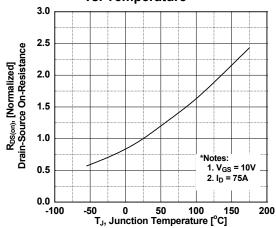


Figure 9. Maximum Safe Operating Area

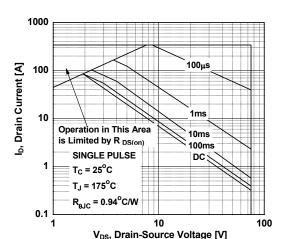


Figure 10. Maximum Drain Current vs. Case Temperature

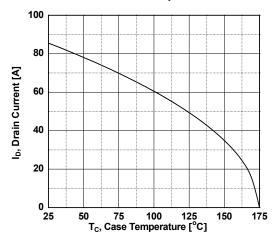
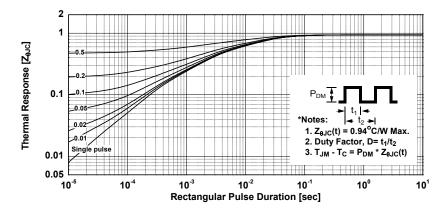
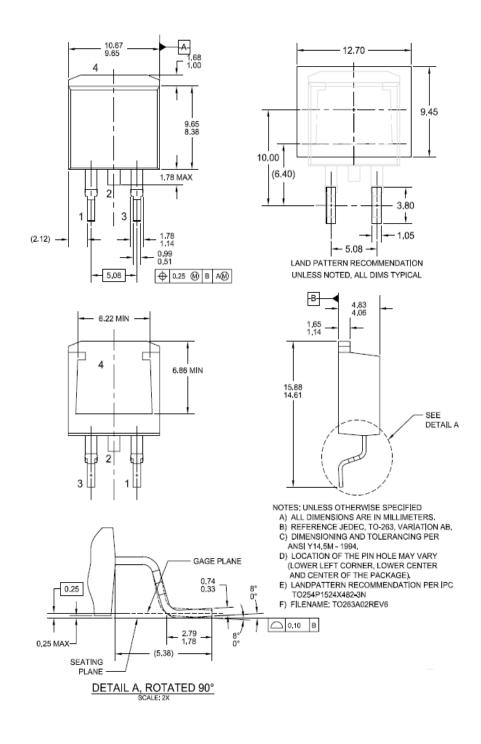


Figure 11. Transient Thermal Response Curve



Mechanical Dimensions

D²PAK



Dimensions in Millimeters Dimensions in Millimeters





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