

April 2013

FDB029N06

N-Channel PowerTrench[®] MOSFET 60 V, 193 A, 3.1 m Ω

Features

- $R_{DS(on)}$ = 2.4 m Ω (Typ.)@ V_{GS} = 10 V, I_D = 75 A
- · Fast Switching Speed
- · Low Gate Charge
- High Performance Trench Technology for Extremely Low $R_{\mbox{\scriptsize DS(on)}}$
- · High Power and Current Handling Capability
- · RoHS Compliant

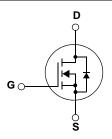
Description

This N-Channel MOSFET is produced using Fairchild Semiconductor[®]s advanced PowerTrench[®] process that has been tailored to minimize the on-state resistance while maintaining superior switching performance.

Applications

- Synchronous Rectification for ATX / Server / Telecom PSU
- · Battery Protection Circuit
- Motor drives and Uninterruptible Power Supplies
- · Renewable system





MOSFET Maximum Ratings T_C = 25°C unless otherwise noted

Symbol		Parameter		FDB029N06	Unit
V_{DSS}	Drain to Source Voltage			60	V
V_{GSS}	Gate to Source Voltage			±20	V
		-Continuous (T _C = 25°C, Silicon	Limited)	193*	
I_D	Drain Current	-Continuous (T _C = 100°C, Silicor	n Limited)	136*	Α
		-Continuous (T _C = 25°C, Packag	ge Limited)	120	
I _{DM}	Drain Current	- Pulsed	- Pulsed (Note 1)		Α
E _{AS}	Single Pulsed Avalanche Energy (Note 2)		(Note 2)	1434	mJ
dv/dt	Peak Diode Recovery dv/d	dt	(Note 3)	6	V/ns
Б	Davier Dissipation	$(T_C = 25^{\circ}C)$		231	W
P_{D}	Power Dissipation	- Derate above 25°C		1.54	W/°C
T _J , T _{STG}	Operating and Storage Temperature Range			-55 to +175	°C
TL	Maximum Lead Temperature for Soldering Purpose, 1/8" from Case for 5 Seconds			300	°C

^{*}Calculated continuous current based on maximum allowable junction temperature. Package limitation current is 120A.

Thermal Characteristics

Symbol	Parameter	FDB029N06	Unit
$R_{\theta JC}$	Thermal Resistance, Junction to Case, Max.	0.65	
Р	Thermal Resistance, Junction to Ambient (minimum pad of 2 oz copper), Max.	62.5	°C/W
$R_{\theta JA}$	Thermal Resistance, Junction to Ambient (1 in ² pad of 2 oz copper), Max.	40	

Package Marking and Ordering Information

Device Marking	Device	Package	Reel Size	Tape Width	Quantity
FDB029N06	FDB029N06	D ² -PAK	330mm	24mm	800

Electrical Characteristics $T_C = 25^{\circ}C$ unless otherwise noted

Symbol	Parameter	Test Conditions	Min.	Тур.	Max.	Unit
Off Charac	cteristics					
BV _{DSS}	Drain to Source Breakdown Voltage	$I_D = 250 \mu A, V_{GS} = 0 V, T_C = 25^{\circ} C$	60	-	-	V
$\frac{\Delta BV_{DSS}}{\Delta T_{J}}$	Breakdown Voltage Temperature Coefficient	I _D = 1mA, Referenced to 25°C	-	0.05	-	V/°C
	Zero Gate Voltage Drain Current	$V_{DS} = 48V, V_{GS} = 0V$	-	-	1	^
DSS	Zero Gate voltage Drain Current	$V_{DS} = 48V, T_{C} = 150^{\circ}C$	-	-	500	μΑ
I _{GSS}	Gate to Body Leakage Current	$V_{GS} = \pm 20V, V_{DS} = 0V$	-	-	±100	nA

On Characteristics

V _{GS(th)}	Gate Threshold Voltage	$V_{GS} = V_{DS}, I_{D} = 250 \mu A$	2.5	3.5	4.5	V
R _{DS(on)}	Static Drain to Source On Resistance	$V_{GS} = 10V, I_D = 75A$	•	2.4	3.1	mΩ
9 _{FS}	Forward Transconductance	$V_{DS} = 10V, I_{D} = 75A$	ı	154	ı	S

Dynamic Characteristics

C _{iss}	Input Capacitance	V _{DS} = 25V, V _{GS} = 0V f = 1MHz		7380	9815	pF
Coss	Output Capacitance			1095	1455	pF
C _{rss}	Reverse Transfer Capacitance	1 - 11/11/12	-	415	625	pF
Q _{g(tot)}	Total Gate Charge at 10V		-	116	151	nC
Q _{gs}	Gate to Source Gate Charge $V_{DS} = 48V, I_D = 75A$		-	40	-	nC
Q _{gd}	Gate to Drain "Miller" Charge	$V_{GS} = 10V$ (Note 4)	-	35	-	nC

Switching Characteristics

t _{d(on)}	Turn-On Delay Time		-	39	87	ns
t _r	Turn-On Rise Time	$V_{DD} = 30V, I_{D} = 75A$	-	178	366	ns
t _{d(off)}	Turn-Off Delay Time	$V_{GS} = 10V$, $R_{GEN} = 4.7\Omega$	-	54	118	ns
t _f	Turn-Off Fall Time	(Note 4)	-	33	76	ns

Drain-Source Diode Characteristics

I _S	Maximum Continuous Drain to Source Diode Forward Current			-	193	Α
I _{SM}	Maximum Pulsed Drain to Source Diode Forward Current			-	772	Α
V_{SD}	Drain to Source Diode Forward Voltage	V _{GS} = 0V, I _{SD} = 75A	-	-	1.3	V
t _{rr}	Reverse Recovery Time	V _{GS} = 0V, I _{SD} = 75A	-	46	-	ns
Q _{rr}	Reverse Recovery Charge	$dI_F/dt = 100A/\mu s$	-	50	-	nC

Notes:

^{1.} Repetitive Rating: Pulse width limited by maximum junction temperature

^{2.} L = 0.51mH, I_{AS} = 75A, V_{DD} = 50V, R_G = 25 Ω , Starting T_J = 25°C

^{3.} $I_{SD} \le 75 A, \ di/dt \le 450 A/\mu s, \ V_{DD} \le BV_{DSS}, \ Starting \ T_J = 25^{\circ}C$

^{4.} Essentially Independent of Operating Temperature Typical Characteristics

Typical Performance Characteristics

Figure 1. On-Region Characteristics

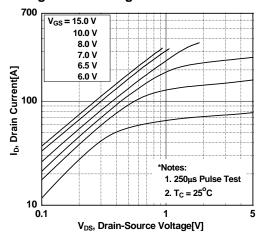


Figure 3. On-Resistance Variation vs.

Drain Current and Gate Voltage

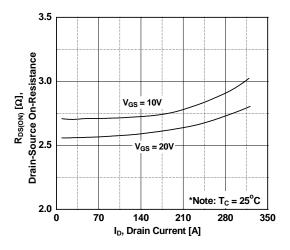


Figure 5. Capacitance Characteristics

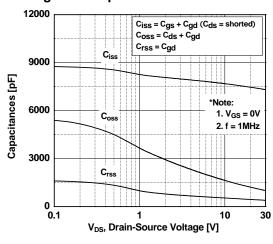


Figure 2. Transfer Characteristics

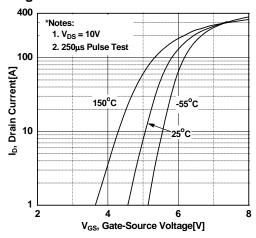


Figure 4. Body Diode Forward Voltage Variation vs. Source Current and Temperature

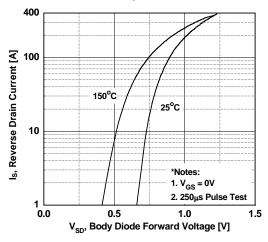
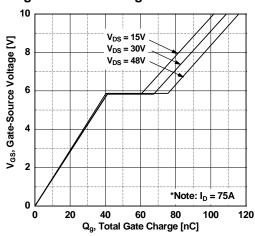


Figure 6. Gate Charge Characteristics



Typical Performance Characteristics (Continued)

Figure 7. Breakdown Voltage Variation vs. Temperature

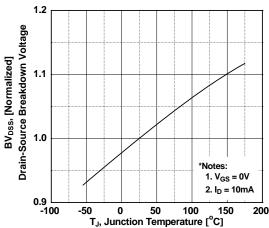


Figure 8. On-Resistance Variation vs. Temperature

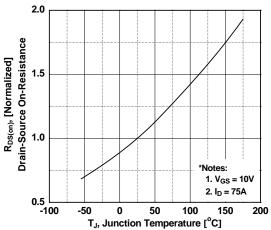


Figure 9. Maximum Safe Operating Area

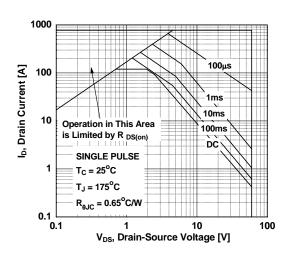


Figure 10. Maximum Drain Current vs. Case Temperature

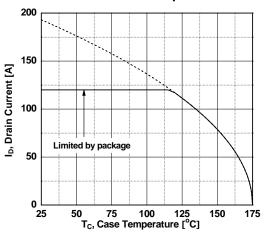
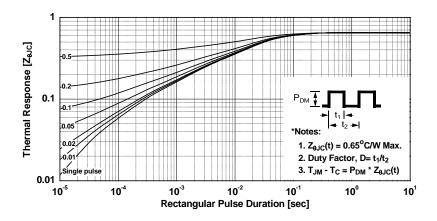
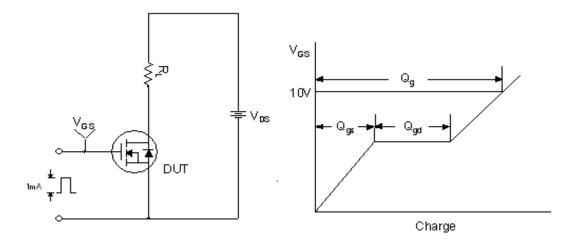


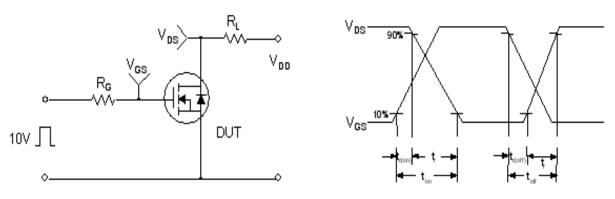
Figure 11. Transient Thermal Response Curve



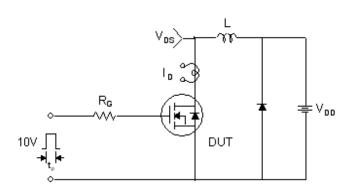
Gate Charge Test Circuit & Waveform

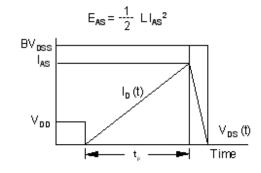


Resistive Switching Test Circuit & Waveforms

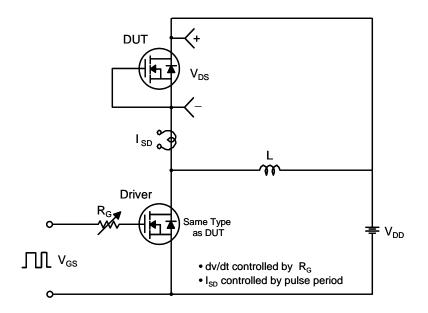


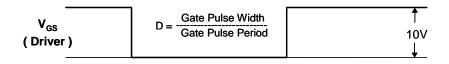
Unclamped Inductive Switching Test Circuit & Waveforms

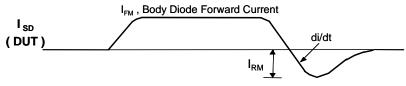




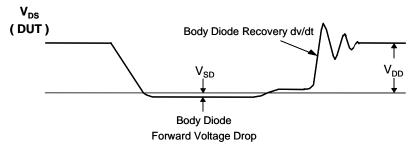
Peak Diode Recovery dv/dt Test Circuit & Waveforms





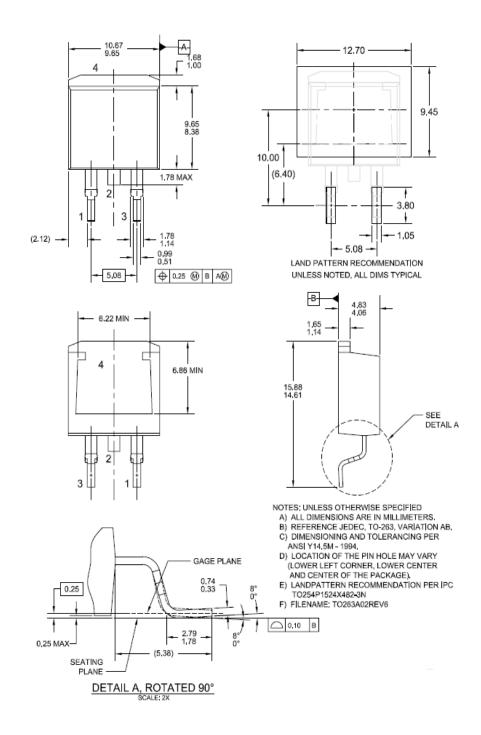


Body Diode Reverse Current



Mechanical Dimensions

D²PAK







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Rev. 164