

March 2013

FDA24N50

N-Channel UniFETTM MOSFET 500 V, 24 A, 190 m Ω

Features

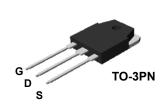
- $R_{DS(on)}$ = 160 m Ω (Typ.) @ V_{GS} = 10 V, I_D = 12 A
- Low Gate Charge (Typ. 65 nC)
- Low C_{rss} (Typ. 35 pF)
- · 100% Avalanche Tested
- · RoHS Compliant

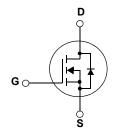
Applications

- PDP TV
- · Uninterruptible Power Supply
- · AC-DC Power Supply

Description

UniFETTM MOSFET is Fairchild Semiconductor[®]'s high voltage MOSFET family based on planar stripe and DMOS technology. This MOSFET is tailored to reduce on-state resistance, and to provide better switching performance and higher avalanche energy strength. This device family is suitable for switching power converter applications such as power factor correction (PFC), flat panel display (FPD) TV power, ATX and electronic lamp ballasts.





MOSFET Maximum Ratings T_C = 25°C unless otherwise noted

Symbol		Parameter		FDA24N50	Unit
V _{DSS}	Drain to Source Voltage			500	V
V _{GSS}	Gate to Source Voltage			±30	V
	Drain Current	- Continuous (T _C = 25°C)		24	۸
ID	Drain Current	- Continuous (T _C = 100°C)		14	A
I _{DM}	Drain Current	- Pulsed	(Note 1)	96	Α
E _{AS}	Single Pulsed Avalanche	Energy	(Note 2)	1872	mJ
I _{AR}	Avalanche Current		(Note 1)	24	Α
E _{AR}	Repetitive Avalanche Energy		(Note 1)	27	mJ
dv/dt	Peak Diode Recovery dv/o	dt	(Note 3)	4.5	V/ns
D	Davis Diagination	$(T_C = 25^{\circ}C)$		270	W
P_{D}	Power Dissipation	- Derate above 25°C		2.2	W/°C
T _J , T _{STG}	Operating and Storage Te	mperature Range		-55 to +150	°C
T _L	Maximum Lead Temperate 1/8" from Case for 5 Seco	•		300	°C

Thermal Characteristics

Symbol	Parameter	FDA24N50	Unit	
$R_{\theta JC}$	Thermal Resistance, Junction to Case, Max.	0.46		
$R_{\theta JA}$	Thermal Resistance, Junction to Ambient, Max.	40		

Package Marking and Ordering Information

Device Marking	Device	Package	Reel Size	Tape Width	Quantity
FDA24N50	FDA24N50	TO-3PN	-	-	30

Electrical Characteristics T_C = 25°C unless otherwise noted

Symbol	Parameter	Test Conditions	Min.	Тур.	Max.	Unit
Off Charac	eteristics					
BV _{DSS}	Drain to Source Breakdown Voltage	$I_D = 250 \mu A$, $V_{GS} = 0 V$, $T_J = 25 ^{\circ} C$	500	-	-	V
ΔBV _{DSS} ΔT _J	Breakdown Voltage Temperature Coefficient	I _D = 250μA, Referenced to 25°C	-	0.66	-	V/°C
1	Zoro Cata Valtaga Drain Current	V _{DS} = 500V, V _{GS} = 0V	-	-	1	
IDSS	Zero Gate Voltage Drain Current	$V_{DS} = 400V, T_C = 125^{\circ}C$	-	-	10	μА
I _{GSS}	Gate to Body Leakage Current	$V_{GS} = \pm 30V, V_{DS} = 0V$	-	-	±100	nA

On Characteristics

V _{GS(th)}	Gate Threshold Voltage	$V_{GS} = V_{DS}, I_{D} = 250 \mu A$	3.0	-	5.0	V
R _{DS(on)}	Static Drain to Source On Resistance	V _{GS} = 10V, I _D = 12A	-	0.16	0.19	Ω
9 _{FS}	Forward Transconductance	$V_{DS} = 20V, I_{D} = 12A$	ı	28	-	S

Dynamic Characteristics

C _{iss}	Input Capacitance	V 05V V 0V		-	3120	4150	pF
C _{oss}	Output Capacitance	V _{DS} = 25V, V _{GS} = 0V f = 1MHz		-	460	615	pF
C _{rss}	Reverse Transfer Capacitance	1 - 11/11/12		-	35	52	pF
Q _{g(tot)}	Total Gate Charge at 10V			-	65	85	nC
Q_{gs}	Gate to Source Gate Charge	V _{DS} = 400V, I _D = 24A		1	18	-	nC
Q _{gd}	Gate to Drain "Miller" Charge	V _{GS} = 10V	(Note 4)	-	26	-	nC

Switching Characteristics

t _{d(on)}	Turn-On Delay Time			-	47	104	ns
t _r	Turn-On Rise Time	V _{DD} = 250V, I _D = 24A		-	108	226	ns
t _{d(off)}	Turn-Off Delay Time	$R_G = 25\Omega$		-	164	338	ns
t _f	Turn-Off Fall Time		(Note 4)	-	86	182	ns

Drain-Source Diode Characteristics

Is	Maximum Continuous Drain to Source Diode Forward Current		-	-	24	Α
I _{SM}	Maximum Pulsed Drain to Source Diode Forward Current		-	-	96	Α
V_{SD}	Drain to Source Diode Forward Voltage V _{GS} = 0V, I _{SD} = 24A		-	-	1.4	V
t _{rr}	Reverse Recovery Time	V _{GS} = 0V, I _{SD} = 24A	-	540	-	ns
Q _{rr}	Reverse Recovery Charge $dl_F/dt = 100A/\mu s$		-	8.1	-	μС

Notes:

- Repetitive Rating: Pulse width limited by maximum junction temperature
- 2. L = 6.5mH, I $_{AS}$ = 24A, V $_{DD}$ = 50V, R $_{G}$ = 25 Ω , Starting T $_{J}$ = 25 $^{\circ}C$
- 3. I $_{SD} \leq$ 24A, di/dt \leq 200A/µs, V $_{DD} \leq$ BV $_{DSS}$, Starting T $_{J}$ = 25°C
- 4. Essentially Independent of Operating Temperature Typical Characteristics

Typical Performance Characteristics

Figure 1. On-Region Characteristics

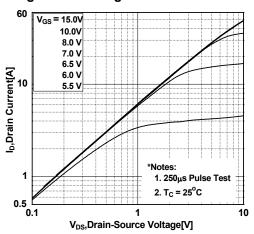


Figure 3. On-Resistance Variation vs.

Drain Current and Gate Voltage

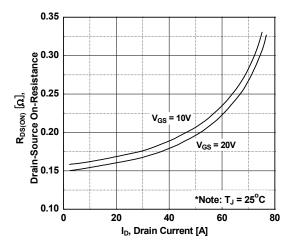


Figure 5. Capacitance Characteristics

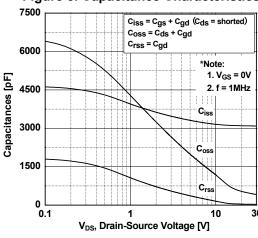


Figure 2. Transfer Characteristics

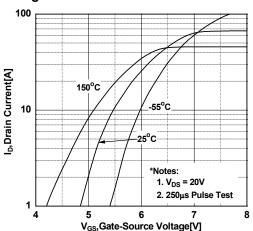


Figure 4. Body Diode Forward Voltage Variation vs. Source Current and Temperature

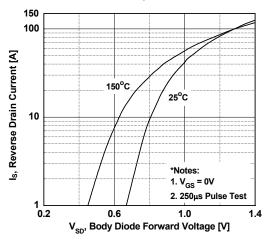
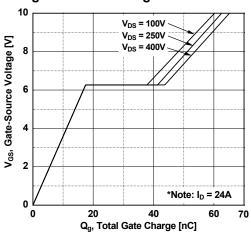


Figure 6. Gate Charge Characteristics



Typical Performance Characteristics (Continued)

Figure 7. Breakdown Voltage Variation vs. Temperature

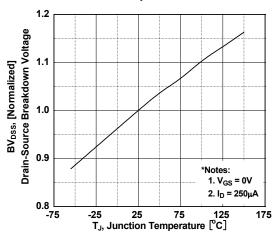


Figure 8. On-Resistance Variation vs. Temperature

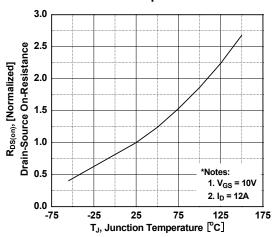


Figure 9. Maximum Safe Operating Area

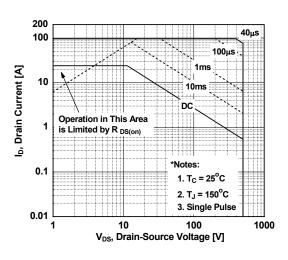


Figure 10. Maximum Drain Current vs. Case Temperature

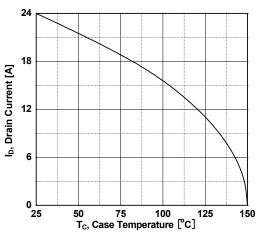
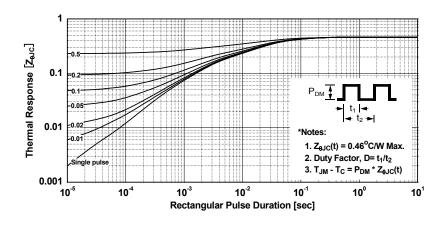
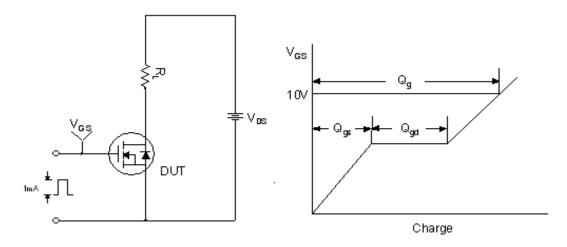


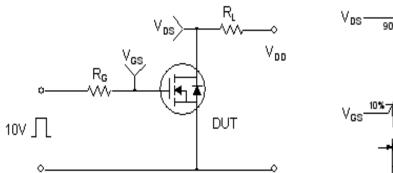
Figure 11. Transient Thermal Response Curve

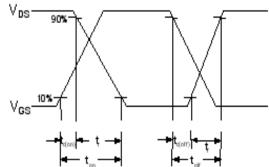


Gate Charge Test Circuit & Waveform

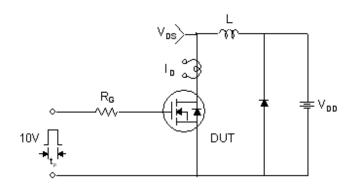


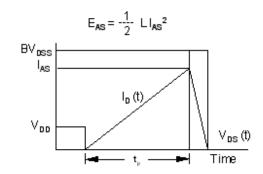
Resistive Switching Test Circuit & Waveforms



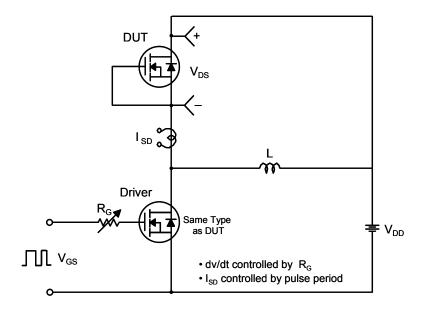


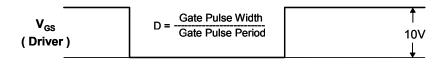
Unclamped Inductive Switching Test Circuit & Waveforms

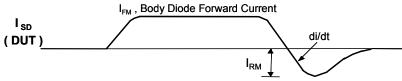




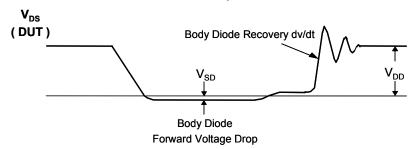
Peak Diode Recovery dv/dt Test Circuit & Waveforms





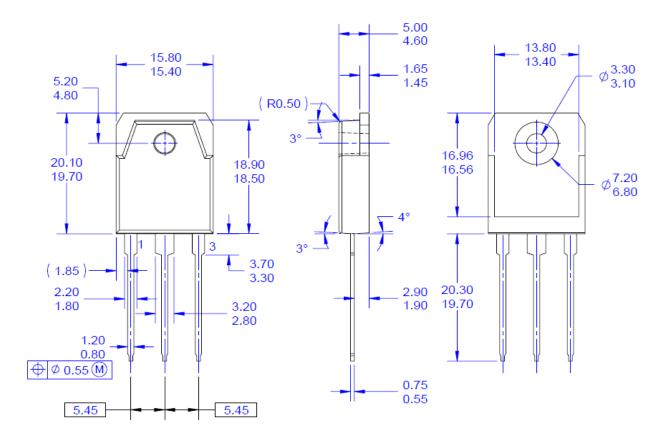


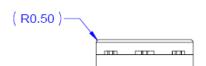
Body Diode Reverse Current



Mechanical Dimensions

TO-3PN





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